



TFT DISPLAY MODULE DATASHEET



Datasheet Release Date 2017-11-10
for

CFAF320240L-035T-CTS

Crystalfontz America, Inc.

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1. General Information

Datasheet Revision History

Datasheet Release: **2017-11-10**
Datasheet for the CFAF320240L-035T-CTS TFT graphic display module.

Product Change Notifications

You can check for or subscribe to [Part Change Notices](#) for this display module on our website.

Variations

Slight variations between lots are normal (e.g., contrast, color, or intensity).

Volatility

This display module has volatile memory.

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2. Module Description

This is a 3.5-inch diagonal full color RGB TFT graphic display module with a white LED backlight and capacitive touch screen. This display has a built-in Solomon Systech SSD1963 controller.

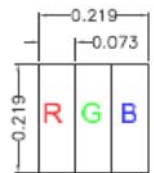
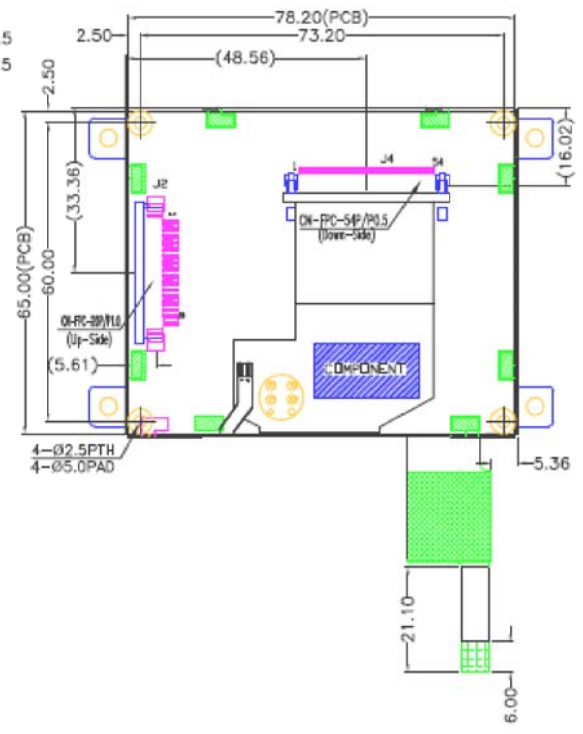
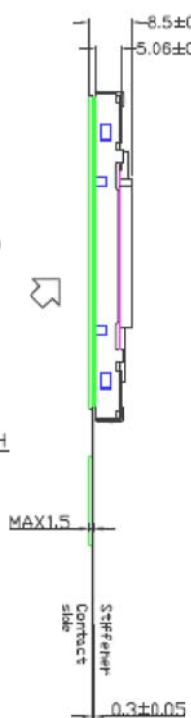
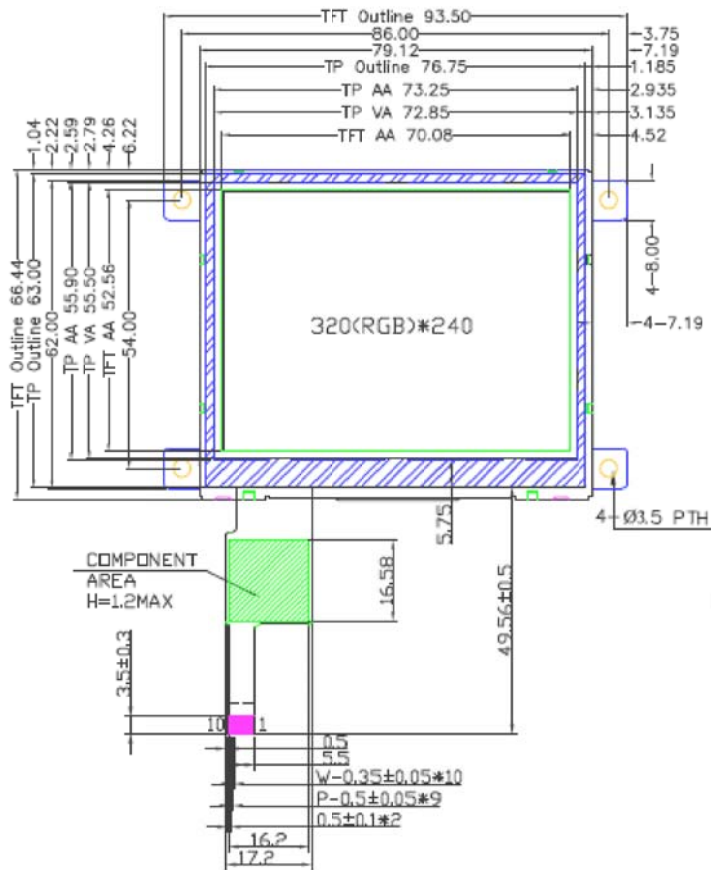
Please see [Solomon Systech SSD1963 LCD Controller Datasheet](#) for further reference.

3. Features

- 320*240 Dot Matrix
- +3.3V Power Supply
- Capacitive Touch Screen
- Display Colors: 262K
- Built-in Controller: SSD1963 (or equivalent)
- Viewing Direction: 12 o'clock
- Operating Temperature: -20°C to +70°C
- Storage Temperature: -30°C to +80°C
- Interface: 8-Bit Parallel

4. Mechanical Data

Item	Specification (mm)	Specification (inch, reference)
Overall Module Dimension	93.5 (W) x 66.44 (H) x 8.5 (D)	3.681 (W) x 2.616 (H) x 0.335 (D)
Viewing Area	73.1 (W) x 55.6 (H)	2.878 (W) x 2.189 (H)
Active Area	70.08 (W) x 52.56 (H)	2.759 (W) x 2.069 (H)
Dot Pitch	0.073 (W) x 0.219 (H)	0.003 (W) x 0.009 (H)
Weight (Typical)	63.7 grams	2.25 ounces



SCALE 1:100

Tolerance is ±0.3 mm unless specified.



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	Not to Scale	WWW.CRYSTALFONTZ.COM
DRAWING NUMBER CFAF320240L master	UNITS Millimeters	DATE 2017-10-11

6. Interface Pin Function

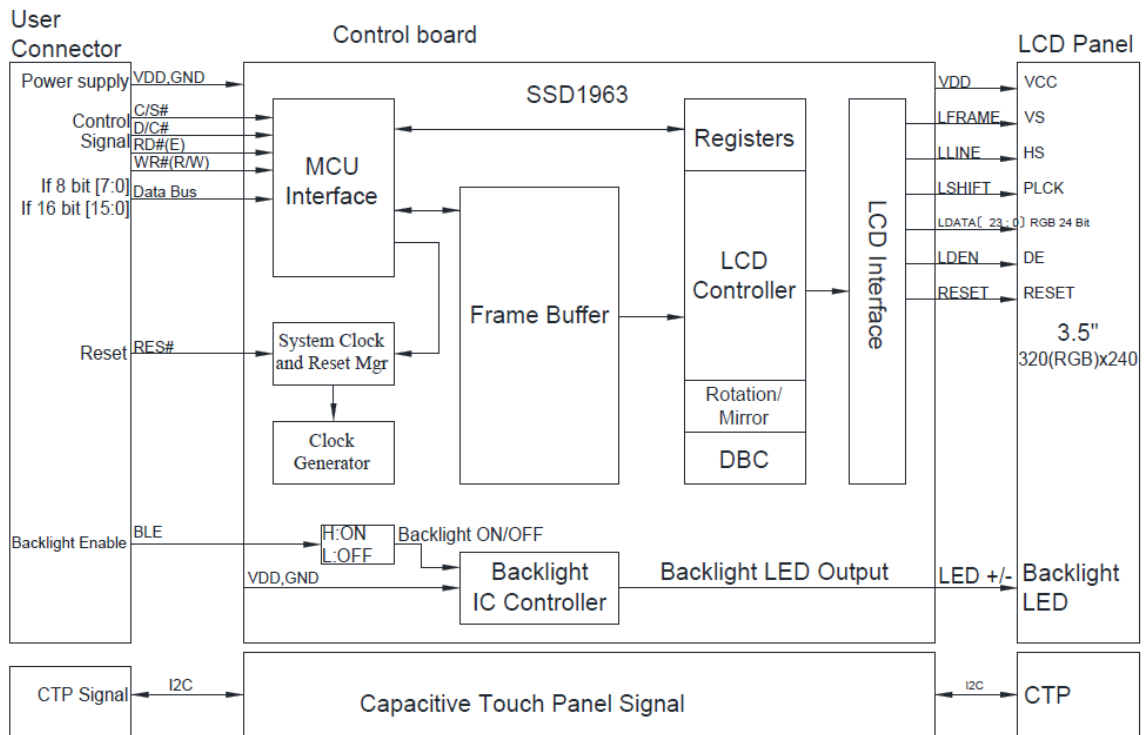
6.1. LCM Pin Definition

Pin	Symbol	Function
1	GND	System ground pin of the IC. Connect to system ground.
2	V _{DD}	Power Supply for Logic (+3.3V)
3	BL_E	Backlight Signal Control (H: ON / L: OFF)
4	D/C	Data / Command Select
5	WR	Write Strobe Signal
6	RD	Read Strobe Signal
7	DB0	Data Bus
8	DB1	Data Bus
9	DB2	Data Bus
10	DB3	Data Bus
11	DB4	Data Bus
12	DB5	Data Bus
13	DB6	Data Bus
14	DB7	Data Bus
15	CS	Chip Select Signal
16	RES	Hardware Reset
17	NC	No Connection
18	NC	No Connection
19	NC	No Connection
20	NC	No Connection

6.2. CTP Pin Definition

Pin	Symbol	Function
1	V _{SS}	System ground pin of the IC. Connect to system ground.
2	V _{DDT}	Power Supply for Logic (+3.3V)
3	SCL	SPI Slave Mode, Chip Select, Active Low / I ² C Clock Input
4	NC	No Connection
5	SDA	SPI Slave Mode, Data Input / I ² C Data Input and Output
6	NC	No Connection
7	/RST	External Reset, Low is Active
8	/WAKE	External Interrupt from the Host
9	/INT	External Interrupt to the Host
10	V _{SS}	System ground pin of the IC. Connect to system ground.

7. System Block Diagram



8. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Operating Temperature	T _{OP}	-20	+70	°C	(1)(2)(3)
Storage Temperature	T _{ST}	-30	+80	°C	(1)(2)(3)

Notes:

- (1) These are stress ratings only. Extended exposure to the absolute maximum ratings listed above may affect device reliability or cause permanent damage.
- (2) Temperature ≤ 60°C, 90% RH Maximum temperature >60°C.
- (3) Absolute humidity should be less than 90%RH at 60°C.

9. Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Logic Supply Voltage	V _{DD}	-	3.0	3.3	3.6	V
Power Supply for LCM	I _{DD}	-	-	310	470	mA
Power Consumption	-	V _{DD} =3.3V	-	1020	1690	mW
Supply Voltage for Touch Logic	V _{DDT}	-	2.8	-	3.3	V

10. Backlight Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Notes
LED Current	-	-	20	-	mA	-
Power Consumption	-	348	384	408	mW	-
LED Voltage	VBL+	17.4	19.2	20.4	V	(1)
LED Lifetime	-	-	50K	-	Hr.	(2)

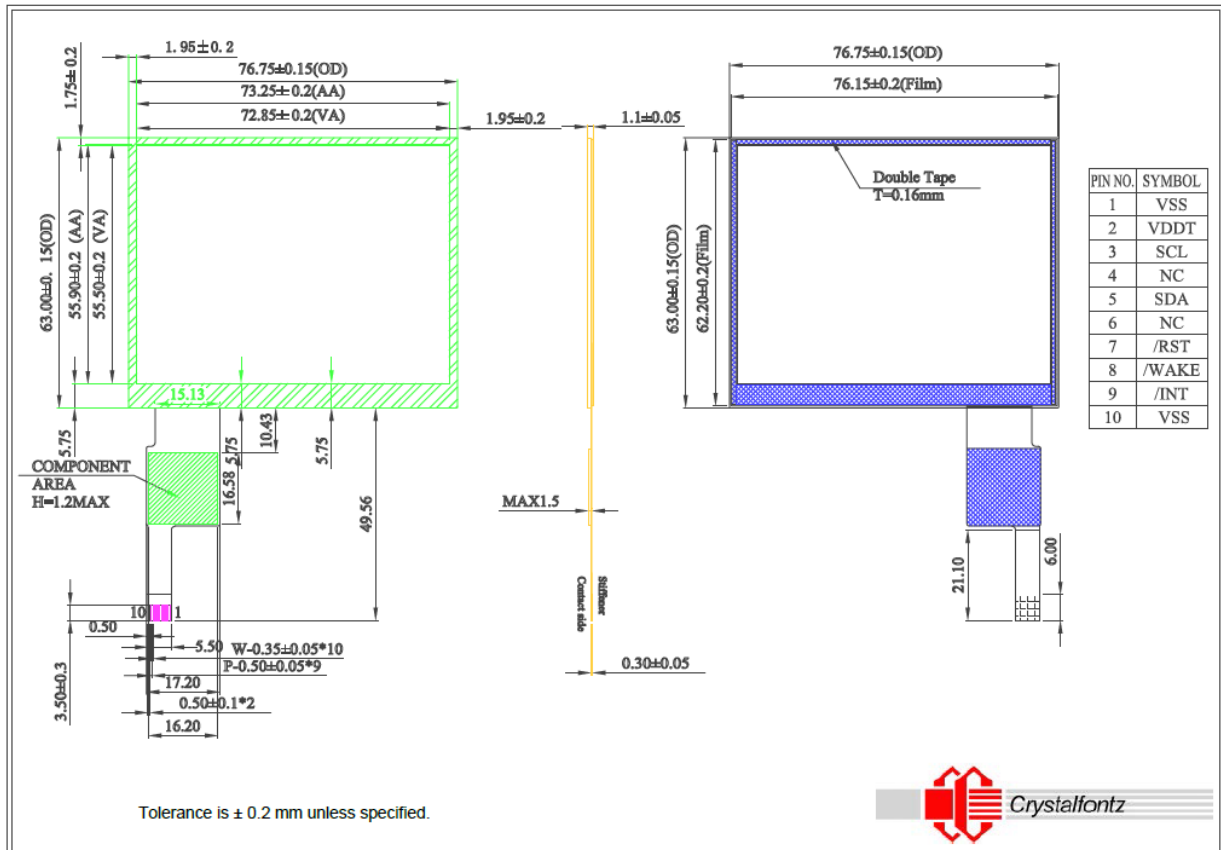
Notes:

- (1) Supply current minimum value is only for reference since the LED brightness efficiency keeps enhancing. Current consumption becomes less and less to achieve the same luminance.
- (2) Lifetime is defined as the amount of time when the luminance has decayed to <50% of the initial value (50K hours is an estimate for reference only).

11. DC Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Input High Voltage	V_{IH}	-	$0.7V_{DD}$	-	V_{DD}	V
Input Low Voltage	V_{IL}	-	0	-	$0.3V_{DD}$	V

12. Touch Panel Drawing



13. Interface Timing

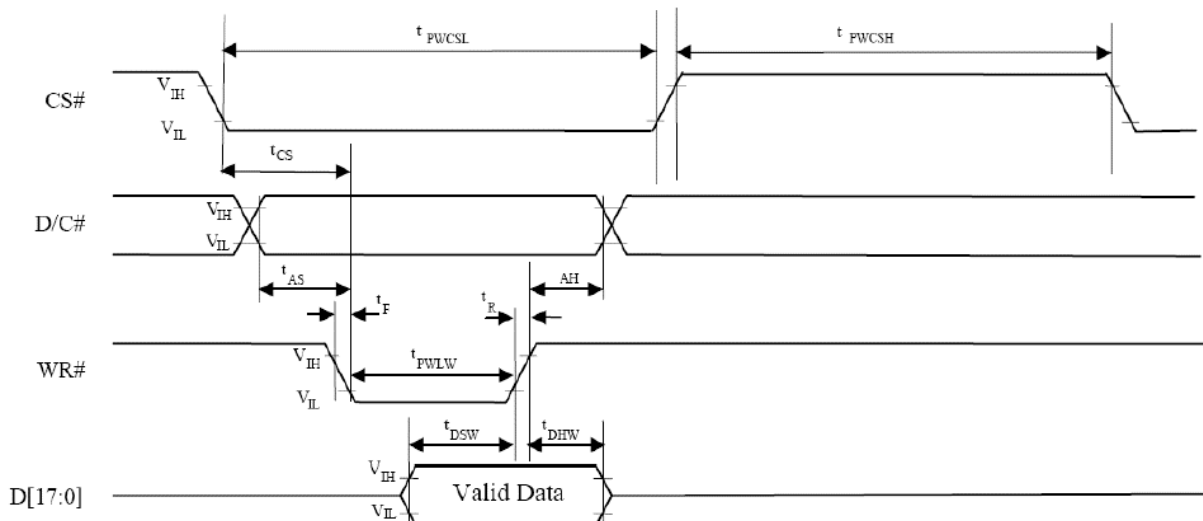
13.1. 8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data bus. This interface uses WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

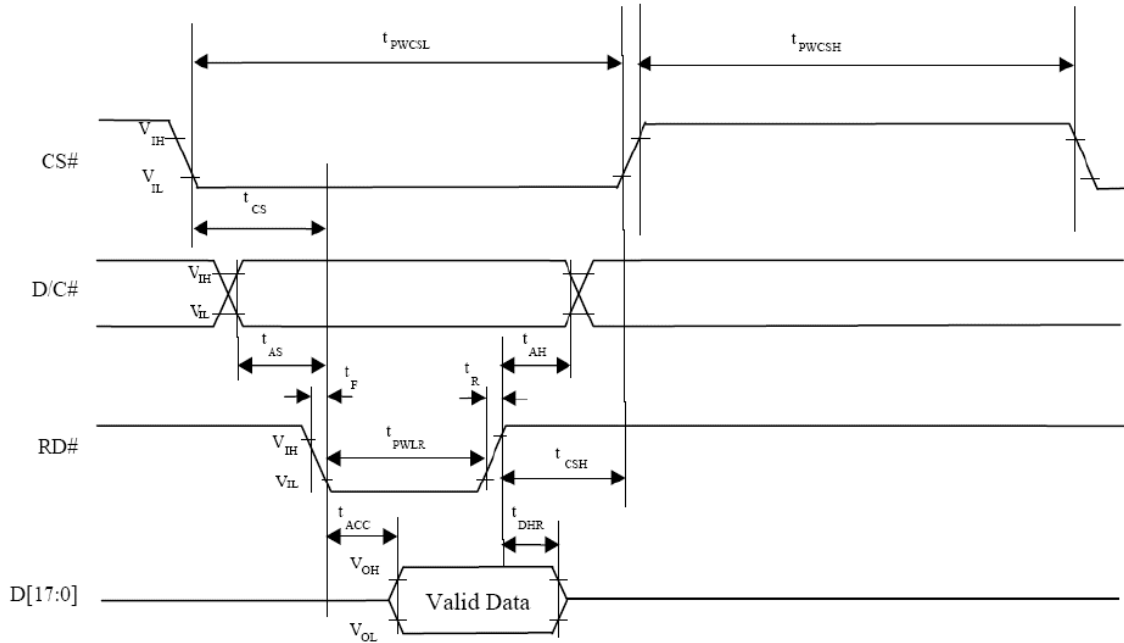
13.2. 8080 Mode Write Cycle

Parameter		Symbol	Min	Typ	Max	Unit
System Clock Frequency		fMCLK	1	-	110	MHz
System Clock Period		tMCLK	1/fMCLK	-	-	ns
Control Pulse High Width	Write	tPWCSH	13	1.5*tMCLK	-	ns
	Read		30	3.5*tMCLK		
Control Pulse Low Width	Write (next write cycle)	tPWCSL	13	1.5*tMCLK	-	ns
	Write (next read cycle)		80	9*tMCLK		
	Read		80	9*tMCLK		
Address Setup Time		tAS	1	-	-	ns
Address Hold Time		tAH	2	-	-	ns
Write Data Setup Time		tDSW	4	-	-	ns
Write Data Hold Time		tDHW	1	-	-	ns
Write Low Time		tPLW	12	-	-	ns
Read Data Hold Time		tDHR	1	-	-	ns
Access Time		tACC	32	-	-	ns
Read Low Time		tPWLR	36	-	-	ns
Rise Time		tR	-	-	0.5	ns
Fall Time		tF	-	-	0.5	ns
Chip Select Setup Time		tCS	2	-	-	ns
Chip Select Hold Time to Read Signal		tCSH	3	-	-	ns

Parallel 8080 Series Interface Timing Diagram (Write Cycle)



Parallel 8080 Series Interface Timing Diagram (Read Cycle)



13.3. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 st	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
	2 nd	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
	3 rd	B7	B6	B5	B4	B3	B2	B1	B0	R7	R6	R5	R4	R3	R2	R1	R0
8 bits	1 st									R7	R6	R5	R4	R3	R2	R1	R0
	2 nd									G7	G6	G5	G4	G3	G2	G1	G0
	3 rd									B7	B6	B5	B4	B3	B2	B1	B0

14. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Contrast Ratio (CR)	CR	at optimized viewing angle	300	350	-	-
TFT Response Time	T _r	θ=0°, Φ=0°	-	10	-	ms
	T _f		-	15	-	ms
White Chromaticity	W _x	CR ≥ 10	0.26	0.31	0.36	ms
	W _y		0.28	0.33	0.38	ms
Viewing Angle Horizontal	θ _R	CR ≥ 10	-	55	-	deg
	θ _L		-	55	-	deg
Viewing Angle Vertical	Φ _T		-	45	-	deg
	Φ _B		-	50	-	deg
Brightness	-	center of display	280	340	-	cd/m ²
Viewing Direction	12 o'clock					

15. LCD Module Precautions

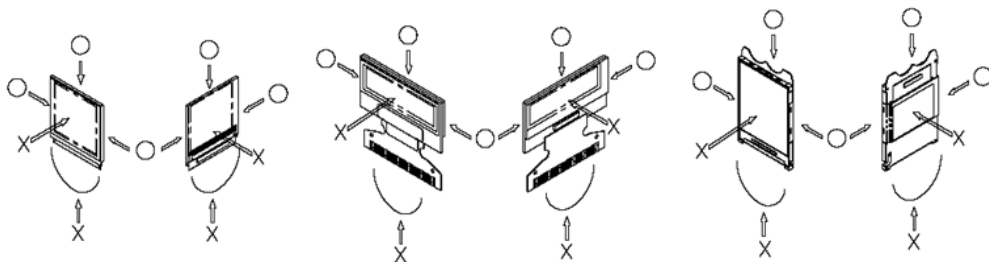
The precautions below should be followed when using LCD modules to help ensure personal safety, module performance, and compliance of environmental regulations.

15.1. Modules

- Avoid applying excessive shocks to module or making any alterations or modifications to it.
- Do not make extra holes on the printed circuit board, modify its shape or change the components of LCD display module.
- Do not disassemble the LCD display module.
- Do not operate the LCD display module above the absolute maximum rating.
- Do not drop, bend or twist the LCD display module.
- Soldering: only to the I/O terminals.
- Store in an anti-static electricity container and clean environment.
- It is common to use the "screen saver" to extend the lifetime of the LCD display module.
 - Do not use the fixed information for long periods of time in real application.
 - Do not use fixed information in LCD panel for long periods of time to extend "screen burn" effect time.
- Crystalfontz has the right to change the passive components, including R3, R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- Crystalfontz have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance, etc., under the premise of not affecting the electrical characteristics and external dimensions, Crystalfontz has the right to modify the version.)

15.2. Handling Precautions

- Since the display panel is made of glass, do not apply mechanical impacts such as dropping from a high position.
- If the display panel is accidentally broken, and the internal organic substance leaks out, be careful not to inhale or touch the organic substance.
- If pressure is applied to the display surface or its neighborhood of the LCD display module, the cell structure may be damaged, so be careful not to apply pressure to these sections.
- The polarizer covering the surface of the LCD display module is soft and can be easily scratched. Please be careful when handling the LCD display module.
- Clean the surface of the polarizer covering the LCD display module if it becomes soiled using following adhesion tape.
 - Scotch Mending Tape No. 810 or an equivalent
 - Never breathe the soiled surface or wipe the surface using a cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.
 - The following liquids/solvents may spoil the polarizer:
 - Water
 - Ketone
 - Aromatic Solvents
- Hold the LCD display module very carefully when placing the LCD display module into the system housing.
- Do not apply excessive stress or pressure to the LCD display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, be sure to secure the sufficient rigidity for the outer cases.





- Do not apply stress to the LSI chips and the surrounding molded sections.
- Do not disassemble or modify the LCD display module.
- Do not apply input signals while the logic power is off.
- Pay sufficient attention to the working environments when handing the LCD display module to prevent occurrence of element breakage accidents by static electricity.
 - Be sure to make human body grounding when handling LCD display modules.
 - Be sure to ground tools to use for assembly such as soldering irons.
 - To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
 - Protective film is being applied to the surface of the display panel of the LCD display module. Be careful since static electricity may be generated when exfoliating the protective film.
- Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the LCD display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after the film has been removed. In such a case, remove the residue material by the method discussed above.
- If electric current is applied when the LCD display module is being dewed or when it is placed under high humidity environments, the electrodes may become corroded. If this happens proceed with caution when handling the LCD display module.

15.3. Storage Precautions

- When storing the LCD display modules put them in static electricity preventive bags to avoid exposure to direct sunlight and fluorescent lamps. Also avoid high temperature and high humidity environments and low temperatures (less than 0°C) environments. (We recommend you store these modules in the packaged state when they were shipped from Crystalfontz). Be careful not to let water drops adhere to the packages or bags, and do not let dew gather on them.
- If electric current is applied when water drops are adhering to the surface of the LCD display module the LCD display module may have become dewed. If a dewed LCD display module is placed under high humidity environments it may cause the electrodes to become corroded. If this happens proceed with caution when handling the LCD display module.

15.4. Designing Precautions

- The absolute maximum ratings are the ratings that cannot be exceeded for LCD display module. If these values are exceeded, panel damage may happen.
- To prevent occurrence of malfunctioning by noise pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible.
- We recommend that you install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}). (Recommend value: 0.5A)
- Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- As for EMI, take necessary measures on the equipment side.
- When fastening the LCD display module, fasten the external plastic housing section.
- If the power supply to the LCD display module is forcibly shut down, by such errors as taking out the main battery while the LCD display panel is in operation, we cannot guarantee the quality of this LCD display module.
 - Connection (contact) to any other potential than the above may lead to rupture of the IC.

15.5. Disposing Precautions

- Request the qualified companies to handle the industrial wastes when disposing of the LCD display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

15.6. Other Precautions

- When an LCD display module is operated for a long period of time with a fixed pattern, the fixed pattern may remain as an after image or a slight contrast deviation may occur.



- If the operation is interrupted and left unused for a while, normal state can be restored.
- This will not cause a problem in the reliability of the module.
- To protect the LCD display module from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the LCD display modules.
 - Pins and electrodes
 - Pattern layouts such as the TCP & FPC
- With this LCD display module, the LCD driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this LCD driver is exposed to light, malfunctioning may occur.
 - Design the product and installation method so that the LCD driver may be shielded from light in actual usage.
 - Design the product and installation method so that the LCD driver may be shielded from light during the inspection processes.
- Although this LCD display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. Therefore, it is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
- We recommend that you construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data), to cope with catastrophic noise.
- Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier.
- Crystalfontz has the right to upgrade and modify the product function.
- The limitation of FPC bending:

