Crystalfontz America, Inc.

SPECIFICATION

CUSTOMER:

ISSUED DATE:

MODULE	NO.: CFA	CFAG240128L-STI-TZ			
SALES BY	APPROVED BY	CHECKED BY	PREPARED BY		
SALES DI	APPROVED BI	CHECKED BY	PREPARED DI		

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1.Module Classification Information

1	Brand: CRYSTALFONTZ AMERICA, INC					
2	Display Type: H→Character Type, G → Graphic Type					
3		mensions: 240 Pixels x 128 Pixels				
4	Model PCB Variant:					
(5)	Backlight Type:	N→Without backlight	T→LED, White			
		B→EL, Blue green	A→LED, Amber			
		D→EL, Green	R→LED, Red			
		W→EL, White	O→LED, Orange			
		F→CCFL, White	G→LED, Green			
		Y→LED, Yellow Green	S→LED, High Output White			
6	LCD Mode:	$B \rightarrow TN$ Positive, Gray $T \rightarrow$	FSTN Negative			
		N→TN Negative,				
		G→STN Positive, Gray				
		Y→STN Positive, Yellow G	reen			
		M→STN Negative, Blue	i de la constanta de la consta			
7	LCD Polarizer	F→FSTN Positive A→Reflective, N.T, 6:00	H→Transflective, W.T,6:00			
	Type/ Temperature	A—Kencenve, N. 1, 0.00	11—11ansnective, w.1,0.00			
	range/ View	D→Reflective, N.T, 12:00	K→Transflective, W.T,12:00			
	direction	G→Reflective, W. T, 6:00	C→Transmissive, N.T,6:00			
		J→Reflective, W. T, 12:00	F→Transmissive, N.T,12:00			
		B→Transflective, N.T,6:00	I→Transmissive, W. T, 6:00			
		E→Transflective, N.T.12:00	L→Transmissive, W.T,12:00			
8	Special Code	T→Built in negative voltage &	& Temperature Compensation ;			
		$ \mathbf{Z}\rightarrow\mathbf{IC} \ \mathbf{NT7086}:$				

2.Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7)Storage: please storage in anti-static electricity container and clean environment.

3.General Specification

Item	Dimension	Unit
Number of Characters	240 x 128	_
Module dimension	150.0 x 82.0 x 14.3(MAX)	mm
View area	114.0 x64.0	mm
Active area	107.98 x 57.58	mm
Dot size	0.43 x 0.43	mm
Dot pitch	0.45 x 0.45	mm
LCD type	FSTN, Negative, Transmissive	
Duty	1/128	
View direction	6 o'clock	
Backlight Type	LED, High light White	

4.Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	T_{OP}	-20	_	+70	°C
Storage Temperature	T_{ST}	-30	_	+80	°C
Input Voltage	$V_{\rm I}$	Vss	_	$V_{\scriptscriptstyle DD}$	V
Supply Voltage For Logic	$V_{ m DD} ext{-}V_{ m SS}$	-0.3	_	+7	V
Supply Voltage For LCD	V_{DD} - V_{0}	0	_	27	V
Negative Voltage Output	$ m V_{EE}$	_	-22V	_	V

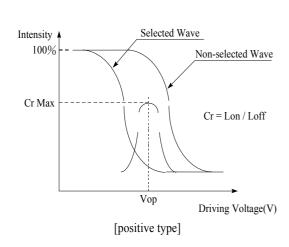
5.Electrical Characteristics

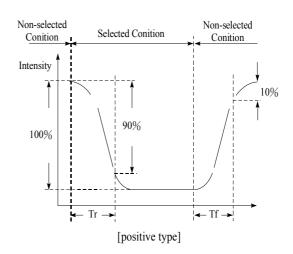
Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	$V_{ m DD}$ - $V_{ m SS}$	_	4.75	5.0	5.25	V
Supply Voltage For LCD	$ m V_{DD} ext{-}V_0$	Ta= -20°C	_	_	20.1	V
		Ta= 25°C			_	V
			_	18.9		
		Ta= -70°C			_	V
			16.3	_		
Input High Volt.	$V_{ ext{IH}}$	_	VDD-2.2	_	$V_{\scriptscriptstyle DD}$	V
Input Low Volt.	V_{IL}	_	0	_	0.8	V
Output High Volt.	$V_{ m OH}$	_	VDD-0.3	_	$V_{ m DD}$	V
Output Low Volt.	$ m V_{OL}$	_	0	_	0.3	V
Supply Current	$I_{ m DD}$	$V_{DD}=5V$	_	28.2	_	mA

6.Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	gle (V)θ		30	_	60	deg
	(Н)ф	CR≧5	-45	_	45	deg
Contrast Ratio	CR	_	_	5	_	_
Response Time T rise		_	_	200	300	ms
	T fall	_	_	200	300	ms

Definition of Operation Voltage (Vop) Definition of Response Time (Tr, Tf)





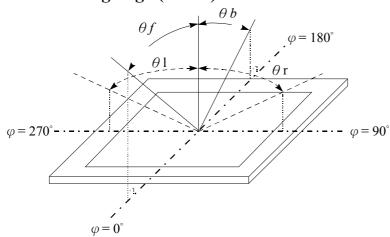
Conditions:

Operating Voltage: Vop

Viewing Angle(θ , φ): 0° , 0°

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

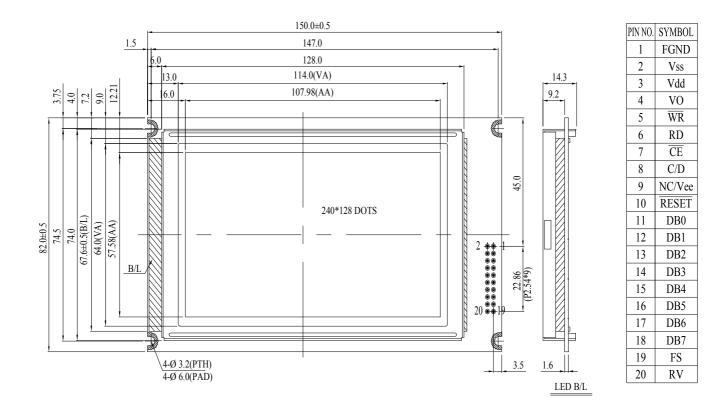
Definition of viewing angle(CR≥2)

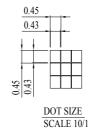


7.Interface Description

Pin No.	Symbol	Level	Description
1	FG	_	Frame ground (Connected to bezel)
2	Vss	_	GND
3	Vdd		Power supply (+5 V)
4	Vo	_	Power supply for LCD driver
5	WR	L	Data write. Write data into T6963C when WR = L
6	RD	L	Data read. Read data from T6963C when RD = L
7	CE	L	L : Chip enable
8	C/D	H/L	WR=L, C/D=H: Command Write C/D=L: Data write
			RD=L, C/D=H: Status Read C/D=L: Data read
9	Vee	_	Negative voltage
10	RESET	H/L	H: Normal; L: Initialize T6963C
11	DB0	H/L	Data bus line
12	DB1	H/L	Data bus line
13	DB2	H/L	Data bus line
14	DB3	H/L	Data bus line
15	DB4	H/L	Data bus line
16	DB5	H/L	Data bus line
17	DB6	H/L	Data bus line
18	DB7	H/L	Data bus line
19	FS	MD2	Pins for selection of font; H: 6 * 8, L: 8 * 8
20	RV	H/L	H: Reverse H: Normal

8.Contour Drawing & Block Diagram





The non-specified tolerance of dimension is ± 0.3 mm.

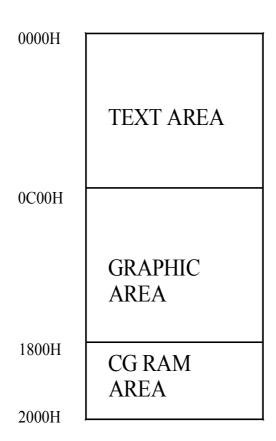
9.Display control instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

•RAM Interface

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 K byte max).

Recommend



·Flowchart of communications with MPU

(1) Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

 $\begin{array}{ccc} RD & L \\ \hline WR & H \\ \hline \overline{CE} & L \\ \hline C/D & H \\ \end{array}$

Do to D7

The T6963C status word format is as follows:

Η

MSB LSB STA2 STA6 STA5 STA4 STA3 STA1 STA0 STA7 D7 D6 D5 D4 D3 D2D1 D0

STA0	Check command execution capability	0:Disable
		1:Enable
STA1	Check data read/write Capability	0:Disable
		1:Enable
STA2	Check Auto mode data read capability	0:Disable
		1:Enable
STA3	Check Auto mode data write capability	0:Disable
		1:Enable
STA4	Not used	_
STA5	Check controller operation capability	0:Disable
		1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy	0:No error
	commands.	1:Error
STA7	Check the blink condition	0:Disable off
		1:Normal display

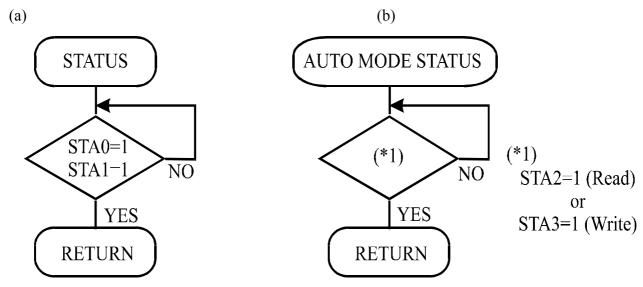
(Note 1) It is necessary to check STA0 and STA1 at the same time.

There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

Status Checking flow



(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line).

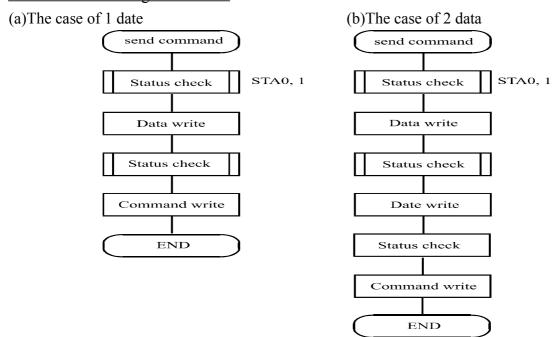
If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status.

If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data date will not be received.

(2) Setting date

When using the T6963C, first set the data, then set the command.

Procedure for sending a command



(Note) When sending more than two data, the last datum (or last two data) is valid.

· COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001	X address	Y address	Set Cursor Pointer
	00100010	Date	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
SET CONTROL WORD	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00H	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
MODE SET	1000×000	_	_	OR mode
	1000×001	_	_	EXOR mode
	1000×011	_	_	AND mode
	1000×100	_	_	Text Attribute mode
	10000×××	_	_	Internal CG ROM mode
	10001×××	_	_	External CG RAM mode
DISPLAY MODE	10010000	_	_	Display off
	1001××10	_	_	Cursor on, blink off
	1001××11	_	_	Cursor on, blink on
	100101××	_	_	Text on, graphic off
	100110××	_	_	Text off, graphic on
	100111××	_	_	Text on, graphic on
CURSOR PATTERN	10100000	_	_	1-line cursor
SELECT	10100001	_	_	2-line cursor
	10100010	_	_	3-line cursor
	10100011	_	_	4-line cursor
	10100100	_	_	5-line cursor
	10100101	_	_	6-line cursor
	10100110	_	_	7-line cursor
	10100111	_	_	8-line cursor
DATA AUTO	10110000	_	_	Set Data Auto Write
READ/WRITE	10110001	_	_	Set Data Auto Read
	10110010	_	_	Auto Reset
DATA READ/WRITE	11000000	Data	_	Data Write and Increment ADP
	11000001	_	_	Data Read and Increment ADP
	11000010	Data	_	Data Write and Decrement ADP
	11000011	_	_	Data Read and Decrement ADP Data
	11000100	Data	_	Write and Nonvariable ADP
	11000101	_	_	Data Read and Nonvariable ADP
SCREEN PEEK	11100000	_	_	Screen Peek

X : invalid

		A . Ilivalia			
COMMAND	CODE	D1	D2	FUNCTION	
SCREEN COPY	11101000	_	_	Screen Copy	
BIT SET/RESET	11110×××	_	_	Bit Reset	
	111111×××	_	_	Bit Set	
	1111× 001	_	_	Bit 0 (LSB)	
	1111× 001	_	_	Bit 1	
	1111× 010	_	_	Bit 2	
	1111× 011	_	_	Bit 3	
	1111× 100	_	_	Bit 4	
	1111× 101	_	_	Bit 5	
	1111× 110	_	_	Bit 6	
	1111× 110	_	_	Bit 7 (MSB)	

X: invalid

Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	YADRS
00100010	23H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1) Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS 00H to 4FH (lower 7 bits are valid)

Y ADRS 00H to 1FH (lower 5 bits are valid)

Single-Scan

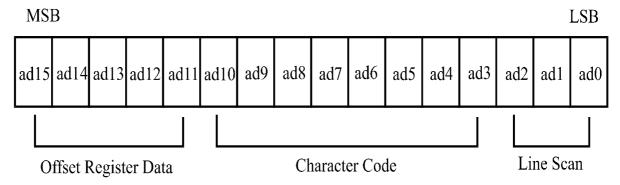
X ADRS 00 to 4FH

Y ADRS 00H to 0FH

(2) Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.



T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH represent the predefined "internal" CG ROM characters, and codes 80H to FFH represent the user's own "external" characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user's own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character's shape.

The relationship between display RAM address and offset register

Offset register data CG RAM hex. address (start to end) 00000 0000 to 07 FFH 0800 to 0FFFH 00001 00010 1000 to 17FFH 11100 E000 to E7FFH E800 to EFFFH 11101 F000 to F7FFH 11110 11111 F800 to FFFFH

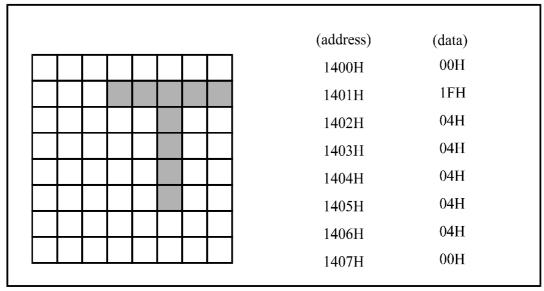
(Example 1)

Offset register 02H

Character code 80H

Character generator RAM start address 0001 0100 0000 0000

1 4 0 0 H



(Example 2) The relationship between display RAM data and display characters

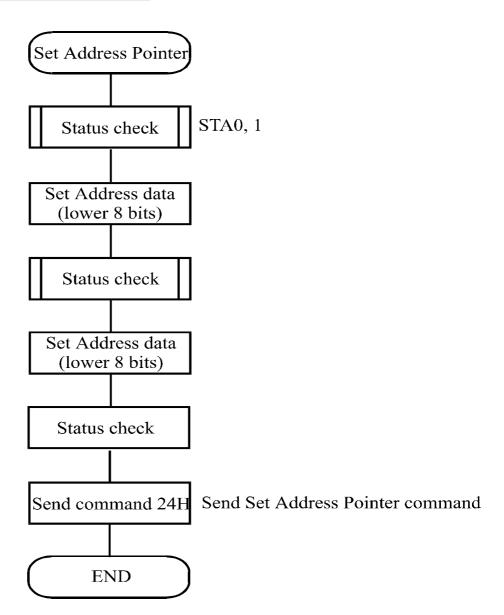
	(RAM DATA)	(Character)
AD & DE & CHILLI M	21H	A
AB γ DE ζ GHIJKLM	22Н	В
	83H	γ
	24H	D
	25H	E
	J 86Н	ζ
Display character		

 γ and ζ are displayed by character generator RAM.

(3) Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from)external RAM.

The Flowchart for Set Address Pointer command



· Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1) Set Text Home Address

The starting address in the external display RAM for text display is defined by this command. The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH	_	TH+CL
TH+TA	_	TH+TA+CL
(TH+TA)+TA	_	TH+2TA+CL
(TH+2TA)+TA	_	TH+3TA+CL
_	_	_
TH+(n-1) TA	_	TH+(n-1) TA+CL

TH: Text home address

TA: Text area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Text home address : 0000H
Text area : 0020H

: 32 Columns

: 4 Lines

0000Н	0001H	_	001EH	001FH
0020Н	0021H	_	003EH	002FH
0040H	0041H	_	005EH	005FH
0060Н	0061H		007EH	007FH

(2) Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command. The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH	_	GH+GL
GH+GA	_	GH+GA+CL
(GH+GA)+GA	_	GH+2GA+CL
(GH+2GA)+GA	_	GH+3GA+CL
_	_	_
GH+(n-1) GA	_	GH+(n-1) GA+CL

GH: Graphic home address

GA: Graphic area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address : 0000H

Graphic area : 0020H

: 32 Columns

0000Н	0001H	_	001EH	001FH
0020H	0021H	_	003EH	003FH
0040H	0041H	_	005EH	005FH
0060H	0061H	_	007EH	007FH
H0800	0081H	_	009EH	009FH
00A0H	00A1H	_	00BEH	00BFH
00C0H	00C1H	_	00DEH	00DFH
00E0H	00E1H	_	00FEH	00FFH
0100H	0101H	_	011EH	011FH
0120H	0121H	_	013EH	013FH
0140H	0141H	_	015EH	014FH
0160H	0161H	_	017EH	017FH
0180H	0181H	_	109EH	019FH
01A0H	01A1H	_	01BEH	01BFH
01C0H	01C1H	_	01DEH	01DFH
01E0H	01E1H	_	01FEH	01FFH

(3) Set Text Area

The display columns are defined by the hardware Setting. This command can be used to adjust the columns of the display.

(Example)

LCD size 20 columns, 4lines

Text home address 0000H

Text area 0014H

Set 32 columns, 4 Lines

0000	0001	 0013	0014	 001F
0014	0015	 0027	0028	 0033
0028	0029	 003B	003C	 0047
003C	003D	 004F	0050	 005B



(4) Set Graphic Area

The display columns are defined by the hardware setting. This command can be used to adjust the columns of the graphic display.

(Example)

LCD size 20 columns, 2lines

Graphic home address : 0000H

Graphic are : 0014H

0000	0001	 0013	0014	 001F
0014	0015	 0027	0028	 0033
0028	0029	 003B	003C	 0047
003C	003D	 004F	0050	 005B
0050	0051	 0063	0064	 006F
0064	0065	 0077	0078	 0083
0078	0079	 008B	008C	 0097
008C	008D	 009F	00A0	 00AB
00A0	00A1	 00B3	00B4	 00BF
00B4	00B5	 00C7	00C8	 00D3
00C8	00C9	 00DB	00DC	 00E7
00DC	00DD	 00EF	00F0	 00FD
00F0	00F1	 0103	0104	 011F
0104	0105	 0127	0128	 0123
0128	0129	 013B	0013C	 00147
013C	013D	 014F	0150	 015B

Set 32 columns, 2 Lines



If the graphic area setting is set to match the desired number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line +1.

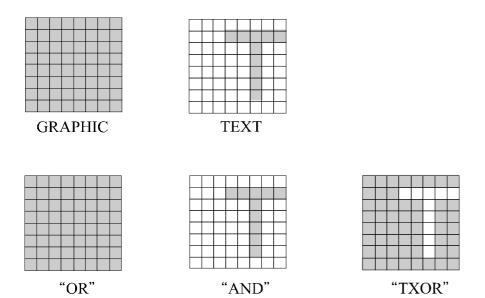
· Mode set

CODE	FUNCTION	OPERAND
1000 x 000	OR Mode	_
1000 x 001	EXOR Mode	_
1000 x 011	AND Mode	_
10 0 0 x 100	TEXT ATTRIBUTE Mode	_
10000xxx	Internal Character Generator Mode	_
10001 xxx	External Character Generator Mode	_

X: invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

(Example)



(Note) Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte	<u> </u>							
	×	×	×	×	d3	d2	d1	d0
								-

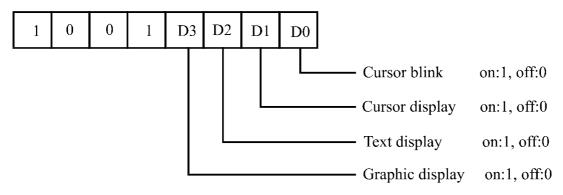
d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

X: invalid

·Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	_
1001 xx 10	Cursor on, blink off	_
1001xx11	Cursor on, blink on	_
100101 xx	Text on, graphic off	_
100110xx	Text off, graphic on	_
100111 xx	Text on, graphic on	_

X:invalid



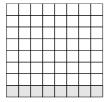
(Note) It is necessary to turn on "Text display" and "Graphic display" in the following cases.

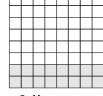
- a) Combination of text/graphic display
- b) Attribute function

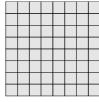
·Cursor pattern select

CODE	FUNCTION	OPERAND
10100000	1-line cursor	_
10100001	2-line cursor	_
10100010	3-line cursor	_
10100011	4-line cursor	_
10100100	5-line cursor	_
10100101	6-line cursor	_
10100110	7-line cursor	_
10100111	8-line cursor	_

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines. The cursor address is defined by the Cursor Pointer Set command.







1-line cursor

2-line cursor

8-line cursor

·Data Auto Read/Write

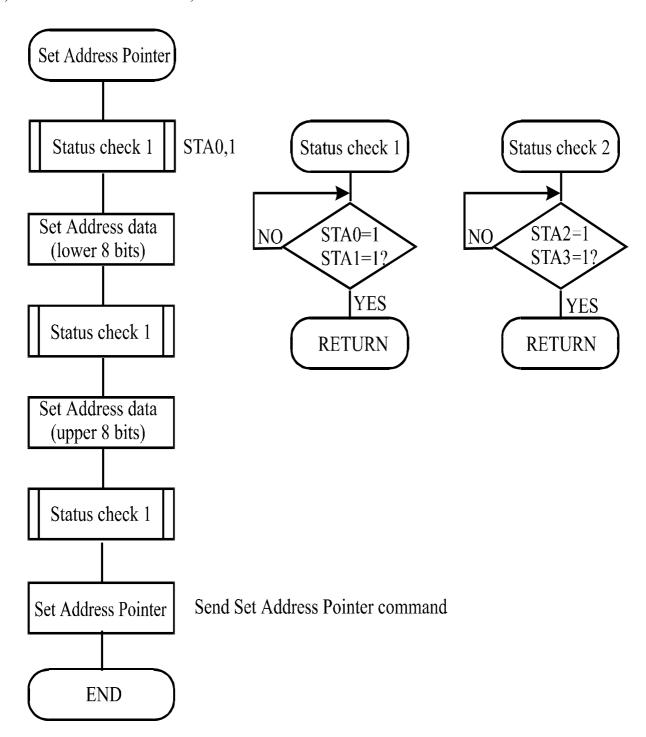
CODE	HEX.	FUNCTION	OPERAND
10110000	В0Н	Set Data Auto Write	_
10110001	B1H	Set Data Auto Read	_
10110010	В2Н	Auto Reset	_

The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

(Note) A Status check for Auto mode

(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.

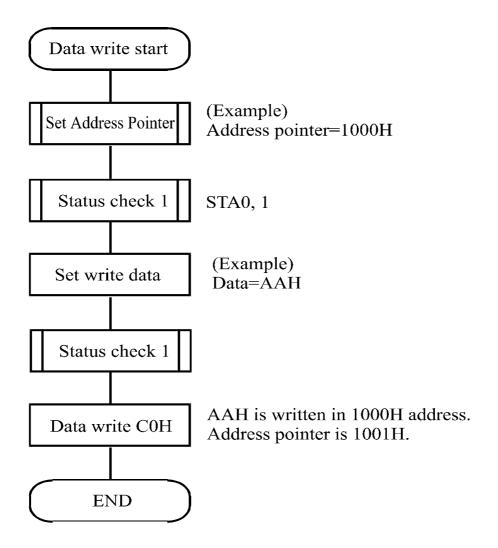


·Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	С0Н	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	_
11000010	С2Н	Data Write and Decrement ADP	Data
11000011	СЗН	Data Read and Decrement ADP	_
11000100	C4H	Data Write and Nonvariable ADP	Data
11000101	C5H	Data Read and Nonvariable ADP	_

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note) This command is necessary for each 1-byte datum.

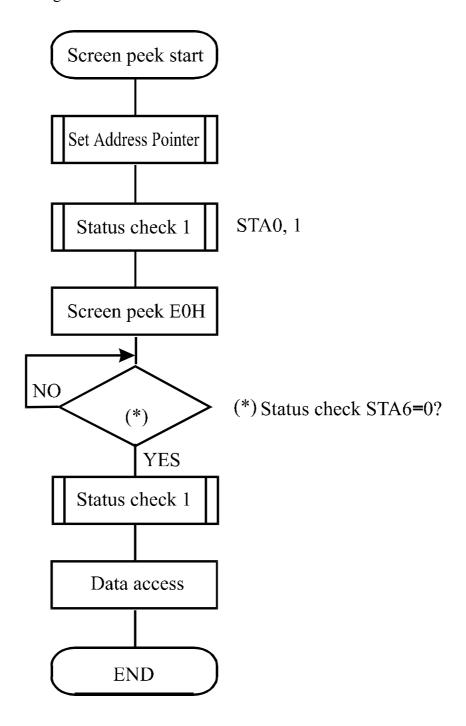


·Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	ЕОН	Screen Peek	_e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this commands is ignored and a status flag (STA6) is set.



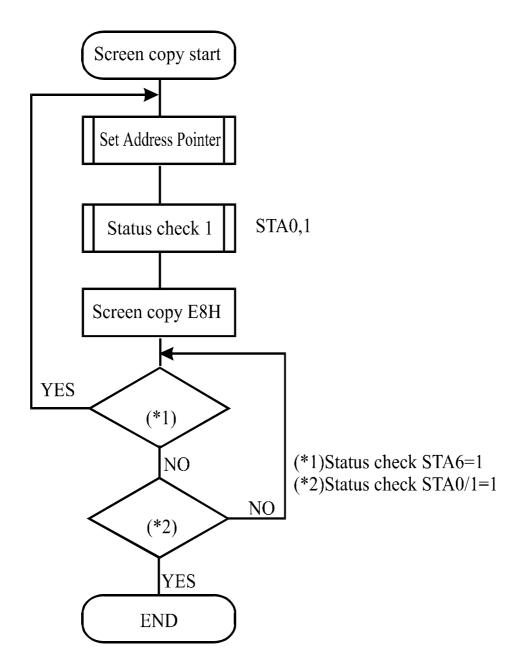
·Screen Copy

CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	

This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available. (With Attribute data is graphic area data.)

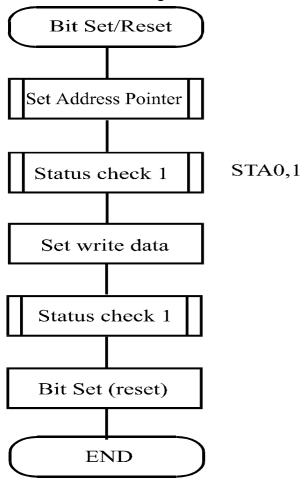


• Bit Set/Reset

CODE	FUNCTION	OPERAND
11110 xxx	Bit Reset	_
11111xxx	Bit Set	_
1111 x 000	Bit 0 (LSB)	_
1111 x 001	Bit 1	_
1111 x 010	Bit 2	_
1111 x 011	Bit 3	_
1111 x 100	Bit 4	_
1111 x 101	Bit 5	_
1111 x 110	Bit 6	_
1111 x 111	Bit 7 (MSB)	_

X: invalid

This command use to set or reset a bit of the byte specified by the address pointer. Only one bit can be set/reset at a time.



Upper							
Lowert 4					LHLH		
bit					:		
LLLH					-:::		
LLHL					#-""		
LHLL					**		
LHLH					ii		
LHHL					I.,.I		
LHHH					1.:.1		
HLLL					`;·;`		
HLLH					•:::•		
HLHL					****		
HLHH		**		! -::	-;	***	
HHLL	;:		••••			,'. !!.	
HHLH	****	****		l'i'i	**************************************	". "!.	
HHHL	**	***	"		****	.".	
НННН	"			::			

10.Timing Characteristics

Bus Timing

 $(V_{SS} = 0 V, V_{DD} = 5 V)$

Item	Symbol	Min	Тур	Max	Unit
C/D Set-up Time	t_{CDS}	100		_	ns
C/D Hold Time	t_{CDH}	10		_	ns
CE, RD, WR Pulse Width	t_{CDS}, t_{RD}, t_{WR}	80		_	ns
Data Set-up Time	$t_{ m DS}$	80		_	ns
Data Hold Time	t_{DH}	40		_	ns
Access Time	$t_{ m ACC}$	_		150	ns
Output Hold Time	t_{OH}	10		50	ns

11. Reliability

Content of Reliability Test (wide temperature, -20°C~70°C)

Environmental Test								
Test Item	Content of Test	Test Condition	Note					
High Temperature	Endurance test applying the high storage	80°C	2					
storage	temperature for a long time.	200hrs						
Low Temperature	Endurance test applying the high storage	-30°C	1,2					
storage	temperature for a long time.	200hrs						
High Temperature	Endurance test applying the electric stress	70°C						
Operation	(Voltage & Current) and the thermal stress to the element for a long time.	200hrs						
Low Temperature	Endurance test applying the electric stress under	-20°C	1					
Operation	low temperature for a long time.	200hrs						
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal	60°C,90%RH 96hrs	1,2					
	temperature.							
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C	-20°C/70°C 10 cycles						
	30min 5min 30min 1 cycle							
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3					
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5k Ω CS=100pF 1 time						

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.

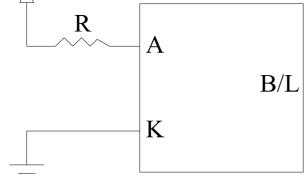
12.Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	-	128	160	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	_
Reverse Voltage	VR	_	_	5	V	_
Luminous	IV	304	380	_	CD/M ²	ILED=128mA
Intensity						
Life Time	_	_	10K	_	Hr.	ILED≦128mA
Color	High light	White	•	•		

Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).

LED B\L Drive Method 1.Drive from A, K R A



13. Material List of Components for RoHS

1. Crystalfontz America, Inc. hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

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Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs	
Limited	100	1000	1000	1000	1000	1000	
Value							
	ppm	ppm	ppm	ppm	ppm	ppm	
Above limited value is set up according to RoHS.							

2. Process for RoHS requirement:

- (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
- (2) Heat-resistance temp.

Reflow: 250°C,30 seconds Max.

Connector soldering wave or hand soldering: 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp.: 235±5°C

Recommended customer's soldering temp. of connector: 280°C, 3 seconds.