

# TFT GRAPHIC DISPLAY MODULE DATASHEET



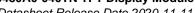
Datasheet Release Date 2020-11-11 for CFAF480480A0-040TN

Revision A0

# **Crystalfontz America, Inc.**

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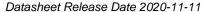




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#### CFAF480480A0-040TN TFT Display Module





### 1. General Information

#### **Datasheet Revision History**

Datasheet Release: 2020-11-11

Datasheet for the CFAF480480A0-040TN TFT graphic display module.

### **Product Change Notifications**

Check for or subscribe to Part Change Notices for this display module on our website.

#### **Variations**

Slight variations between lots are normal (e.g., contrast, color, or intensity).

#### Volatility

This display module has volatile memory.

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### 2. Module Description

This is a 16.7M color TFT graphic display module with a white LED backlight. This display has a built-in ST7701-G5 controller.

Please see the Sitronix ST7701 datasheet for further reference.

### 3. Features

480\*480 Dot MatrixViewing Direction: Free

• Built-in Controller: ST7701-G5 (or equivalent)

Operating Temperature: -20 – 70°C
 Storage Temperature: -30 – 80°C

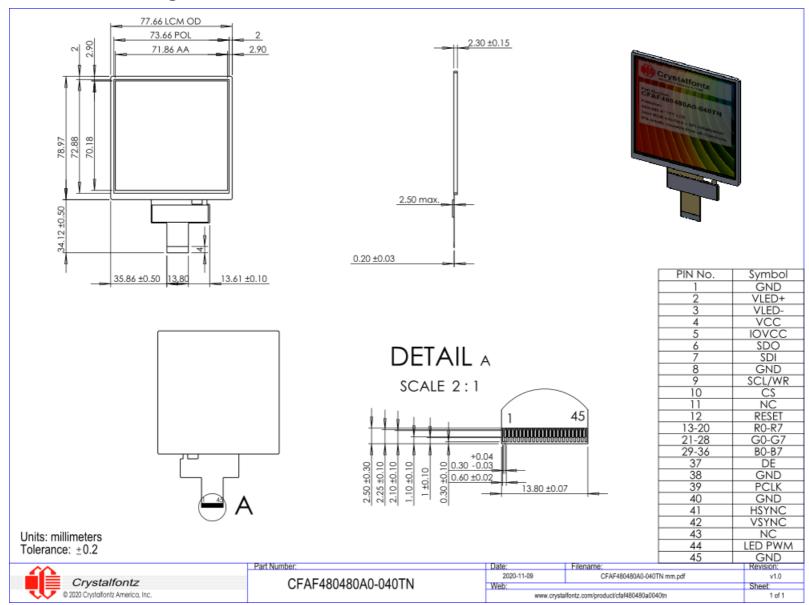
+3.3V Logic Supply
 +15V Backlight Supply
 Power consumption 0.6 W
 Interface: RGB with SPI initialization

### 4. Mechanical Data

Item	Specification (mm)	Specification (inch, reference)
Overall Dimensions	77.66 x 78.97 x 2.3	3.06 x 3.11 x 0.09
Active Area	71.86 x 70.18	2.83 x 2.76
Pixel Pitch	0.149 x 0.146	0.006 x 0.006
Weight (Typical)	29.1 grams	1.03 oz



# 5. Mechanical Drawing



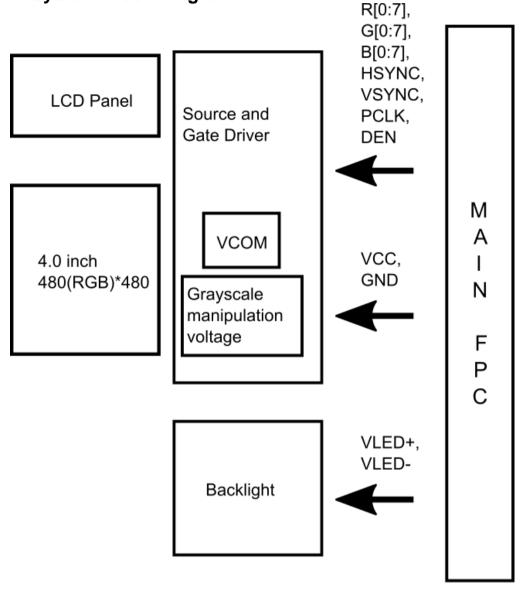


# 6. Interface Pin Function

PIN No.	Symbol	Function
1	GND	Ground
2	V <sub>LED+</sub>	Backlight LED Anode (15V)
3	V <sub>LED</sub> -	Backlight LED Cathode
4	Vcc	Power supply (3.3V)
5	IOVcc	Power supply (1.8V)
6	SDO	Serial Data Output
7	SDI	Serial Data Input
8	GND	Ground
9	SCL WR	Serial Clock Parallel Write
10	CS	Chip Select
11	NC	No Connect
12	RESET	Reset
13-20	R0-R7	Red Data bus
21-28	G0-G7	Green Data bus
29-36	B0-B7	Blue Data bus
37	DE	Data Enable. Active HIGH
38	GND	Ground
39	PCLK	Dot Clock. Input data latched on rising edge.
40	GND	Ground
41	HSYNC	Horizontal sync. Negative polarity
42	VSYNC	Vertical sync. Negative polarity.
43	NC	No Connect.
44	LED PWM	Pulse width modulation for the LED driver
45	GND	Ground



# 7. System Block Diagram



# 8. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	Vcc	-0.5	5	V
Operating Temperature	T <sub>OP</sub>	-20	70	°C
Storage Temperature	T <sub>STG</sub>	-30	80	°C

#### Notes:

- (1) These are stress ratings only. Extended exposure to the absolute maximum ratings listed above may affect device reliability or cause permanent damage.
- (2) Functional operation should be restricted to the limits in the Electrical Characteristics table below.



### 9. Electrical Characteristics

Item	Symbol	Min	Тур	Max	Unit
Supply Voltage for Logic	Vcc	3.0	3.3	3.6	V
High-level Input	ViH	0.7xVcc	-	Vcc	V
Low-level Input	V <sub>IL</sub>	GND	-	0.3xV <sub>CC</sub>	V
High-level Output	Voн	Vcc-0.4	-	Vcc	V
Low-level Output	Vol	GND	-	Vcc+0.4	V
Power Consumption (Panel+LSI)	Black Mode (60 Hz)	-	74	-	nW
	Standby	-	50	-	μW

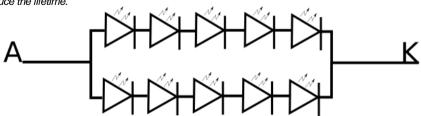
10. Optical Characteristics

o. Optical Characteristics							
Item		Symbol	Condition	Min	Тур	Max	Unit
\(':\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		(V)θ	00.40	-	160	-	deg
View Angle		(Η)φ	CR≥10	-	160	-	deg
Contrast Ratio	)	CR	Dark	500	700	-	-
Response Tim	ne	Trise+ Tfall	-	-	25	35	ms
Surface Lumir	nance	LV	θ=0°	-	350	-	cd/m <sup>2</sup>
Luminance Ur	Luminance Uniformity		θ=0°	55	60	-	%
NTSC	NTSC		θ=0°	-	50	-	%
	Б.	R		0.5784	0.6284	0.6784	
	Red	R		0.3046	0.3546	0.4046	
	•	G		0.2914	0.3414	0.3914	
	Green	G	θ=0°	0.5068	0.5568	0.6068	
Chromaticity	i	В	$\Phi = 0^{\circ}$ $T_a = 25^{\circ}$	0.0952	0.1452	0.1952	
	Blue	В	1 a — 20	0.0297	0.0797	0.1298	
	NA # 14	W		0.2511	0.3011	0.3511	
	White	W		0.2526	0.3026	0.3526	

11. Backlight Characteristics

Item	Symbol	Min	Тур	Max	Unit		
Forward Current	lF	-	40	-	mA		
Forward Voltage	V <sub>F</sub>	14.4	15	15.6	V		
Backlight Power Consumption	W <sub>BL</sub>	-	0.6	-	W		
LED Lifetime	-	-	30,000	-	Hr.		

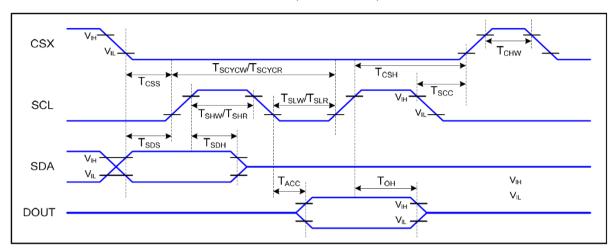
 For each LED, I=20mA, V=3.2±0.2V
 Lifetime is defined as the amount of time when the luminance has decayed to <50% of the initial value, and this</li> value is provided as an estimate only. Operating at high current, ambient temperature, or humidity conditions will reduce the lifetime.





# 12. Interface Timing

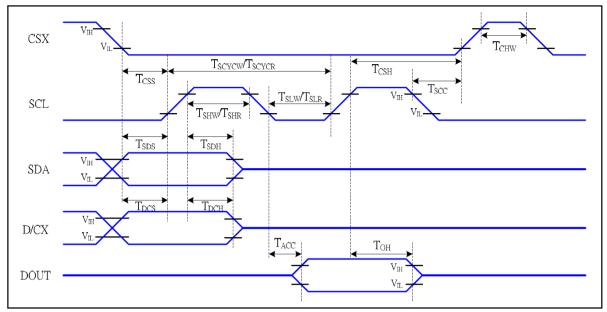
# 12.1. Serial Interface Characteristics (3-Wire SPI)



Signal	Symbol	Parameter	Min	Max	Unit
	Tcss	Chip Select Setup time (write)	15	-	ns
	Тсѕн	Chip Select Hold time (write)	15	-	ns
CSX	Tcss	Chip Select Setup time (read)	60	-	ns
	Тсѕн	Chip Select Hold time (read)	60	-	ns
	T <sub>CHW</sub>	Chip Select "H" pulse width	40	-	ns
	T <sub>SCYCW</sub>	Serial Clock Cycle (write)	66	-	ns
	T <sub>SHW</sub>	SCL "H" pulse width (write)	15	-	ns
SCL	T <sub>SLW</sub>	SCL "L" pulse width (write)	15	-	ns
SCL	Tscycr	Serial Clock Cycle (read)	150	-	ns
	T <sub>SHR</sub>	SCL "H" pulse width (read)	60	-	ns
	T <sub>SLR</sub>	SCL "L" pulse width (read)	60	•	ns
004	T <sub>SDS</sub>	Data setup time	10	-	ns
SDA	T <sub>SDH</sub>	Data hold time	10	-	ns



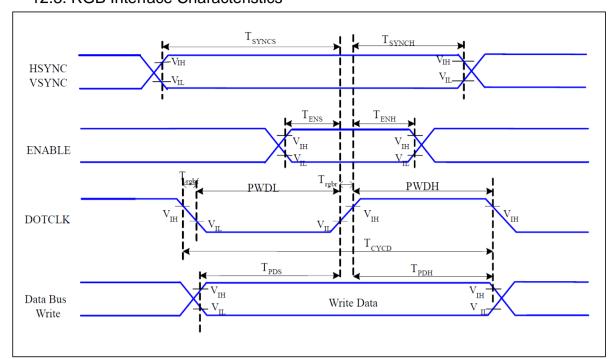
# 12.2. Serial Interface Characteristics (4-Wire SPI)



Signal	Symbol	Parameter	Min	Max	Unit
	Tcss	Chip Select Setup time (write)	15	-	ns
	Тсѕн	Chip Select Hold time (write)	15	-	ns
CSX	Tcss	Chip Select Setup time (read)	60	-	ns
	T <sub>CSH</sub>	Chip Select Hold time (read)	65	-	ns
	Тснw	Chip Select "H" pulse width	40	-	ns
	T <sub>SCYCW</sub>	Serial Clock Cycle (write)	66	-	ns
	T <sub>SHW</sub>	SCL "H" pulse width (write)	15	-	ns
001	T <sub>SLW</sub>	SCL "L" pulse width (write)	15	-	ns
SCL	TSCYCR	Serial Clock Cycle (read)	150	-	ns
	T <sub>SHR</sub>	SCL "H" pulse width (read)	60	-	ns
	T <sub>SLR</sub>	SCL "L" pulse width (read)	60	-	ns
D/C#	T <sub>DCS</sub>	D/C# setup time	10	-	ns
D/C#	T <sub>DCH</sub>	D/C# hold time	10	-	ns
CD4	T <sub>SDS</sub>	Data setup time	10	-	ns
SDA	T <sub>SDH</sub>	Data hold time	10	-	ns



### 12.3. RGB Interface Characteristics



Signal	Symbol	Parameter	Min	Max	Unit
VSYNC, HSYNC	T <sub>SYNCS</sub>	VSYNC, HSYNC Setup Time	5	-	ns
ENABLE	T <sub>ENS</sub>	Enable Setup Time	5	•	ns
ENABLE	T <sub>ENH</sub>	Enable Hold Time	5	-	ns
	PWDH	PCLK High-level Pulse Width	15	-	ns
PCLK	PWDL	PCLK Low-level Pulse Width	15	-	ns
(DOTCLK)	T <sub>CYCD</sub>	PCLK Cycle Time	33	-	ns
	T <sub>RGHR</sub> , T <sub>RGHF</sub>	PCLK Rise/Fall Time	-	15	ns
DD	T <sub>PDS</sub>	PD Data Setup Time	5	-	ns
DB	T <sub>PDH</sub>	PD Data Hold time	5	-	ns

For more timing information, see the ST7701 datasheet



### 13. LCD Module Precautions

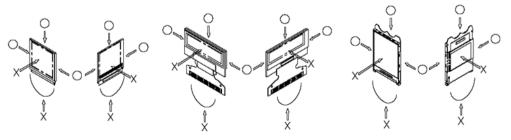
The precautions below should be followed when using LCD modules to help ensure personal safety, module performance, and compliance of environmental regulations.

#### 13.1. Modules

- Avoid applying excessive shocks to module.
- Do not make extra holes on the printed circuit board, modify its shape or change the components of LCD display module.
- Do not disassemble, alter, or modify the LCD display module.
- Do not operate the LCD display module above the absolute maximum rating.
- Do not drop, bend or twist the LCD display module.
- Solder only to the I/O terminals.
- Store in an anti-static electricity container and clean environment.
- It is common to use the "screen saver" to extend the lifetime of the LCD display module.
  - o Do not use the fixed information for long periods of time in real application.
  - Do not use fixed information in LCD panel for long periods of time to extend "screen burn" effect time.
- Crystalfontz has the right to change the passive components, including R2 and R3 adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- Crystalfontz has the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance, etc., under the premise of not affecting the electrical characteristics and external dimensions, Crystalfontz has the right to modify the version.).

### 13.2. Handling Precautions

- Since the display panel is made of glass, do not apply mechanical impacts such as dropping from a high position.
- If the display panel is accidently broken, and the internal organic substance leaks out, be careful not to inhale or touch the organic substance.
- If pressure is applied to the display surface or its neighborhood of the LCD display module, the cell structure may be damaged, so be careful not to apply pressure to these sections.
- The polarizer covering the surface of the LCD display module is soft and can be easily scratched. Please be careful when handling the LCD display module.
- Clean the surface of the polarizer covering the LCD display module if it becomes soiled using following adhesion tape.
  - o Scotch Mending Tape No. 810 or an equivalent
  - Never breathe the soiled surface or wipe the surface using a cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.
  - o The following liquids/solvents may spoil the polarizer:
    - Water
    - Ketone
    - Aromatic Solvents
- Hold the LCD display module very carefully when placing the LCD display module into the system housing.
- Do not apply excessive stress or pressure to the LCD display module. And, do not over bend
  the film with electrode pattern layouts. These stresses will influence the display performance.
  Also, be sure to secure the sufficient rigidity for the outer cases.





- Do not apply stress to the LSI chips and the surrounding molded sections.
- Do not disassemble or modify the LCD display module.
- Do not apply input signals while the logic power is off.
- Pay sufficient attention to the working environments when handing the LCD display module to prevent occurrence of element breakage accidents by static electricity.
  - o Be sure to make human body grounding when handling LCD display modules.
  - o Be sure to ground tools to use for assembly such as soldering irons.
  - To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
  - Protective film is being applied to the surface of the display panel of the LCD display module. Be careful since static electricity may be generated when exfoliating the protective film.
- Protection film is being applied to the surface of the display panel. Remove the protection film before assembling it. At this time, if the LCD display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after the film has been removed. In such a case, remove the residue material by the method discussed above.
- If electric current is applied when the LCD display module is being dewed or when it is placed under high humidity environments, the electrodes may become corroded. If this happens proceed with caution when handling the LCD display module.

### 13.3. Storage Precautions

- Store LCD modules in static electricity preventive bags. Avoid exposure to direct sunlight and fluorescent lamps. Also avoid high temperature and high humidity environments and low temperatures (less than 0°C) environments. (We recommend you store these modules in the packaged state when they were shipped from Crystalfontz). Be careful not to let water drops adhere to the packages or bags, and do not let dew gather on them.
- If electric current is applied when water drops are adhering to the surface of the LCD display
  module the LCD display module may have become dewed. If a dewed LCD display module is
  placed under high humidity environments it may cause the electrodes to become corroded. If
  this happens proceed with caution when handling the LCD display module.

#### 13.4. Designing Precautions

- The absolute maximum ratings are the ratings that cannot be exceeded for LCD display module. If these values are exceeded, panel damage may happen.
- To prevent occurrence of malfunctioning by noise pay attention to satisfy the  $V_{\mathbb{L}}$  and  $V_{\mathbb{H}}$  specifications and, at the same time, to make the signal line cable as short as possible.
- We recommend that you install excess current preventive unit (fuses, etc.) to the power circuit (V<sub>DD</sub>). (Recommend value: 0.5A)
- Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- As for EMI, take necessary measures on the equipment side.
- When fastening the LCD display module, fasten the external plastic housing section.
- If the power supply to the LCD display module is forcibly shut down, by such errors as taking
  out the main battery while the LCD display panel is in operation, we cannot guarantee the
  quality of this LCD display module.
  - o Connection (contact) to any other potential than the above may lead to rupture of the IC.

#### 13.5. Disposing Precautions

 Request the qualified companies to handle the industrial wastes when disposing of the LCD display modules. Observe all environmental and hygienic laws and regulations.

### 13.6. Other Precautions

- When an LCD display module is operated for a long period of time with a fixed pattern, the fixed pattern may remain as an after image or a slight contrast deviation may occur.
  - Restore the display by shutting the display off for an extended period.



- o This will not cause a problem in the reliability of the module.
- To protect the LCD display module from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the LCD display modules.
  - Pins and electrodes
  - o Pattern layouts such as the TCP & FPC
- With this LCD display module, the LCD driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this LCD driver is exposed to light, malfunctioning may occur.
  - Design the product and installation method so that the LCD driver may be shielded from light in actual usage.
  - Design the product and installation method so that the LCD driver may be shielded from light during the inspection processes.
- Excessive electrical noise may compromise the performance of the LCD module. Ensure system design suppresses noise generation and protect the module from noise.
- We recommend that software periodically refreshes the operation statuses (reset the commands and resend the display data), to cope with noise.
- Resistors, capacitors, and other passive components may have different appearances and colors.
- Crystalfontz has the right to upgrade and modify the product function.
- The limitation of FPC bending:

