

## CFAL12832D-B Series Pin out to interface table

Pin No	Sym	Interface		
		3 Wire SPI	4 Wire SPI	I2C
1	GND	<b>GND</b>	<b>GND</b>	<b>GND</b>
2	D2	NC	NC	SDA*
3	D1	SDA	SDA	SDA*
4	D0	SCLK	SCLK	SCL
5	D/C#	NC	D/C	I2C Address***
6	RES#	RESET	RESET	RESET
7	CS#	<b>GND</b>	<b>GND</b>	<b>GND</b>
8	BS1	<b>GND</b>	<b>GND</b>	<b>Vcc</b>
9	BS0	<b>Vcc</b>	<b>GND</b>	<b>GND</b>
10	Vdd	<b>Vcc**</b>	<b>Vcc**</b>	<b>Vcc**</b>
11	Vbat	<b>Vcc**</b>	<b>Vcc**</b>	<b>Vcc**</b>
12	GND	<b>GND</b>	<b>GND</b>	<b>GND</b>

Microcontroller	Control lines defined by layout / code
<b>+3.3v</b>	Supply voltage
<b>Ground</b>	Supply ground

Notes:			
*	Tie D2 and D1 together		
**	Tie Vdd and Vbat together		
***	D/C#	<b>Vcc</b>	<b>GND</b>
	I2C Address	0x7A	0x78

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