

TFT DISPLAY MODULE DATASHEET



Datasheet Release Date 2017-11-16 for CFAF480800FT2-040T

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1. General Information

Datasheet Revision History

Datasheet Release: 2017-11-16

Datasheet for the CFAF480800FT2-040T TFT graphic display module.

Product Change Notifications

You can check for or subscribe to Part Change Notices for this display module on our website.

Variations

Slight variations between lots are normal (e.g., contrast, color, or intensity).

Volatility

This display module has volatile memory.

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2. Module Description

This is a 4-inch diagonal full color TFT graphic display module with a white LED backlight. This display has a built-in Himax HX8363-A controller.

Please see Himax HX8363-A LCD Controller Datasheet for further reference.

3. Features

• 480*800 Dot Matrix

• Built-in Controller: HX8363-A (or equivalent)

+3V Power SupplyFull Viewing Direction

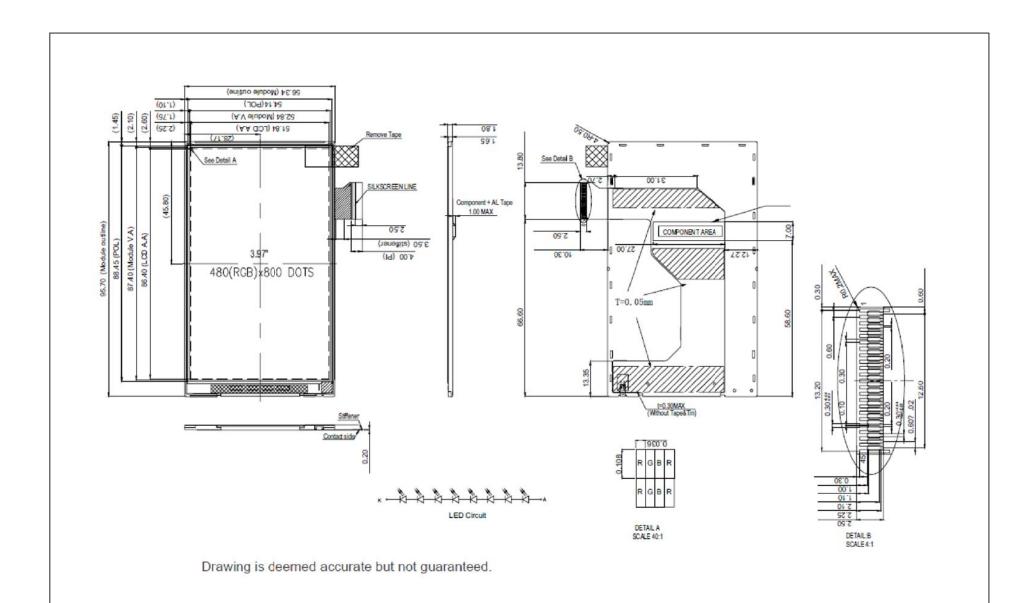
Operating Temperature: -20°C to +70°C
 Storage Temperature: -30°C to +80°C

• Direct Sunlight Readable

• Interface: DOT-CLK Interface / RGB

4. Mechanical Data

Item	Specification (mm)	Specification (inch, reference)
Overall Module Dimension	56.34 (W) x 95.70 (H) x 1.80 (D)	2.218 (W) x 3.768 (H) x 0.071 (D)
Viewing Area	52.84 (W) x 87.4 (H)	2.080 (W) x 3.441 (H)
Active Area	51.84 (W) x 86.40 (H)	20.41 (W) x 3.402 (H)
Dot Pitch	0.11 (W) x 0.11 (H)	0.004 (W) x 0.004 (H)
Weight (Typical)	26 grams	0.92 ounces





PART NUMBER(S)	SCALE		COPYRIGHT © 2017 BY CRYSTALFONTZ AMERICA, INC.
CFAF480800FT2-040T		Not to Scale	WWW.CRYSTALFONTZ.COM
DRAWING NUMBER	UNITS		DATE
master		Millimeters	2017-11-16



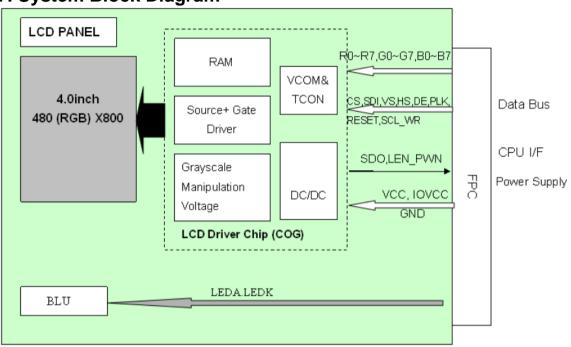
6. Interface Pin Function

Pin Symbol I/O Function 1 GND P LCM Ground 2 LEDA P Backlight Anode 3 LEDK P Backlight Cathode 4 VCC P Analog Power Supply 5 IOVCC P Digital Interface Power Supply 6 SDO O 3-Wire Serial Interface Output 7 SDI I 3-Wire Serial Interface Output 8 GND P LCM Ground 9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus<	0. 111	o. Interface Fill I direction		
2 LEDA P Backlight Anode 3 LEDK P Backlight Cathode 4 VCC P Analog Power Supply 5 IOVCC P Digital Interface Power Supply 6 SDO O 3-Wire Serial Interface Output 7 SDI I 3-Wire Serial Interface Input 8 GND P LCM Ground 9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 20 R7 I/O Data Bus	Pin	Symbol	I/O	Function
Section Sect	1	GND	Р	LCM Ground
VCC	2	LEDA	Р	Backlight Anode
5 IOVCC P Digital Interface Power Supply 6 SDO O 3-Wire Serial Interface Output 7 SDI I 3-Wire Interface Input 8 GND P LCM Ground 9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus	3	LEDK	Р	Backlight Cathode
6 SDO O 3-Wire Serial Interface Output 7 SDI I 3-Wire Interface Input 8 GND P LCM Ground 9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 24 G3 I/O Data Bus 25 G4	4	VCC	Р	Analog Power Supply
7	5	IOVCC	Р	Digital Interface Power Supply
8 GND P LCM Ground 9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IIM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O	6	SDO	0	3-Wire Serial Interface Output
9 SCL_WR I 3-Wire Serial Interface Clock Input 10 CS P Chip Select 11 IM3 (NC) O LCM Interface Select (reserved) 12 RESET O Reset Signal 13 R0 I/O Data Bus 14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 20 R7 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 30 B1 I/O	7	SDI	I	3-Wire Interface Input
10	8	GND	Р	LCM Ground
11	9	SCL_WR	I	3-Wire Serial Interface Clock Input
12	10	CS	Р	Chip Select
13	11	IM3 (NC)	0	LCM Interface Select (reserved)
14 R1 I/O Data Bus 15 R2 I/O Data Bus 16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B	12	RESET	0	Reset Signal
15	13	R0	I/O	Data Bus
16 R3 I/O Data Bus 17 R4 I/O Data Bus 18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 37	14	R1	I/O	Data Bus
17 R4 I/O Data Bus 18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37	15	R2	I/O	Data Bus
18 R5 I/O Data Bus 19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input	16	R3	I/O	Data Bus
19 R6 I/O Data Bus 20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input	17	R4	I/O	Data Bus
20 R7 I/O Data Bus 21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground	18	R5	I/O	Data Bus
21 G0 I/O Data Bus 22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 40 GND P LCM Ground	19	R6	I/O	Data Bus
22 G1 I/O Data Bus 23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground	20	R7	I/O	Data Bus
23 G2 I/O Data Bus 24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line	21	G0	I/O	Data Bus
24 G3 I/O Data Bus 25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal <td>22</td> <td>G1</td> <td>I/O</td> <td>Data Bus</td>	22	G1	I/O	Data Bus
25 G4 I/O Data Bus 26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	23	G2	I/O	Data Bus
26 G5 I/O Data Bus 27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	24	G3	I/O	Data Bus
27 G6 I/O Data Bus 28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	25	G4	I/O	Data Bus
28 G7 I/O Data Bus 29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	26	G5	I/O	Data Bus
29 B0 I/O Data Bus 30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	27	G6	I/O	Data Bus
30 B1 I/O Data Bus 31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	28	G7	I/O	Data Bus
31 B2 I/O Data Bus 32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	29	В0	I/O	Data Bus
32 B3 I/O Data Bus 33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	30	B1	I/O	Data Bus
33 B4 I/O Data Bus 34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	31	B2	I/O	Data Bus
34 B5 I/O Data Bus 35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	32	B3	I/O	Data Bus
35 B6 I/O Data Bus 36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	33	B4	I/O	Data Bus
36 B7 I/O Data Bus 37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	34	B5	I/O	Data Bus
37 DE I DPI Interface Enable Input 38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	35	B6	I/O	Data Bus
38 GND P LCM Ground 39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	36	В7	I/O	Data Bus
39 PLK I DPI Interface Dot Clock Input 40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	37	DE	I	DPI Interface Enable Input
40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	38	GND	Р	LCM Ground
40 GND P LCM Ground 41 HS I DPI Interface Line Synchronizing Signal	39	PLK	I	DPI Interface Dot Clock Input
41 HS I DPI Interface Line Synchronizing Signal	-	GND	Р	•
	41	HS	I	
	42	VS	I	DPI Interface Column Synchronizing Signal



Pin	Symbol	I/O	Function
43	IC_ID (IOVCC)	0	LCM ID Pin (Link with IOVCC in LCM)
44	LEN_PWM	0	LCM Backlight PWM Control Pin
45	GND	Р	LCM Ground

7. System Block Diagram





8. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes		
Logic Supply Voltage	IOVCC	-0.3	4.6	V	(1)(2)		
Analog Supply Voltage	Vcc	-0.3	5.5	V	(1)(2)		
Input Voltage	I/O	-0.3	IOVCC +0.5	V	-		
Backlight Forward Current	ILED	-	25	mA	-		
Operating Temperature	T _{OP}	-20	+70	°C	-		
Storage Temperature	T _{ST}	-30	+80	°C	-		
Humidity	RH	-	90	%RH	-		

Notes:

- (1) These are stress ratings only. Extended exposure to the absolute maximum ratings listed above may affect device reliability or cause permanent damage.

 (2) Functional operation should be restricted to the limits in the Electrical Characteristics table below.

9. Electrical Characteristics

Item	Symbol	Min	Тур	Max	Unit
Logic Supply Voltage	IOVCC	1.65	1.8/2.8	3.3	V
Analog Supply Voltage	VCI	2.3	2.8	4.3	V
Input Voltage "H" Level	VIH	0.7 IOVCC	-	IOVCC	V
Input Voltage "L" Level	VIL	GND	-	0.3 IOVCC	V
Output Voltage "H" Level	V _{OH}	0.8 IOVCC	-	IOVCC	V
Output Voltage "L" Level	Vol	0	-	0.2 IOVCC	V



10. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Response Time	Tr + Tf		-	50	70	ms
Contrast Ratio	CR	θ=0°	600	800	-	-
Luminance Uniformity	δ WHITE	Φ=0° T _A =25°C	75	80	-	%
Surface Luminance	Lv		320	350	-	cd/m ²
	Viewing Angle θ	Ø = 90°	-	80	-	doa
Viewing Angle		Ø = 270°	-	80	1	
viewing Angle		Ø = 0°	-	80	-	deg
		Ø = 180°	-	80	-	
Dad Chromoticity	Х		0.590	0.640	0.690	ms
Red Chromaticity	У		0.300	0.350	0.400	ms
Croon Chromoticity	Х	θ=0°	0.250	0.300	0.350	ms
Green Chromaticity	У	Φ=0°	0.540	0.590	0.640	ms
Blue Chromoticity	X	Ψ=0 T _A =25°C	0.100	0.150	0.200	ms
Blue Chromaticity	у	1A-25 C	0.010	0.060	0.110	ms
White Chromoticity	Х		0.240	0.290	0.340	ms
White Chromaticity	у		0.260	0.310	0.360	ms
NTSC Ratio	S	-	65	70	-	%

11. Backlight Characteristics

1. Dacklight Characteristics						
Item	Symbol	Min	Тур	Max	Unit	Remark
Forward Current	l _F	-	20	-	mA	1 LED
Forward Current Voltage	VF	-	3.2	-	V	1 LED
Backlight Power Consumption	W _{BL}	-	512	-	mW	8 LEDs



12. LCD Module Precautions

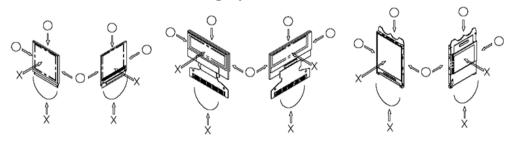
The precautions below should be followed when using LCD modules to help ensure personal safety, module performance, and compliance of environmental regulations.

12.1. Modules

- · Avoid applying excessive shocks to module or making any alterations or modifications to it.
- Do not make extra holes on the printed circuit board, modify its shape or change the components of LCD display module.
- Do not disassemble the LCD display module.
- Do not operate the LCD display module above the absolute maximum rating.
- Do not drop, bend or twist the LCD display module.
- Soldering: only to the I/O terminals.
- Store in an anti-static electricity container and clean environment.
- It is common to use the "screen saver" to extend the lifetime of the LCD display module.
 - o Do not use the fixed information for long periods of time in real application.
 - Do not use fixed information in LCD panel for long periods of time to extend "screen burn" effect time.
- Crystalfontz has the right to change the passive components, including R3, R6 & backlight
 adjust resistors. (Resistors, capacitors and other passive components will have different
 appearance and color caused by the different supplier.)
- Crystalfontz have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance, etc., under the premise of not affecting the electrical characteristics and external dimensions, Crystalfontz has the right to modify the version.).

12.2. Handling Precautions

- Since the display panel is made of glass, do not apply mechanical impacts such as dropping from a high position.
- If the display panel is accidently broken, and the internal organic substance leaks out, be careful not to inhale or touch the organic substance.
- If pressure is applied to the display surface or its neighborhood of the LCD display module, the cell structure may be damaged, so be careful not to apply pressure to these sections.
- The polarizer covering the surface of the LCD display module is soft and can be easily scratched. Please be careful when handling the LCD display module.
- Clean the surface of the polarizer covering the LCD display module if it becomes soiled using following adhesion tape.
 - o Scotch Mending Tape No. 810 or an equivalent
 - Never breathe the soiled surface or wipe the surface using a cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.
 - o The following liquids/solvents may spoil the polarizer:
 - Water
 - Ketone
 - Aromatic Solvents
- Hold the LCD display module very carefully when placing the LCD display module into the system housing.
- Do not apply excessive stress or pressure to the LCD display module. And, do not over bend
 the film with electrode pattern layouts. These stresses will influence the display performance.
 Also, be sure to secure the sufficient rigidity for the outer cases.





- Do not apply stress to the LSI chips and the surrounding molded sections.
- Do not disassemble or modify the LCD display module.
- Do not apply input signals while the logic power is off.
- Pay sufficient attention to the working environments when handing the LCD display module to prevent occurrence of element breakage accidents by static electricity.
 - o Be sure to make human body grounding when handling LCD display modules.
 - Be sure to ground tools to use for assembly such as soldering irons.
 - To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
 - Protective film is being applied to the surface of the display panel of the LCD display module. Be careful since static electricity may be generated when exfoliating the protective film.
- Protection film is being applied to the surface of the display panel and removes the protection
 film before assembling it. At this time, if the LCD display module has been stored for a long
 period of time, residue adhesive material of the protection film may remain on the surface of
 the display panel after the film has been removed. In such a case, remove the residue material
 by the method discussed above.
- If electric current is applied when the LCD display module is being dewed or when it is placed
 under high humidity environments, the electrodes may become corroded. If this happens
 proceed with caution when handling the LCD display module.

12.3. Storage Precautions

- When storing the LCD display modules put them in static electricity preventive bags to avoid
 exposure to direct sunlight and fluorescent lamps. Also avoid high temperature and high
 humidity environments and low temperatures (less than 0°C) environments. (We recommend
 you store these modules in the packaged state when they were shipped from Crystalfontz). Be
 careful not to let water drops adhere to the packages or bags, and do not let dew gather on
 them.
- If electric current is applied when water drops are adhering to the surface of the LCD display
 module the LCD display module may have become dewed. If a dewed LCD display module is
 placed under high humidity environments it may cause the electrodes to become corroded. If
 this happens proceed with caution when handling the LCD display module.

12.4. Designing Precautions

- The absolute maximum ratings are the ratings that cannot be exceeded for LCD display module. If these values are exceeded, panel damage may happen.
- To prevent occurrence of malfunctioning by noise pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible.
- We recommend that you install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}). (Recommend value: 0.5A)
- Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- As for EMI, take necessary measures on the equipment side.
- When fastening the LCD display module, fasten the external plastic housing section.
- If the power supply to the LCD display module is forcibly shut down, by such errors as taking out the main battery while the LCD display panel is in operation, we cannot guarantee the quality of this LCD display module.
 - Connection (contact) to any other potential than the above may lead to rupture of the IC

12.5. Disposing Precautions

 Request the qualified companies to handle the industrial wastes when disposing of the LCD display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

12.6. Other Precautions

- When an LCD display module is operated for a long period of time with a fixed pattern, the fixed pattern may remain as an after image or a slight contrast deviation may occur.
 - If the operation is interrupted and left unused for a while, normal state can be restored.



- o This will not cause a problem in the reliability of the module.
- To protect the LCD display module from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the LCD display modules.
 - Pins and electrodes
 - o Pattern layouts such as the TCP & FPC
- With this LCD display module, the LCD driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this LCD driver is exposed to light, malfunctioning may occur.
 - Design the product and installation method so that the LCD driver may be shielded from light in actual usage.
 - Design the product and installation method so that the LCD driver may be shielded from light during the inspection processes.
- Although this LCD display module stores the operation state data by the commands and the
 indication data, when excessive external noise, etc. enters into the module, the internal status
 may be changed. Therefore, it is necessary to take appropriate measures to suppress noise
 generation or to protect from influences of noise on the system design.
- We recommend that you construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data), to cope with catastrophic noise.
- Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier.
- Crystalfontz has the right to upgrade and modify the product function.
- The limitation of FPC bending:

