



TFT DISPLAY MODULE DATASHEET



Datasheet Release Date 2023-03-17
for
CFAF320480C7-035TR

Crystalfontz America, Inc.

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1. General Information

Datasheet Revision History

Datasheet Release: **2023-03-17**
Datasheet for the CFAF320480C7-035TR TFT graphic display module.

Product Change Notifications

You can check for or subscribe to [Part Change Notices](#) for this display module on our website.

Variations

Slight variations between lots are normal (e.g., contrast, color, or intensity).

Volatility

This display module has volatile memory.

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2. Module Description

This is a 3.5-inch diagonal full color TFT graphic display module with a 4-wire analog resistive touch screen and a white LED backlight. This display requires only a single source 3.3v for both power supply and logic. This display has a built-in Ilitek ILI9488 or compatible controller.

Please see [Ilitek ILI9488 LCD Controller Datasheet](#) for further reference.

3. Features

- 320*480 Dot Matrix
- Built-in Controller: Ilitek ILI9488 (or equivalent)
- +3V Power Supply
- Viewing Direction: 6 o'clock
- 1/16 Duty
- Operating Temperature: -20°C to +70°C
- Interface: parallel (8/9/16/18), 3 or 4-Wire SPI, DOTCLK RGB with SPI initialization

4. Mechanical Data

Item	Specification (mm)	Specification (inch, reference)
Overall Module Dimension with FPC Folded	55.50 (W) x 84.96 (H) x 3.7 (D)	2.19 (W) x 3.35 (H) x 0.15 (D)
Viewing Area	50.96 (W) x 75.44 (H)	2.01 (W) x 2.97 (H)
Active Area	48.96 (W) x 73.44 (H)	1.93 (W) x 2.89 (H)
Pixel Pitch	0.153 (W) x 0.153 (H)	0.002 (W) x 0.002 (H)
Weight (Typical)	35 grams	1.2 ounces



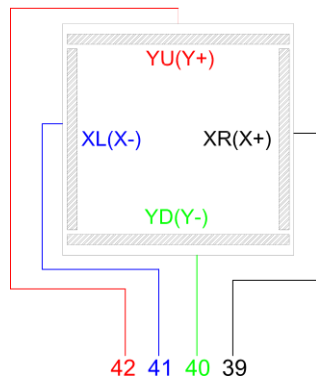
5. Interface Pin Function

Pin	Symbol	Direction	Function																																													
1	GND	P	Ground. Must be connected to an external ground.																																													
2-3	IOV _{cc}	P	Logic Supply Voltage and Input/Output Supply																																													
4-5	V _{Cl}	P	Analog Supply																																													
6	IM0	I	<table border="1"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>Interface Mode</th> <th>Pins in Use</th> </tr> </thead> <tbody> <tr> <td colspan="5" style="text-align:center">Parallel Interfaces (DBI Type-B)</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>18-Bit</td> <td>DB17-BD0</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>9-Bit</td> <td>DB8-DB0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>16-Bit</td> <td>DB15-DB0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>8-Bit</td> <td>DB7-DB0</td> </tr> <tr> <td colspan="5" style="text-align:center">3- or 4-Wire SPI (DBI Type-C)</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>3-Wire, 9-Bit SPI</td> <td>MOSI, SCL, CS</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>4-Wire, 8-Bit SPI</td> <td>MOSI, SCL, CS, D/C</td> </tr> </tbody> </table> <p>For RGB/DPI mode select a serial interface</p>	IM2	IM1	IM0	Interface Mode	Pins in Use	Parallel Interfaces (DBI Type-B)					0	0	0	18-Bit	DB17-BD0	0	0	1	9-Bit	DB8-DB0	0	1	0	16-Bit	DB15-DB0	0	1	1	8-Bit	DB7-DB0	3- or 4-Wire SPI (DBI Type-C)					1	0	1	3-Wire, 9-Bit SPI	MOSI, SCL, CS	1	1	1	4-Wire, 8-Bit SPI	MOSI, SCL, CS, D/C
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7	IM1																																															
8	IM2																																															
9	RST	I	Reset Signal Pin, must be applied to initialize chip. Keep high during normal operation.																																													
10	V _{SYNC}	I	Vertical and Horizontal Frame Synchronizing Signal used for RGB/DPI Mode. When not used, pull high or low.																																													
11	H _{SYNC}																																															
12	P _{CLK}	I	Pixel Clock Signal for RGB/DPI Mode. When not used, pull high or low.																																													
13	DE	I	Data Enable Signal for RGB/DPI Mode. When not used, pull high or low.																																													
14-31	DB17-DB0	I/O	<table border="1"> <thead> <tr> <th colspan="2">Parallel Interfaces (DBI Type-B)</th> </tr> </thead> <tbody> <tr> <td>18-Bit</td> <td>DB17-BD0</td> </tr> <tr> <td>9-Bit</td> <td>DB8-DB0</td> </tr> <tr> <td>16-Bit</td> <td>DB15-DB0</td> </tr> <tr> <td>8-Bit</td> <td>DB7-DB0</td> </tr> </tbody> </table> <p>If not used, tie low.</p>	Parallel Interfaces (DBI Type-B)		18-Bit	DB17-BD0	9-Bit	DB8-DB0	16-Bit	DB15-DB0	8-Bit	DB7-DB0																																			
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32	GND	P	Ground																																													
33	SDO	O	SPI Data output (SDO, DOUT). If not used, no connection.																																													
34	SDA	I	SPI Data input pin (SDI, SDA, DIN_SDA). Data is applied on the rising edge of SCL. If not used, tie high or low.																																													
35	RD	I	Parallel: Read signal and read data at the low level. If not used, tie high or low																																													
36	WR SCL	I	Parallel: Write signal and write data at the low level. SPI: Serial Clock (SCL), data applied on rising edge.																																													
37	D/C	-	Data/Command Control. Low command, high data.																																													
38	CS	I	Chip Select Pin, active low																																													
39-42	See Function Column	-	XR (X+): Right YD (Y-): Down XL (X-): Left YU (Y+): Up																																													
43	LEDA (LED+)	P	Backlight anode																																													
44-49	LEDK ₁ - LEDK ₆ (LED-)	P	Backlight cathodes																																													
50	GND	P	Ground																																													



6. Touch Screen Connections (4-Wire Analog)

Touch Screen Circuit



NOTE: The touch screen must be driven in one direction, then read in the other direction.

The circuitry is industry standard CMOS logic and is susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other static sensitive devices such as expansion cards, motherboards, or integrated circuits. Ground your body, work surfaces, and equipment.

7. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Digital Logic Supply and Input/Output Supply	IOV _{CC}	-0.3	+3.3	V
Analog Supply	V _{CI}	-0.3	+3.3	V
Operating Temperature	T _{OP}	-20	+70	°C
Storage Temperature	T _{ST}	-30	+80	°C

Note: These are stress ratings. Extended exposure to the absolute maximum ratings listed above may affect device reliability or cause permanent damage. Functional operation should be restricted to the limits in the Electrical Characteristics table below.

8. Electrical Characteristics

Item	Symbol	Min	Typical	Max	Unit
Digital Logic Supply and Input/Output Supply	IOV _{CC}	1.65	1.8	3.3	V
Analog Supply	V _{CI}	2.5	2.8	3.3	V
Current Consumption for Normal Operation	I _{CC}	-	8	16	mA
High-level Input	V _{IH}	0.7 x IOV _{CC}	-	IOV _{CC}	V
Low-level Input	V _{IL}	0v (GND)	-	0.3 x IOV _{CC}	V
High-level Output	V _{OH}	0.8 x IOV _{CC}	-	IOV _{CC}	V
Low-level Output	V _{OL}	0v (GND)	-	0.2 x IOV _{CC}	V



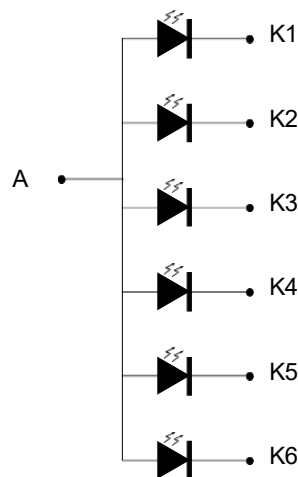
9. Optical Characteristics

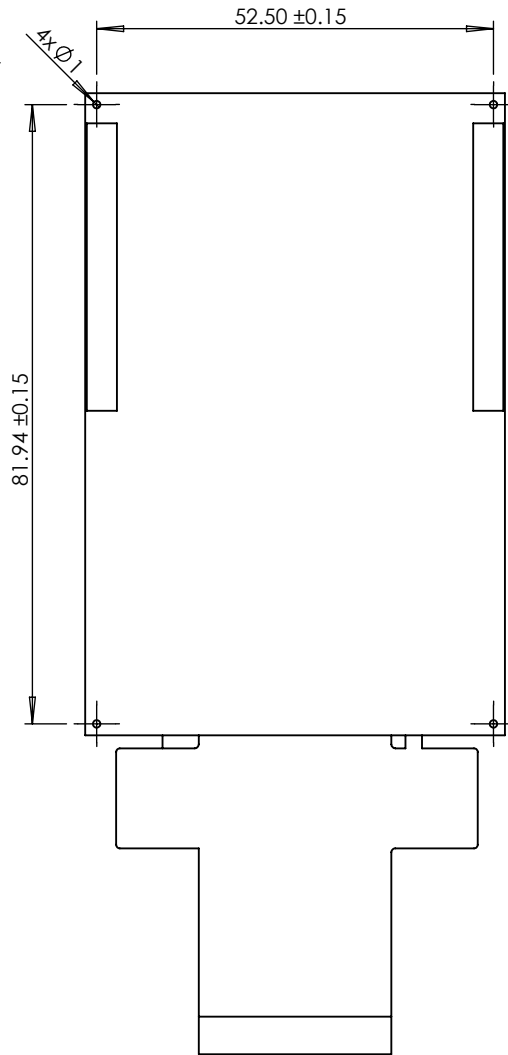
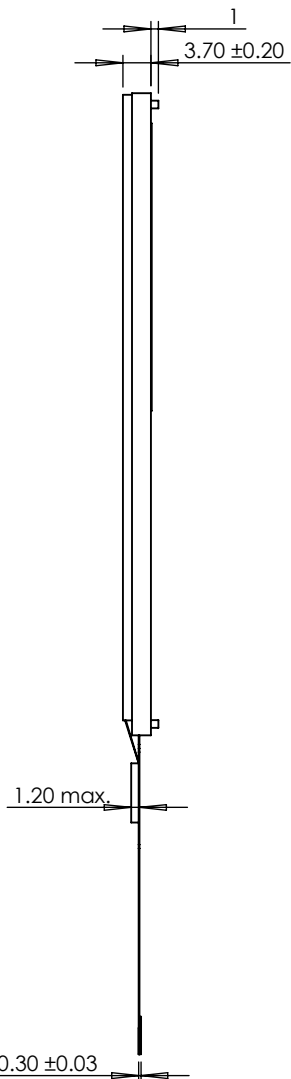
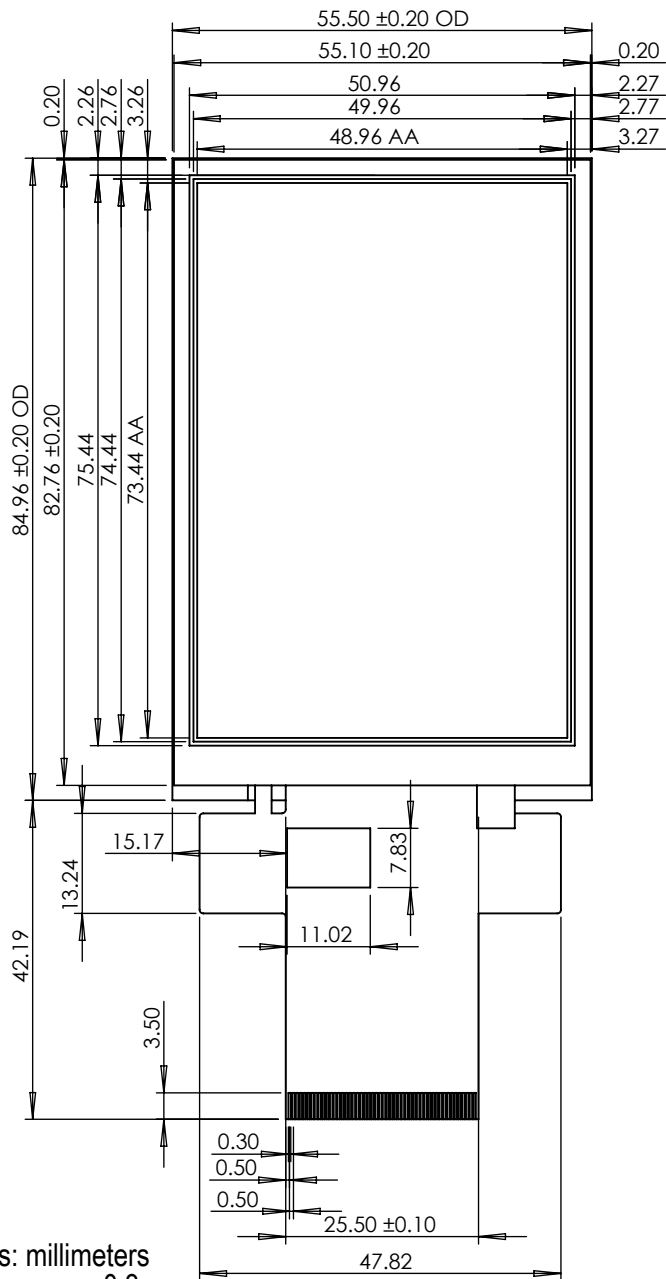
Item	Symbol	Condition	Min	Typical	Max	Unit	
Color Depth	-	-	-	262k	-	colors	
Contrast Ratio	CR	-	-	500	-	-	
TFT Response Time	$T_{rise}+T_{fall}$	-	-	20	40	ms	
Red Chromaticity	x	-	0.609	0.624	0.639		
	y		0.316	0.331	0.346		
Green Chromaticity	x	-	0.281	0.296	0.311		
	y		0.562	0.577	0.591		
Blue Chromaticity	x	-	0.128	0.143	0.158		
	y		0.094	0.109	0.124		
White Chromaticity	x	-	0.292	0.307	0.322		
	y		0.312	0.327	0.342		
Viewing Angle, Horizontal	θ_{X+}	Center CR \geq 10	-	70	-	°	
	θ_{X-}			70			
Viewing Angle, Vertical	θ_{Y+}		-	-	60	-	°
	θ_{Y-}				60		
Viewing Direction	6 o'clock						

10. Backlight Characteristics

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Supply Current	I_{LED}	-	120	-	mA
Supply Voltage	V_{LED}	2.8	3.2	3.4	V
Luminous Intensity I_{LED} – Typical	I_v	200	-	-	cd/m ²
Uniformity	-	80	-	-	%

Note: Supply current minimum value is only for reference since LED brightness efficiency keeps enhancing. Current consumption becomes less and less to achieve the same luminance.





Units: millimeters
Tolerance: ±0.3

Pin	Symbol
1	GND
2-3	IOVCC
4-5	VCI
6	IM0
7	IM1
8	IM2
9	RST
10	VSYNC
11	HSYNC
12	PCLK
13	DE
14-31	DB17-DB0
32	GND
33	SDO
34	SDA
35	RD
36	WR/SCL
37	D/C
38	CS
39-42	Touch Panel
43	LEDA (LED+)
44-49	LEDK 1-6
50	GND

Display Controller	ILI9488
Brightness	200 nits
Viewing Direction	6:00
Operating Temperature	-20 to 70 °C
Voltage Levels	3.3v



12. LCD Module Precautions

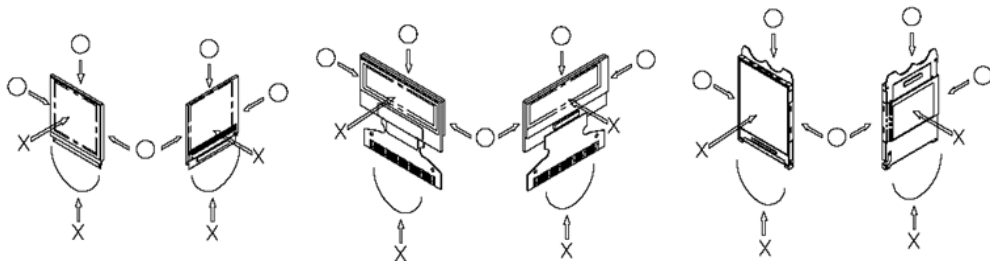
The precautions below should be followed when using LCD modules to help ensure personal safety, module performance, and compliance of environmental regulations.

12.1. Modules

- Avoid applying excessive shocks to module or making any alterations or modifications to it.
- Do not make extra holes on the printed circuit board, modify its shape or change the components of LCD display module.
- Do not disassemble the LCD display module.
- Do not operate the LCD display module above the absolute maximum rating.
- Do not drop, bend or twist the LCD display module.
- Soldering: only to the I/O terminals.
- Store in an anti-static electricity container and clean environment.
- It is common to use the "screen saver" to extend the lifetime of the LCD display module.
 - Do not use the fixed information for long periods of time in real application.
 - Do not use fixed information in LCD panel for long periods of time to extend "screen burn" effect time.
- Crystalfontz has the right to change the passive components, including R3, R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- Crystalfontz have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance, etc., under the premise of not affecting the electrical characteristics and external dimensions, Crystalfontz has the right to modify the version.)

12.2. Handling Precautions

- Since the display panel is made of glass, do not apply mechanical impacts such as dropping from a high position.
- If the display panel is accidentally broken, and the internal organic substance leaks out, be careful not to inhale or touch the organic substance.
- If pressure is applied to the display surface or its neighborhood of the LCD display module, the cell structure may be damaged, so be careful not to apply pressure to these sections.
- The polarizer covering the surface of the LCD display module is soft and can be easily scratched. Please be careful when handling the LCD display module.
- Clean the surface of the polarizer covering the LCD display module if it becomes soiled using following adhesion tape.
 - Scotch Mending Tape No. 810 or an equivalent
 - Never breathe the soiled surface or wipe the surface using a cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.
 - The following liquids/solvents may spoil the polarizer:
 - Water
 - Ketone
 - Aromatic Solvents
- Hold the LCD display module very carefully when placing the LCD display module into the system housing.
- Do not apply excessive stress or pressure to the LCD display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, be sure to secure the sufficient rigidity for the outer cases.





- Do not apply stress to the LSI chips and the surrounding molded sections.
- Do not disassemble or modify the LCD display module.
- Do not apply input signals while the logic power is off.
- Pay sufficient attention to the working environments when handing the LCD display module to prevent occurrence of element breakage accidents by static electricity.
 - Be sure to make human body grounding when handling LCD display modules.
 - Be sure to ground tools to use for assembly such as soldering irons.
 - To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
 - Protective film is being applied to the surface of the display panel of the LCD display module. Be careful since static electricity may be generated when exfoliating the protective film.
- Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the LCD display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after the film has been removed. In such a case, remove the residue material by the method discussed above.
- If electric current is applied when the LCD display module is being dewed or when it is placed under high humidity environments, the electrodes may become corroded. If this happens proceed with caution when handling the LCD display module.

12.3. Storage Precautions

- When storing the LCD display modules put them in static electricity preventive bags to avoid exposure to direct sunlight and fluorescent lamps. Also avoid high temperature and high humidity environments and low temperatures (less than 0°C) environments. (We recommend you store these modules in the packaged state when they were shipped from Crystalfontz). Be careful not to let water drops adhere to the packages or bags, and do not let dew gather on them.
- If electric current is applied when water drops are adhering to the surface of the LCD display module the LCD display module may have become dewed. If a dewed LCD display module is placed under high humidity environments it may cause the electrodes to become corroded. If this happens proceed with caution when handling the LCD display module.

12.4. Designing Precautions

- The absolute maximum ratings are the ratings that cannot be exceeded for LCD display module. If these values are exceeded, panel damage may happen.
- To prevent occurrence of malfunctioning by noise pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible.
- We recommend that you install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}). (Recommend value: 0.5A)
- Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- As for EMI, take necessary measures on the equipment side.
- When fastening the LCD display module, fasten the external plastic housing section.
- If the power supply to the LCD display module is forcibly shut down, by such errors as taking out the main battery while the LCD display panel is in operation, we cannot guarantee the quality of this LCD display module.
 - Connection (contact) to any other potential than the above may lead to rupture of the IC.

12.5. Disposing Precautions

- Request the qualified companies to handle the industrial wastes when disposing of the LCD display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

12.6. Other Precautions

- When an LCD display module is operated for a long period of time with a fixed pattern, the fixed pattern may remain as an after image or a slight contrast deviation may occur.



- If the operation is interrupted and left unused for a while, normal state can be restored.
- This will not cause a problem in the reliability of the module.
- To protect the LCD display module from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the LCD display modules.
 - Pins and electrodes
 - Pattern layouts such as the TCP & FPC
- With this LCD display module, the LCD driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this LCD driver is exposed to light, malfunctioning may occur.
 - Design the product and installation method so that the LCD driver may be shielded from light in actual usage.
 - Design the product and installation method so that the LCD driver may be shielded from light during the inspection processes.
- Although this LCD display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. Therefore, it is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
- We recommend that you construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data), to cope with catastrophic noise.
- Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier.
- Crystalfontz has the right to upgrade and modify the product function.
- The limitation of FPC bending:

