

CFAF320240F Series Pin Table

| Label | Pin | 6800 – 8 Bit | 6800 – 9 Bit | 6800 – 16 Bit | 6800 – 18 bit | 8080 – 8 Bit | 8080 – 9 Bit | 8080 – 16 Bit | 8080 – 18 Bit | SPI – 4 Wire | SPI – 3 Wire | RGB – 262K | RGB – 64K |
|--------------------|-----|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|-----------------------|--------------------|--------------------|--------------------|
| GND | 1 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| NC | 2 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| NC | 3 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| GND | 4 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| GND | 5 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| GND | 6 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| RD/E | 7 | Enable | Enable | Enable | Enable | Read Strobe | Read Strobe | Read Strobe | Read Strobe | Ground | Ground | Ground | Ground |
| SDO | 8 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| Reset | 9 | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset |
| CS | 10 | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Serial Chip Select | Serial Chip Select | Serial Chip Select | Serial Chip Select |
| SCL | 11 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | Serial Clock Input | Serial Clock Input | Serial Clock Input | Serial Clock Input |
| SDA | 12 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | Data Input | Data Input | Data Input | Data Input |
| D/C | 13 | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Serial Data / Command | No Connect | No Connect | No Connect |
| WR/RW | 14 | Read / Write | Read / Write | Read / Write | Read / Write | Write / Read | Write / Read | Write / Read | Write / Read | No Connect | No Connect | Ground | Ground |
| PS3 | 15 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 |
| PS2 | 16 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| PS1 | 17 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| PS0 | 18 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 |
| DB17 | 19 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR5 | RR4 |
| DB16 | 20 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR4 | RR3 |
| DB15 | 21 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR3 | RR2 |
| DB14 | 22 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR2 | RR1 |
| DB13 | 23 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR1 | RR0 |
| DB12 | 24 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR0 | GG5 |
| DB11 | 25 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | GG5 | GG4 |
| DB10 | 26 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | GG4 | GG3 |
| DB9 | 27 | NC or GND | x | NC or GND | x | NC or GND | x | NC or GND | x | NC or GND | NC or GND | GG3 | NC or GND |
| DB8 | 28 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG2 | GG2 |
| DB7 | 29 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG1 | GG1 |
| DB6 | 30 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG0 | GG0 |
| DB5 | 31 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB5 | BB4 |
| DB4 | 32 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB4 | BB3 |
| DB3 | 33 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB3 | BB2 |
| DB2 | 34 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB2 | BB1 |
| DB1 | 35 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB1 | BB0 |
| DB0 | 36 | NC or GND | NC or GND | NC or GND | x | NC or GND | NC or GND | NC or GND | x | NC or GND | NC or GND | BB0 | NC or GND |
| DEN | 37 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Data Enable | Data Enable |
| H _{SYNC} | 38 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Line Sync | Line Sync |
| V _{SYNC} | 39 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Frame / RAM Sync | Frame / RAM Sync |
| DCLK | 40 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Dot Clock | Dot Clock |
| NC | 41 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| GND | 42 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| GND | 43 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| V _{LOGIC} | 44 | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v |
| V _{LOGIC} | 45 | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v |
| NC | 46 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| K2 (LED 2-) | 47 | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - |
| A2 (LED 2+) | 48 | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + |
| A1 (LED 1+) | 49 | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + | Backlight + |
| K1 (LED 1-) | 50 | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - | Backlight - |
| Label | Pin | 6800 – 8 Bit | 6800 – 9 Bit | 6800 – 16 Bit | 6800 – 18 bit | 8080 – 8 Bit | 8080 – 9 Bit | 8080 – 16 Bit | 8080 – 18 Bit | SPI – 4 Wire | SPI – 3 Wire | RGB – 262K | RGB – 64K |

| Usage | Pin |
|---------|-----|
| X- (XL) | 1 |
| Y- (YD) | 2 |
| X+ (XR) | 3 |
| Y+ (YU) | 4 |

CFAF320240F Series Carrier Board Pin Table

| Label | Pin | 6800 – 8 Bit | 6800 – 9 Bit | 6800 – 16 Bit | 6800 – 18 bit | 8080 – 8 Bit | 8080 – 9 Bit | 8080 – 16 Bit | 8080 – 18 Bit | SPI – 4 Wire | SPI – 3 Wire | RGB – 262K | RGB – 64K |
|---------|-----|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|--------------------------------------|
| VDD | 1 | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v | +3v |
| GND | 2 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground |
| BL | 3 | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ | Enable / $\overline{\text{Disable}}$ |
| RESET | 4 | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset | Chip Reset |
| RD | 5 | Enable | Enable | Enable | Enable | Read Strobe | Read Strobe | Read Strobe | Read Strobe | Ground | Ground | Ground | Ground |
| CS | 6 | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Chip Select | Serial Chip Select | Serial Chip Select | Serial Chip Select | Serial Chip Select |
| SDO | 7 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| _SCL | 8 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | Serial Clock Input | Serial Clock Input | Serial Clock Input | Serial Clock Input |
| RS | 9 | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Data / Command | Serial Data / Command | No Connect | No Connect | No Connect |
| SDI | 10 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | Data Input | Data Input | Data Input | Data Input |
| WR | 11 | Read / Write | Read / Write | Read / Write | Read / Write | Write / Read | Write / Read | Write / Read | Write / Read | No Connect | No Connect | Ground | Ground |
| PS3 | 12 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 |
| PS2 | 13 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| PS1 | 14 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| PS0 | 15 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 |
| DB17 | 16 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR5 | RR4 |
| DB16 | 17 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR4 | RR3 |
| DB15 | 18 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR3 | RR2 |
| DB14 | 19 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR2 | RR1 |
| DB13 | 20 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR1 | RR0 |
| DB12 | 21 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | RR0 | GG5 |
| DB11 | 22 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | GG5 | GG4 |
| DB10 | 23 | x | x | x | x | x | x | x | x | NC or GND | NC or GND | GG4 | GG3 |
| DB9 | 24 | NC or GND | x | NC or GND | x | NC or GND | x | NC or GND | x | NC or GND | NC or GND | GG3 | NC or GND |
| DB8 | 25 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG2 | GG2 |
| DB7 | 26 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG1 | GG1 |
| DB6 | 27 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | GG0 | GG0 |
| DB5 | 28 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB5 | BB4 |
| DB4 | 29 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB4 | BB3 |
| DB3 | 30 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB3 | BB2 |
| DB2 | 31 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB2 | BB1 |
| DB1 | 32 | NC or GND | NC or GND | x | x | NC or GND | NC or GND | x | x | NC or GND | NC or GND | BB1 | BB0 |
| DB0 | 33 | NC or GND | NC or GND | NC or GND | x | NC or GND | NC or GND | NC or GND | x | NC or GND | NC or GND | BB0 | NC or GND |
| DEN | 34 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Data Enable | Data Enable |
| HSYNC | 35 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Line Sync | Line Sync |
| VSYNC | 36 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Frame / RAM Sync | Frame / RAM Sync |
| DCCLK | 37 | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Ground | Dot Clock | Dot Clock |
| Y- (YD) | 38 | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom | Bottom |
| X+ (XR) | 39 | Right | Right | Right | Right | Right | Right | Right | Right | Right | Right | Right | Right |
| Y+ (YU) | 40 | Top | Top | Top | Top | Top | Top | Top | Top | Top | Top | Top | Top |
| X- (XL) | 41 | Left | Left | Left | Left | Left | Left | Left | Left | Left | Left | Left | Left |
| NC | 42 | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect | No Connect |
| Label | Pin | 6800 – 8 Bit | 6800 – 9 Bit | 6800 – 16 Bit | 6800 – 18 bit | 8080 – 8 Bit | 8080 – 9 Bit | 8080 – 16 Bit | 8080 – 18 Bit | SPI – 4 Wire | SPI – 3 Wire | RGB – 262K | RGB – 64K |

| Usage | Pin |
|---------|-----|
| X- (XL) | 38 |
| Y- (YD) | 39 |
| X+ (XR) | 40 |
| Y+ (YU) | 41 |