Crystalfontz America, Inc.

SPECIFICATION

CUSTOMER:	
MODULE NO.:	CFAH2001Z-YYH-JP (Preliminary)

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
ISSUED DATE:			

Crystalfontz America, Inc.

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1.Module Classification Information

1	Brand: CRYSTALFONTZ AMERICA, INC							
2	Display Type: H→Character Type , G→Graphic Type							
3	Display's logical dimensions: 20 columns by 01 lines							
4	Model PCB Variant							
(5)	Backlight Type	N→Without backlight	T→LED, White					
		B→EL, Blue green	A→LED, Amber					
		D→EL, Green	R→LED, Red					
		W→EL, White	O→LED, Orange					
		F→CCFL, White	G→LED, Green					
		Y→LED, Yellow Green						
6	LCD Mode	$B \rightarrow TN$ Positive, Gray $T \rightarrow$	FSTN Negative					
		N→TN Negative,						
		G→STN Positive, Gray						
		Y→STN Positive, Yellow Gre	en					
		M→STN Negative, Blue						
		F→FSTN Positive						
7	LCD Polarizer	A→Reflective, N.T, 6:00	H→Transflective, W.T,6:00					
	Type/ Temperature range/ View	D→Reflective, N.T, 12:00	K→Transflective, W.T,12:00					
	direction	G→Reflective, W. T, 6:00	C→Transmissive, N.T,6:00					
		J→Reflective, W. T, 12:00	F→Transmissive, N.T,12:00					
		B→Transflective, N.T,6:00	I→Transmissive, W. T, 6:00					
		E→Transflective, N.T.12:00	L→Transmissive, W.T,12:00					
8	Special Code	JP→English and Japanese sta	andard font					

2. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7)Storage: please storage in anti-static electricity container and clean environment.

3. General Specification

Item	Dimension Unit					
Number of Characters	20 characters x 1 Line	-				
Module dimension(With LED Backlight)	182.0 x 43.5 x 14.0 (MAX)	mm				
View area	147.0 x 18.7	mm				
Active area	137.0 x 12.7	mm				
Dot size	1.10 x 1.50	mm				
Dot pitch	1.20 x 1.60	mm				
Character size	5.90 x 12.70	mm				
Character pitch	6.90	mm				
LCD type	STN					
Duty	1/16					
View direction	6 o'clock					
Backlight Type	YELLOW-GREEN					

4. Absolute Maximum Ratings

It	Symbol	Min	Max	Unit	
Input Voltage	$V_{\rm I}$	-0.3	VDD+0.3	V	
Supply Voltage For I	Logic	$VDD-V_{SS}$	-0.3	7.0	V
Supply Voltage For LCD		$V_{ m DD}$ - V_0	0	30	V
Wide Temperature	Cemperature Operating Temp.		-20	70	°C
LCM	Storage Temp.	Tstr	-30	80	°C

5. Electrical Characteristics

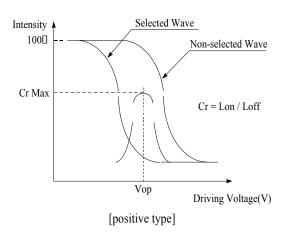
Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V_{DD} - V_{SS}	-	4.5	5.0	5.5	V
Supply Voltage For LCD	V_{DD} - V_{0}	Ta=25°C	4.8	5.0	5.2	V
Input High Volt.	$ m V_{IH}$	-	$0.7 V_{DD}$	ı	V_{DD}	V
Input Low Volt.	$ m V_{IL}$	-	V_{ss}	ı	$0.3 V_{DD}$	V
Supply Current	I_{DD}	$V_{DD}=5V$	1.5	2.0	2.5	mA
Supply Voltage of Yellow-	$ m V_{LED}$	Forward	3.8	4.2	4.5	V
green backlight		current				
		=380 mA				
		Number of				
		LED die				
		2x38 = 76				

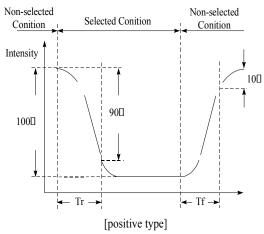
6. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	CR≥2	-20	-	35	deg
	(H)φ	CR≥2	-30	-	30	deg
Contrast Ratio	CR	-	-	3	-	-
Response Time	T rise	-	-	-	250	ms
	T fall	-	-	-	250	ms

Definition of Operation Voltage (Vop)

Definition of Response Time (Tr, Tf)



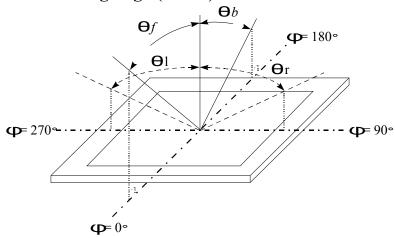


Conditions:

Operating Voltage : Vop Viewing $Angle(\theta^{[]}\phi): 0^{\circ[]} 0^{\circ}$

Frame Frequency : 64 HZ $\;\;$ Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle(CR≥2)

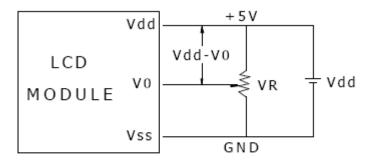


7. Interface Pin Function

Pin No.	Symbol	Level	Description
1	$V_{\rm SS}$	0V	Ground
2	$V_{\scriptscriptstyle DD}$	5.0V	Supply Voltage for logic
3	V0	(Variable)	Operating voltage for LCD
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU→Module) L: Write(MPU→Module)
6	Е	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	LED(+)		Anode of LED Backlight
16	LED(-)		Cathode of LED Backlight

8. POWER SUPPLY

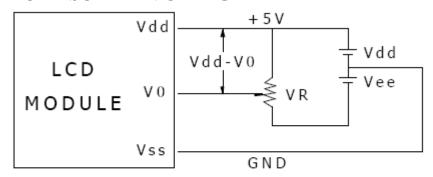
SINGLE SUPPLY VOLTAGE TYPE



Vdd-V0: LCD Driving Voltage

VR: 10K - 20K

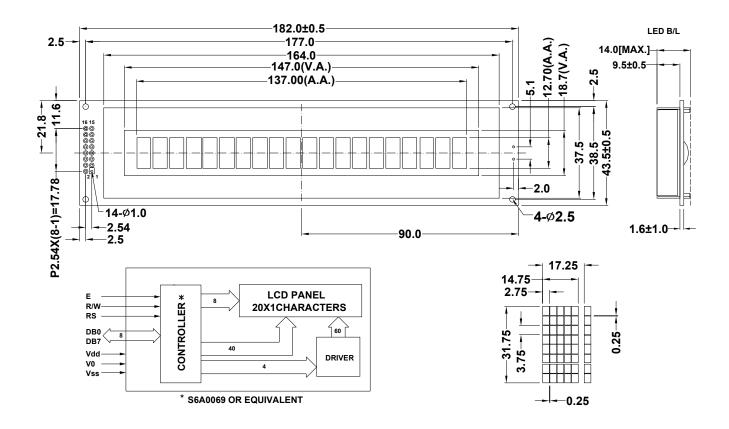
DUAL SUPPLY VOLTAGE TYPE



Vdd-V0: LCD Driving Voltage

VR: 10K - 20K

9. Contour Drawing & Block Diagram



10. Function Description

The LCD display Module is built in a LSI controller, the controller has two 8-bit registers, an instruction register (IR) and a data register (DR).

The IR stores instruction codes, such as display clear and cursor shift, and address information for display data RAM (DDRAM) and character generator (CGRAM). The IR can only be written from the MPU. The DR temporarily stores data to be written or read from DDRAM or CGRAM. When address information is written into the IR, then data is stored into the DR from DDRAM or CGRAM. By the register selector (RS) signal, these two registers can be selected.

RS	R/W	Operation
0	0	IR write as an internal operation (display clear, etc.)
0	1	Read busy flag (DB7) and address counter (DB0 to DB7)
1	0	Write data to DDRAM or CGRAM (DR to DDRAM or CGRAM)
1	1	Read data from DDRAM or CGRAM (DDRAM or CGRAM to DR)

Busy Flag (BF)

When the busy flag is 1, the controller LSI is in the internal operation mode, and the next instruction will not be accepted. When RS=0 and R/W=1, the busy flag is output to DB7. The next instruction must be written after ensuring that the busy flag is 0.

Address Counter (AC)

The address counter (AC) assigns addresses to both DDRAM and CGRAM

Display Data RAM (DDRAM)

This DDRAM is used to store the display data represented in 8-bit character codes. Its extended capacity is 80×8 bits or 80 characters. Below figure is the relationships between DDRAM addresses and positions on the liquid crystal display.

AC (hexadecimal)

Character Generator ROM (CGROM)

The CGROM generate 5×8 dot or 5×10 dot character patterns from 8-bit character codes. See Table 2.

Character Generator RAM (CGRAM)

In CGRAM, the user can rewrite character by program. For 5×8 dots, eight character patterns can be written, and for 5×10 dots, four character patterns can be written.

Write into DDRAM the character code at the addresses shown as the left column of table 1. To show the character patterns stored in CGRAM.

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Character patterns

Table 1.

For 5 * 8 dot character patterns

Character Codes (DDRAM data)	CGRAM Address	CGRAM Address Character Patterns (CGRAM data)					
7 6 5 4 3 2 1 0	5 4 3 2 1 0	7 6 5 4 3 2 1 0					
High Low	High Low	High Low					
0 0 0 0 * 0 0 0	0 0 0 0 0 1 0 1 0 0 1 1 0 0 0 1 0 0 1 0 1 1 1 0 1 1 1 0 0 0	* * * * * * * * * * * * * * * * * * *	Character pattern(1) Cursor pattern				
0 0 0 0 * 0 0 1	0 0 1 0 1 0 0 1 1 0 0 1 1 0 0 1 0 1 1 1 0 0 0 0	* * * * 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Character pattern(2) Cursor pattern				
		• •					
0 0 0 0 * 1 1 1	1 1 1 1 0 0 1 0 1 1 1 0 1 1 1	* * *					

For 5 * 10 dot character patterns

10 dot character part	CITIS		
Character Codes (DDRAM data)	CGRAM Address	Character Patterns (CGRAM data)	
7 6 5 4 3 2 1 0	5 4 3 2 1 0	7 6 5 4 3 2 1 0	
High Low	High Low	High Low	
0 0 0 0 * 0 0 0	0 0 0 0 0 0 0 0 0 0 0 1 1 0 0 0 1 1 0 0 0 1 0 0 0 1 1 1 0 0 0 1 1 1 1 1 1 1 0 0 0 0 1 1 1 0 0 1 1 0 0 1 1 1 0 1 0 0 1 1 1 0 1 0 0 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 0 0 0 1 1 1 1 1 0 1 1 0 0 0 1 1 1 1 1 0 1 1 0 0 0 1 1 1 1 1 0 1 1 0 0 0 1	* * * * 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Character pattern Cursor pattern
	1 1 1 1	* * * * * * * *	

■ : " High "

11. Character Generator ROM Pattern

Table.2

Upper 4 Lower Bits 4 Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			0	<u>a</u>	P	`	P					7	E	α	þ
xxxx0001	(2)		I	1	A	Q	a	4				7	7	4	ä	q
xxxx0010	(3)		II	2	B	R	Ь	۲			r	1	ij	X	ß	Θ
xxxx0011	(4)		#	3	C	5	C	S			L	Ż	Ŧ	E	ε	60
xxxx0100	(5)		\$	4	D	T	d	t.			۸.	I	ŀ	þ	Ы	Ω
xxxx0101	(6)		7.	5	E	U	e	u			•	7		1	σ	ü
xxxx0110	(7)		&	6	F	Ų	f.	V			7	Ħ	_	3	ρ	Σ
xxxx0111	(8)		,	7	G	Ŵ	9	W			7	#	7	5	9	π
xxxx1000	(1)		(8	H	X	h	X			4	7	礻	IJ	.,	\overline{X}
xxxx1001	(2))	9	I	Y	i	Ч			Ċ	፞ፓ	J	ıĿ	-1	Ч
xxxx1010	(3)		*		J	Z	j	Z			I		ıΊ	ŀ	1	7
xxxx1011	(4)		+	7	K		k	{			7	Ħ	L		×	Я
xxxx1100	(5)		,	<		¥	1				ħ	Ð	7	7	¢	Ħ
xxxx1101	(6)		_		M		M	}			ュ	Z	ኅ	_,	Ł	÷
xxxx1110	(7)			>	N	۸	n	÷			3	t	†	**	ñ	
xxxx1111	(8)		/	?	0	_	0	÷			·y	y	7		ö	

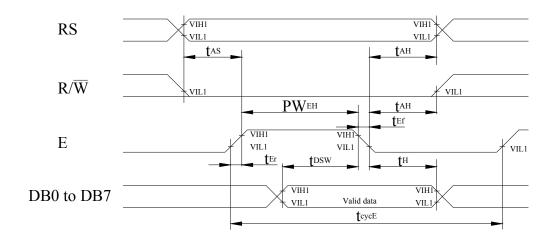
12. Instruction Table

Instruction				Ins	structi	ion Co	ode				Description	Execution time
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		(fosc=270Khz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "00H" to DDRAM and set DDRAM address to "00H" from AC	1.53ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39μs
Display ON/OFF Control	0	0	0	0	0	0	1	D	С	В	Set display (D), cursor (C), and blinking of cursor (B) on/off control bit.	39μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39µs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL:8-bit/4-bit), numbers of display line (N:2-line/1-line)and, display font type (F:5×11 dots/5×8 dots)	39μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39μs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43µs

"-" = don't care

13. Timing Characteristics

13.1 Write Operation



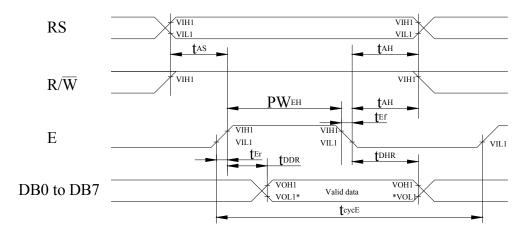
Symbol Item Min Typ Max Unit Enable cycle time $t_{\rm cycE}$ 1200 ns Enable pulse width (high level) PW_{EH} 140 ns Enable rise/fall time 25 $t_{\rm Er}, t_{\rm Ef}$ ns Address set-up time (RS, R/W to E) 0 t_{AS} ns Address hold time 10 t_{AH} ns Data set-up time 40 ns $t_{\rm DSW}$ Data hold time 10

 $t_{\rm H}$

Ta=25°C, VDD=5.0± 0.5V

ns

13.2 Read Operation



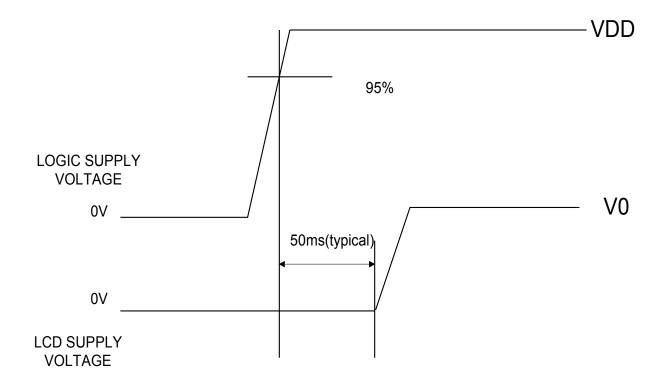
NOTE: *VOL1 is assumed to be 0.8V at 2 MHZ operation.

Ta=25 \Box , VDD=5.0 \pm 0.5V

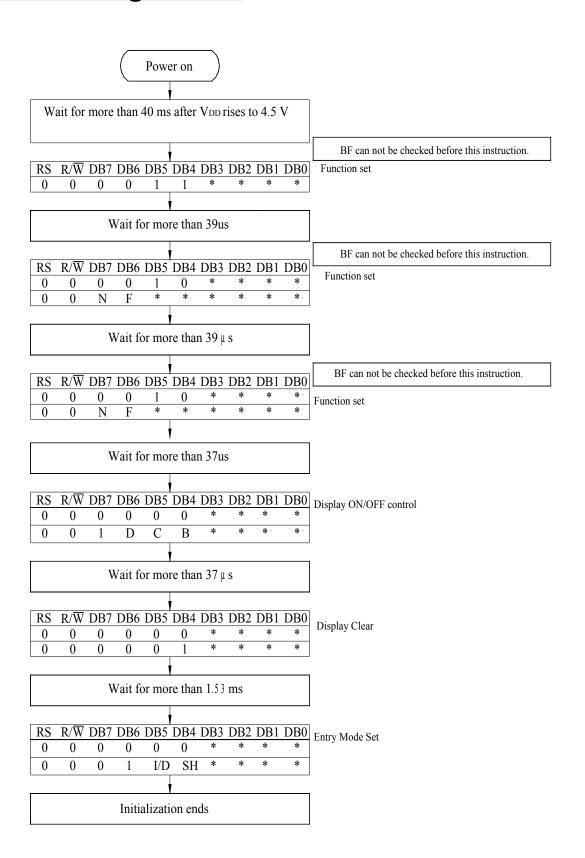
				- , .	DD 0.0- 0.0 1
Item	Symbol	Min	Тур	Max	Unit
Enable cycle time	$t_{ m cycE}$	1200	-	-	ns
Enable pulse width (high level)	PW_{EH}	140	-	-	ns
Enable rise/fall time	$t_{\mathrm{Er}}, t_{\mathrm{Ef}}$	-	-	25	ns
Address set-up time (RS, R/W to E)	t_{AS}	0	-	-	ns
Address hold time	$t_{ m AH}$	10	-	-	ns
Data delay time	$t_{ m DDR}$	-	-	100	ns
Data hold time	$t_{ m DHR}$	10	-		ns

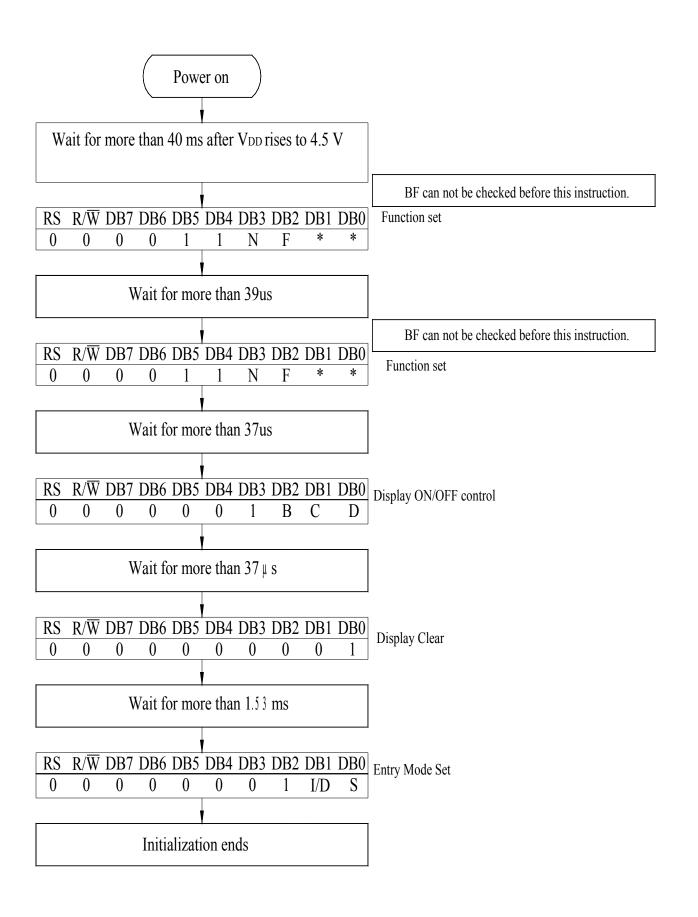
13.3 Timing Diagram of VDD Against V0.

Power on sequence shall meet the requirement of Figure 4, the timing diagram of VDD against V0.



14.Initializing of LCM





15.Quality Assurance

Screen Cosmetic Criteria

Item	Defect	Judgment Criterion	Partition
1	Spots	A)Clear	Minor
		Size: d mm Acceptable Qty in active area	
		d ≤0.1 Disregard	
		0.1 <d≤0.2 6<="" td=""><td></td></d≤0.2>	
		0.2 <d≤0.3 2<="" td=""><td></td></d≤0.3>	
		0.3 <d 0<="" td=""><td></td></d>	
		Note: Including pin holes and defective dots which must	
		be within one pixel size.	
		B)Unclear	
		Size: d mm Acceptable Qty in active area	
		d ≤0.2 Disregard	
		0.2 <d≤0.5 6<="" td=""><td></td></d≤0.5>	
		0.5 <d≤0.7 2<="" td=""><td></td></d≤0.7>	
2	Bubbles in Polarizer	0.7 <d 0="" acceptable="" active="" area<="" d="" in="" mm="" qty="" size:="" td=""><td>Minor</td></d>	Minor
		d≤0.3 Disregard	
		0.3 <d≤1.0 3<="" td=""><td></td></d≤1.0>	
		1.0 <d≤1.5 1<="" td=""><td></td></d≤1.5>	
		1.5 <d 0<="" td=""><td></td></d>	
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be	Minor
		remarkable.	
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor
5	Coloration		Minor
		LCD panels.	
		Back-light type should be judged with back-light on state	
		only.	

16.Reliability

Content of Reliability Test

Environmenta	l Test		
Test Item	Content of Test	Test Condition	Applicable
			Standard
High	Endurance test applying the high storage	80°C	
Temperature	temperature for a long time.		
storage		96hrs	
Low	Endurance test applying the high storage	-30°C	
Temperature	temperature for a long time.		
storage		96hrs	
High	Endurance test applying the electric stress	70°C	
Temperature	(Voltage & Current) and the thermal stress		
Operation	to the element for a long time.	96hrs	
Low	Endurance test applying the electric stress	-20°C	<u> </u>
Temperature	under low temperature for a long time.	== 0	
Operation	and to me temperature for a fong time.	96hrs	
High	Endurance test applying the high	80°C90%RH	
Temperature/	temperature and high humidity storage for a		
Humidity	long time.	96hrs	
Storage	iong time.	701113	
High	Endurance test applying the electric stress	70°C,90%RH	<u> </u>
Temperature/	(Voltage & Current) and temperature /	/ · · · · · · · · · · · · · · · · · · ·	
Humidity	humidity stress to the element for a long		
Operation	time.	96hrs	
Temperature	Endurance test applying the low and high	-30°C/80°C	
Cycle	temperature cycle.	30 0,00 0	
C y 010	temperature cycle.	10 cycles	
	-30°C 25°C 80°C		
	-30 C 23 C 80 C		
	30min 5min 30min		
N. 1 . 1 1	1 cycle		
Mechanical Tes	ST .		
Vibration test	Endurance test applying the vibration	10~22Hz→1.5mmp-p	<u> </u>
	during transportation and using.		
		22~500Hz→1.5G	
		Total 0.5hrs	
Shock test	Constructional and mechanical endurance	50G Half sign	<u> </u>
	test applying the shock during		
	transportation.	wave 11 msedc	
		2 times of cost	
		3 times of each	
		direction	<u> </u>

^{***}Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25°C