

CFAL12832C0-091B-[W|Y]

CFAL4864A-071B-W

CFAL6448A-066B-W

OLED_FRONT

GND

GND



94v0
U.S.A.
China
RoHS

RFG

BS1	BS0	Interface Mode	D2	D1	D0	D/C
0	0	SPI: 8-bit, 4-wire	NC	MOSI	SCK	D/C
0	1	SPI: 9-bit, 3-wire	NC	MOSI	SCK	NC
1	0	I ² C	SDA=SDA	SCL	ADDR	

GND D2 D1 D0 D/C RES CS

SDA=SDA

BS1=1

BS0=1

C6

C5

BS1=0

BS0=0

3.3v

GND



Crystalfontz
CFA-10054 0v3

www.crystalfontz.com

©2019 Crystalfontz America, Inc.

GND

GND

OLED_BACK

GND



94wD
U.S.A.
China
RoHS



Crystalfontz
CFA-10054 0v3
www.crystalfontz.com

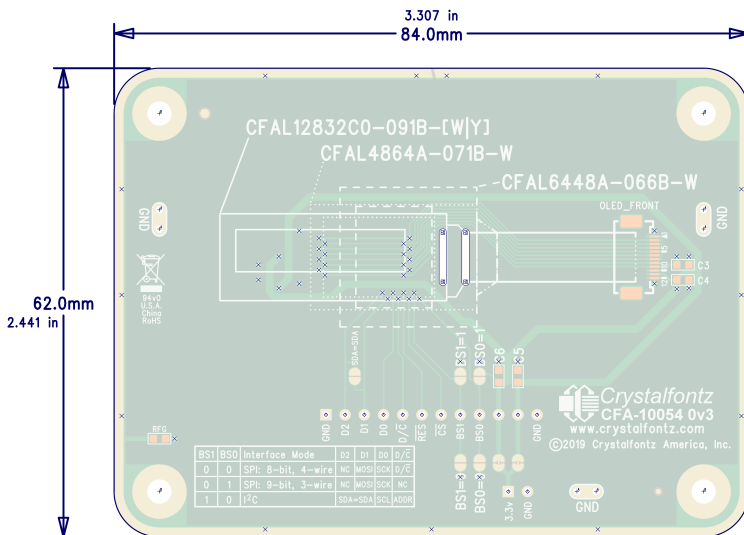
©2019 Crystalfontz America, Inc.

GND D2 D1 D0 D/C RES CS BS1 BS0 VDD VBAT GND

GND

3.3V
GND

SIZE	QTY	SYM	PLATED	TOL
0.5	56	✕	YES	+/-0.0
0.889	14	◇	YES	+/-0.0
0.9652	6	⊕ ^A	YES	+/-0.0
3.2	4	⊕ ^B	YES	+/-0.0
1 x 7.5	2	⊞	NO	+/-0.0



LAYERS: 2
MATERIAL: FR4, RoHS, 94v0
THICKNESS: 1.6mm (62.99 mils)
MINIMUM SPACING: 0.2 mm (7.874 mils)
MINIMUM TRACE: 0.2 mm (7.874 mils)
SMALLEST VIA: 0.5mm drill in 0.8mm pad
(19.68 mil drill in 31.49 mil pad)

FINISH: Gold
SILK SCREEN: White
SOLDER MASK: Green

Many pads have intentional undersize solder mask openings for physical strength.

Make top and bottom solder masks exactly as shown. Do not "open-up" the solder mask layers.

BARE TRACE BEYOND EDGE IS INTENTIONAL
ESD HANDLING FEATURE. ROUTING WILL
TRIM COPPER TO EXACT BOARD OUTLINE.
Refer to CFA-10054_0v3_PCB_Preview.pdf

Crystallfontz 12412 East Saltese Avenue Spokane Valley, WA 99216 USA Tel: (509) 892-1200 http://www.crystallfontz.com Copyright © 2019 Crystallfontz America, Inc.	PROJECT CFA-10054 VERSION 0v3	
	PCB PCB10054 REVISION 0v3	DATE 2019-11-06
LAYER Drill Drawing/Notes	FILE NAME C-CFA10054_0v3_DD.pho	

Edge is
not plated

Copper goes
all the way
to the edge

