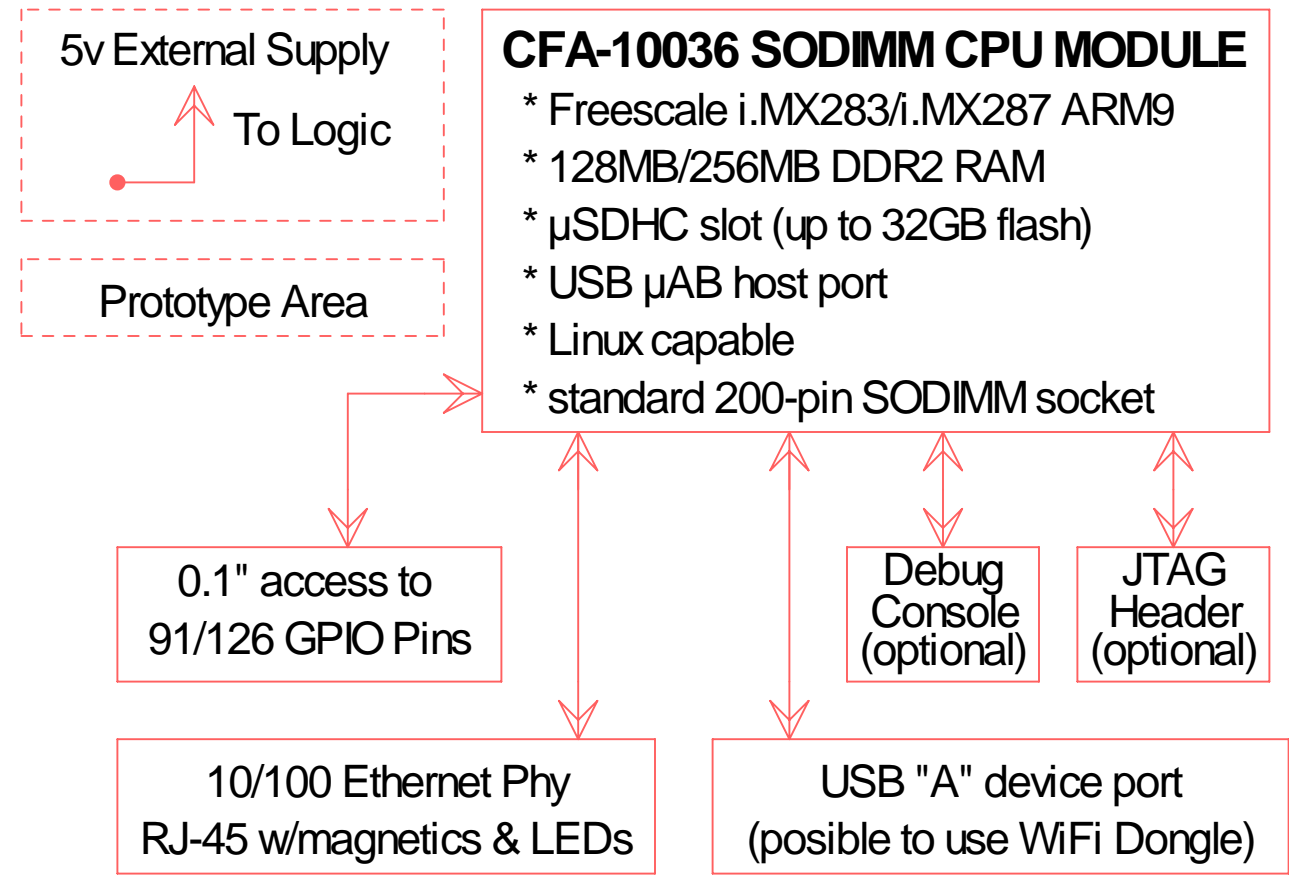


REV	ENGINEER	DATE	REMARKS
0v0	BAC	2012-11-02	Initial Creation
1.0	BAC	2013-01-21	1st Production Run
-	-	-	-
-	-	-	-
-	-	-	-



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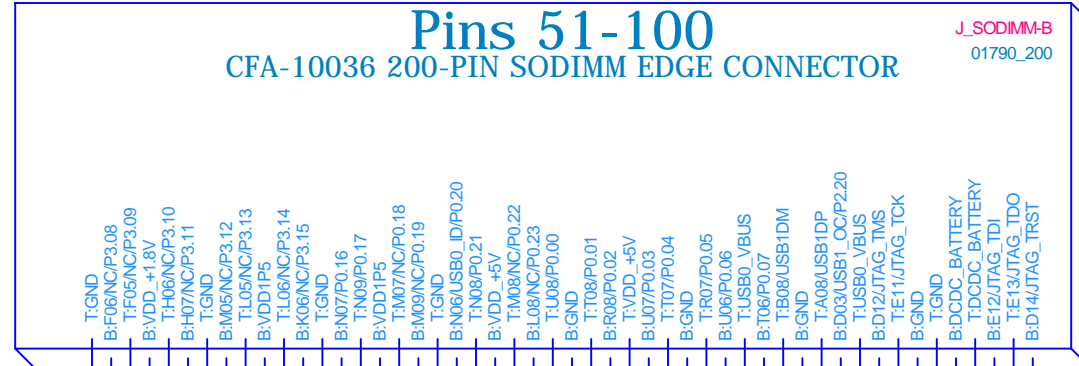
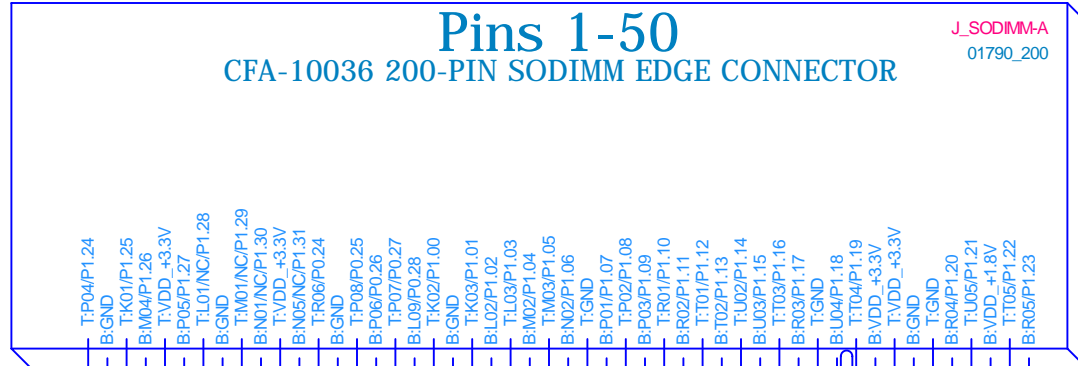
CFA-10037: Breakout Demo Board for CFA-10036

Page 1 / 6 : Block Diagram

FILE NAME:
CFA-10037_v1p0.SCH

REVISION:
1.0

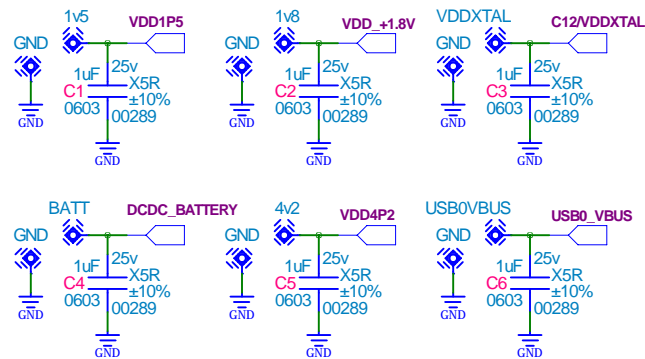
REV	ENGINEER	DATE	REMARKS
0v0	BAC	2012-11-02	Initial Creation
1.0	BAC	2013-01-21	1st Production Run
-	-	-	-
-	-	-	-
-	-	-	-



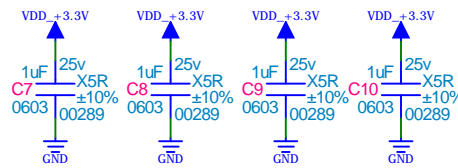
Signal Naming:
B##/[NC/FUNC]/P#.#
B## = i.MX28 298-ball BGA ball/pin number
NC = No connect on i.MX283 processor
FUNC = dedicated function on 10K36, or i.MX28 dedicated function
P#.# = GPIO port bank and pin

X: No Connect on i.MX283 version
6: Primary use is internal to CFA-100036
R: Reserved function for i.MX28

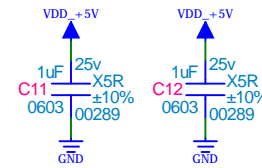
Test Points & Decoupling on Unused Supplies



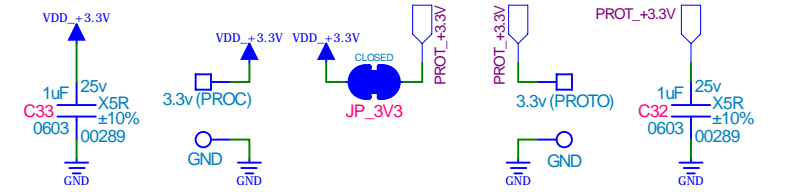
Decoupling where the 10K36 gives current back to us.



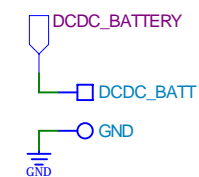
Decoupling where the 10K36 draws current from us.



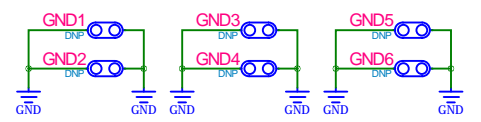
Split or Join 3.3v to Prototype Area



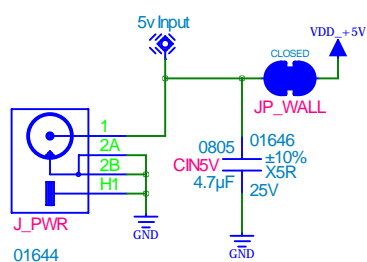
0.1" Test Point for DCDC_BATTERY



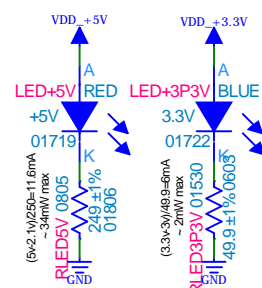
Scope Grounds



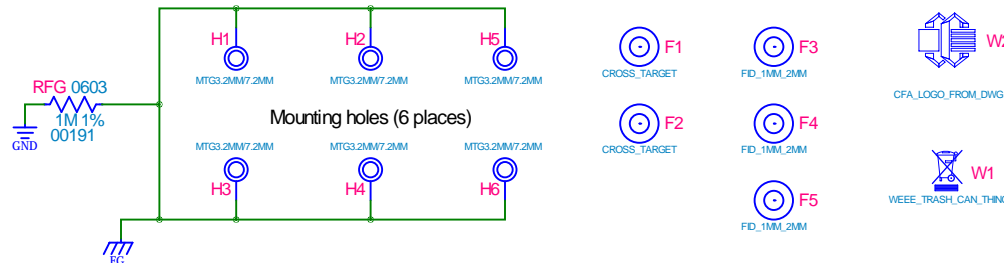
5VDC Power Input



Power LEDs



Mounting holes (6 places)



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CFA-10037: Breakout Demo Board for CFA-10036

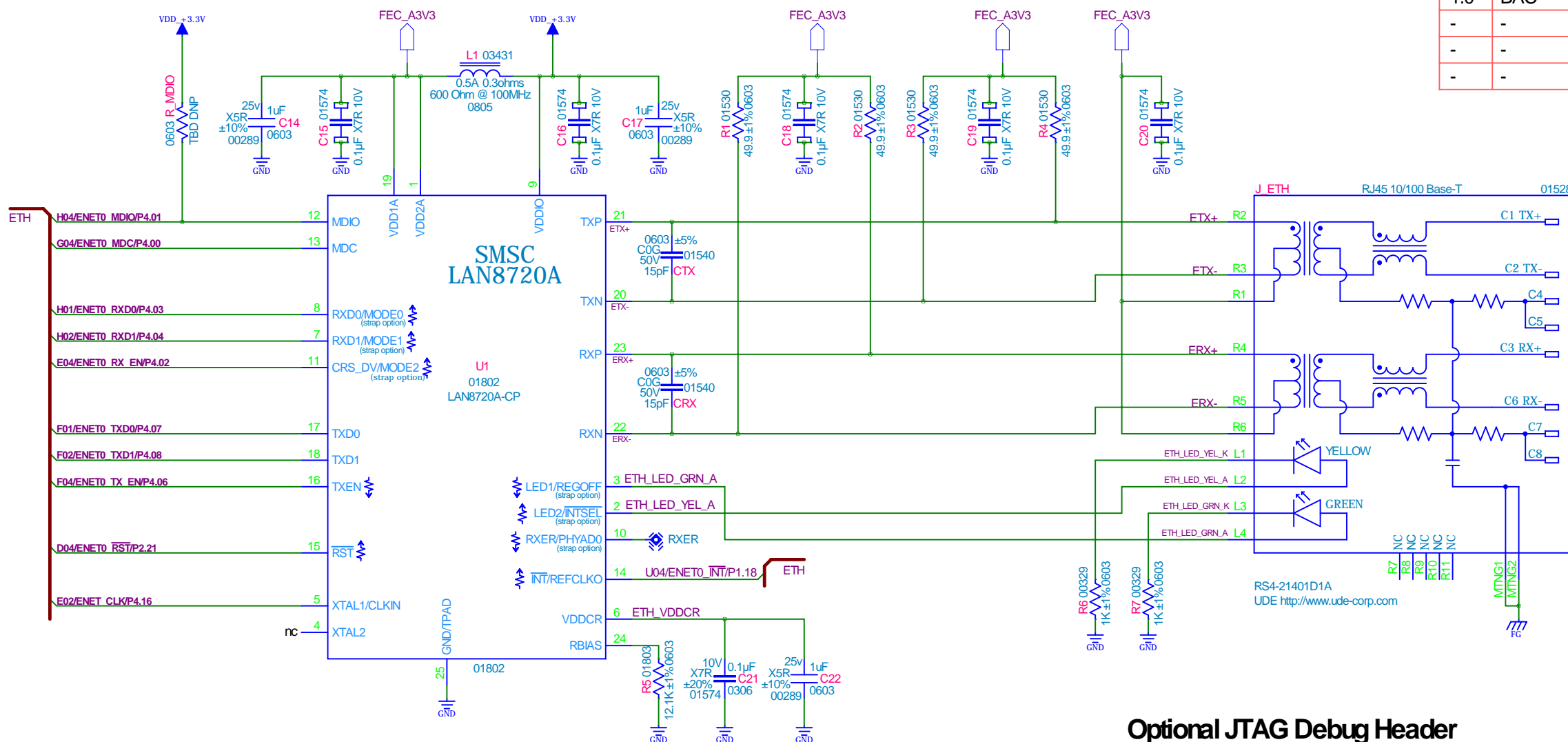
Page 2 / 6 : Processor SODIMM (1/2)

FILE NAME: CFA-10037_v1p0.SCH

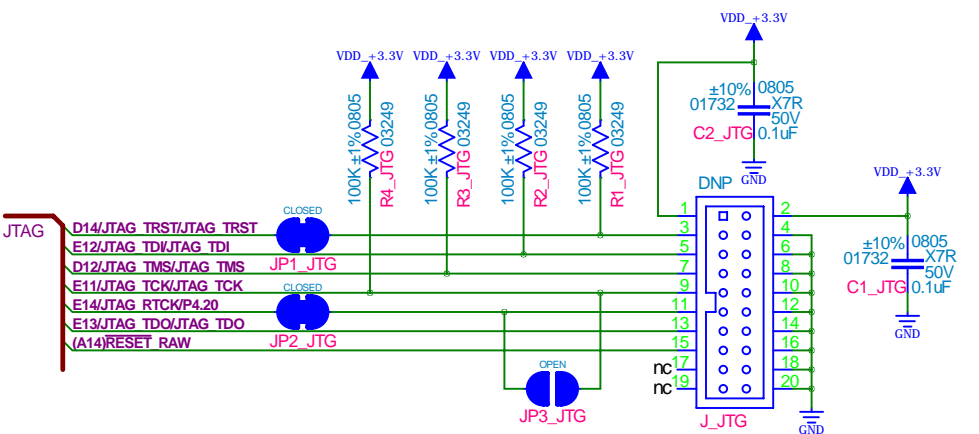
REVISION: 1.0

10/100 Ethernet Phy and RJ-45

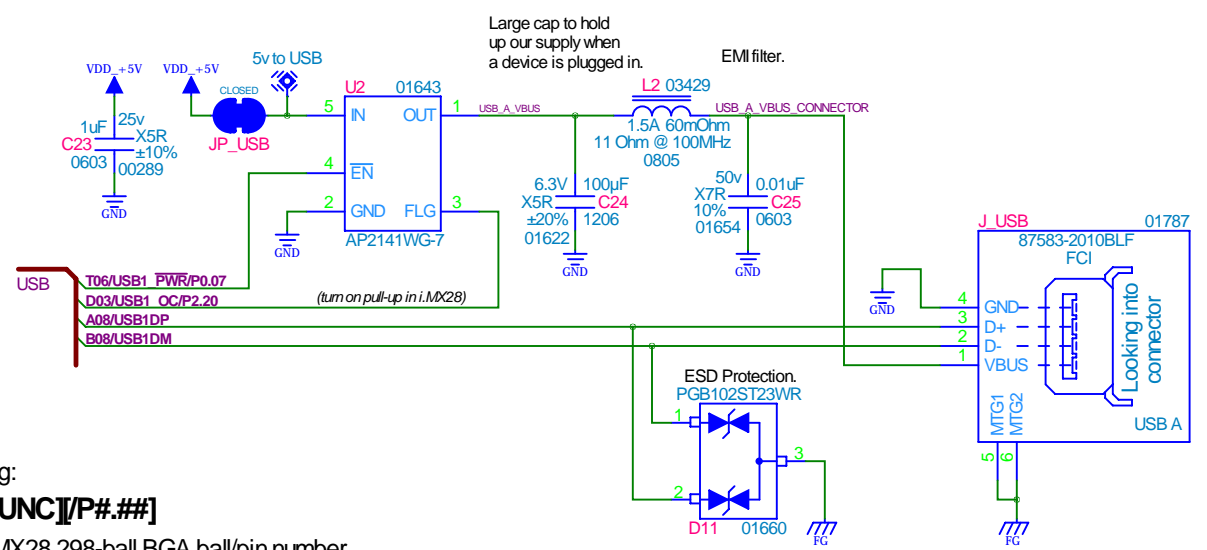
REV	ENGINEER	DATE	REMARKS
0v0	BAC	2012-11-02	Initial Creation
1.0	BAC	2013-01-21	1st Production Run
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-	-	-	-



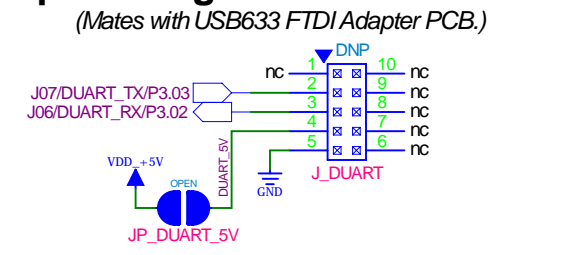
Optional JTAG Debug Header



USB A Connector and Power Switch



Optional Logic Level Console Port

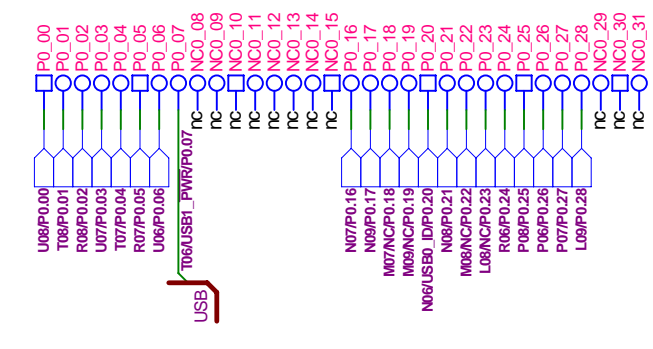


Signal Naming:
B##/[NC]/FUNC[/P#./##]
B## = i.MX28 298-ball BGA ball/pin number
NC = No connect on i.MX283 processor
FUNC = dedicated function on 10K36, or i.MX28 dedicated function
P#./## = GPIO port bank and pin

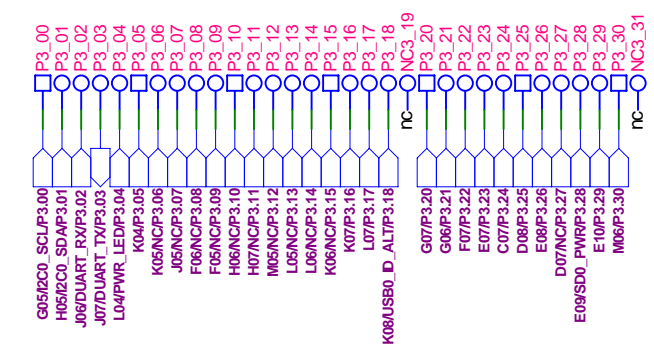
Crystalfontz America, Inc.
CFA-10037: Breakout Demo Board for CFA-10036
Page 4 / 6 : Ethernet and USB
 FILE NAME: **CFA-10037_v1p0.SCH** REVISION: **1.0**

REV	ENGINEER	DATE	REMARKS
0v0	BAC	2012-11-02	Initial Creation
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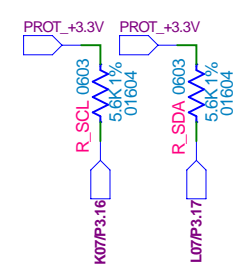
Port 0



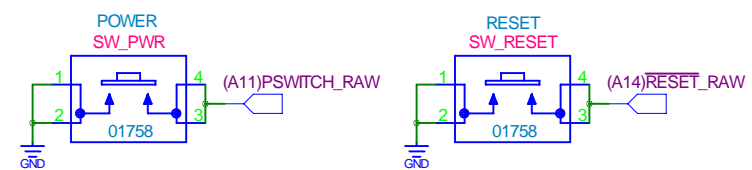
Port 3



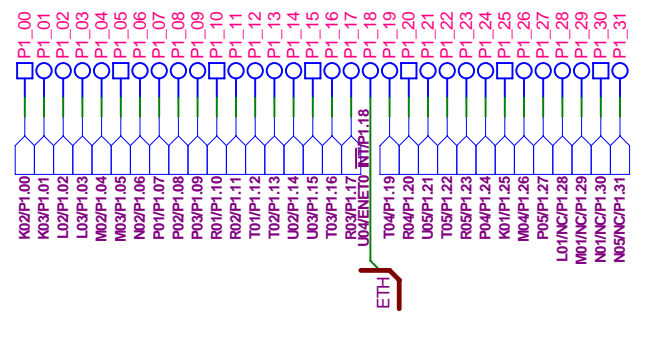
I2C Pull-Ups (optional)



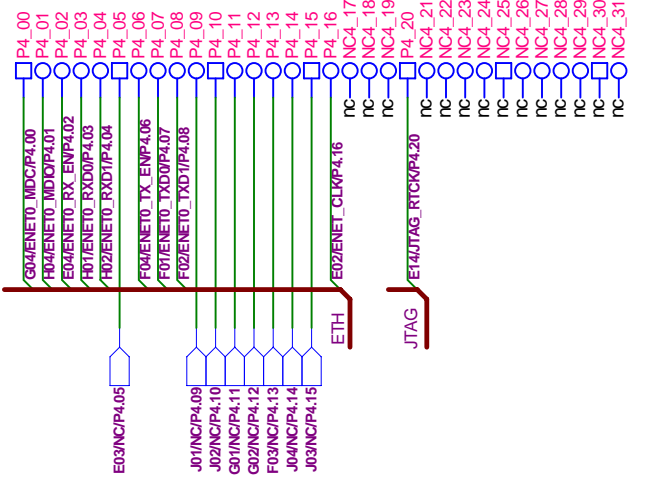
Power & Reset Switches



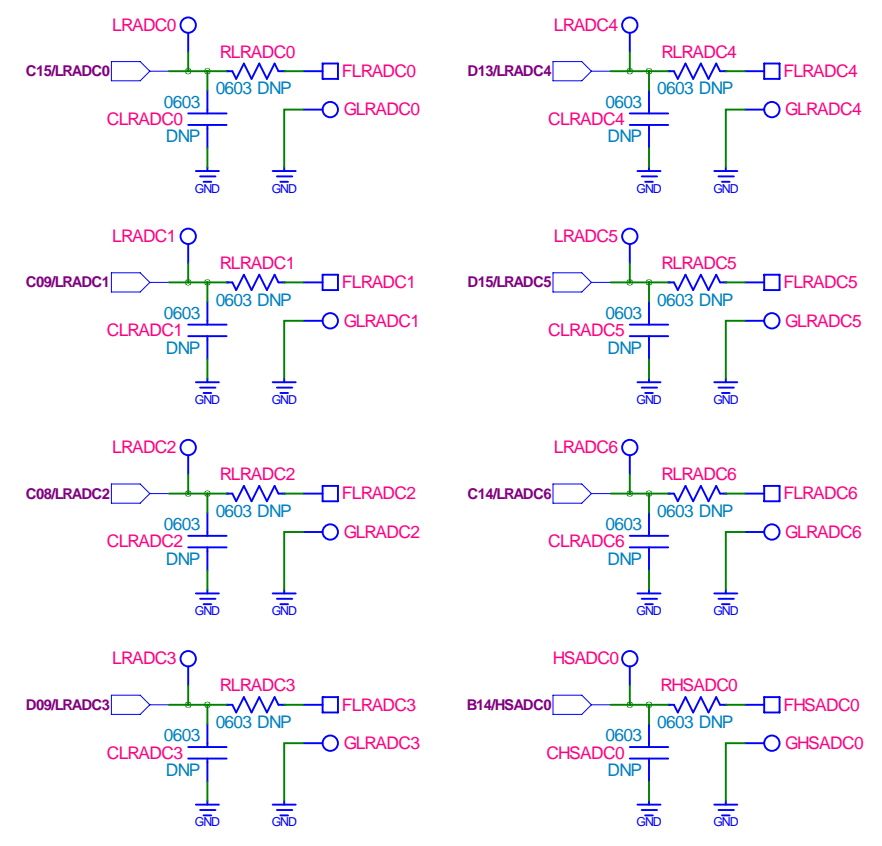
Port 1



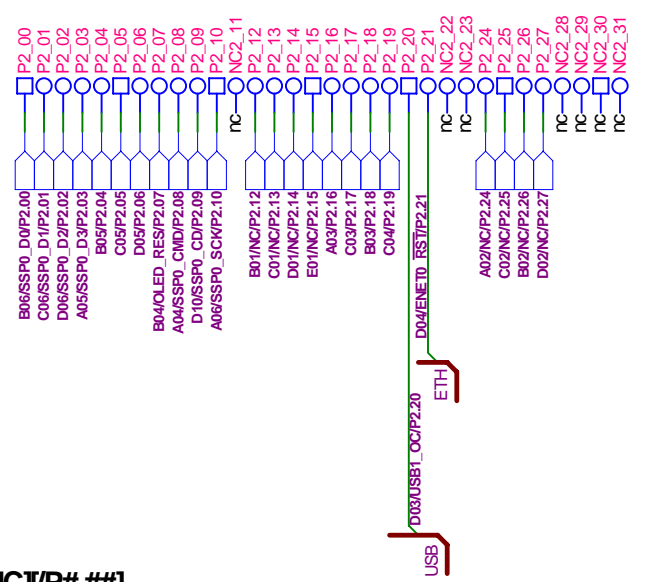
Port 4



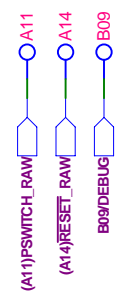
ADC and optional filters



Port 2



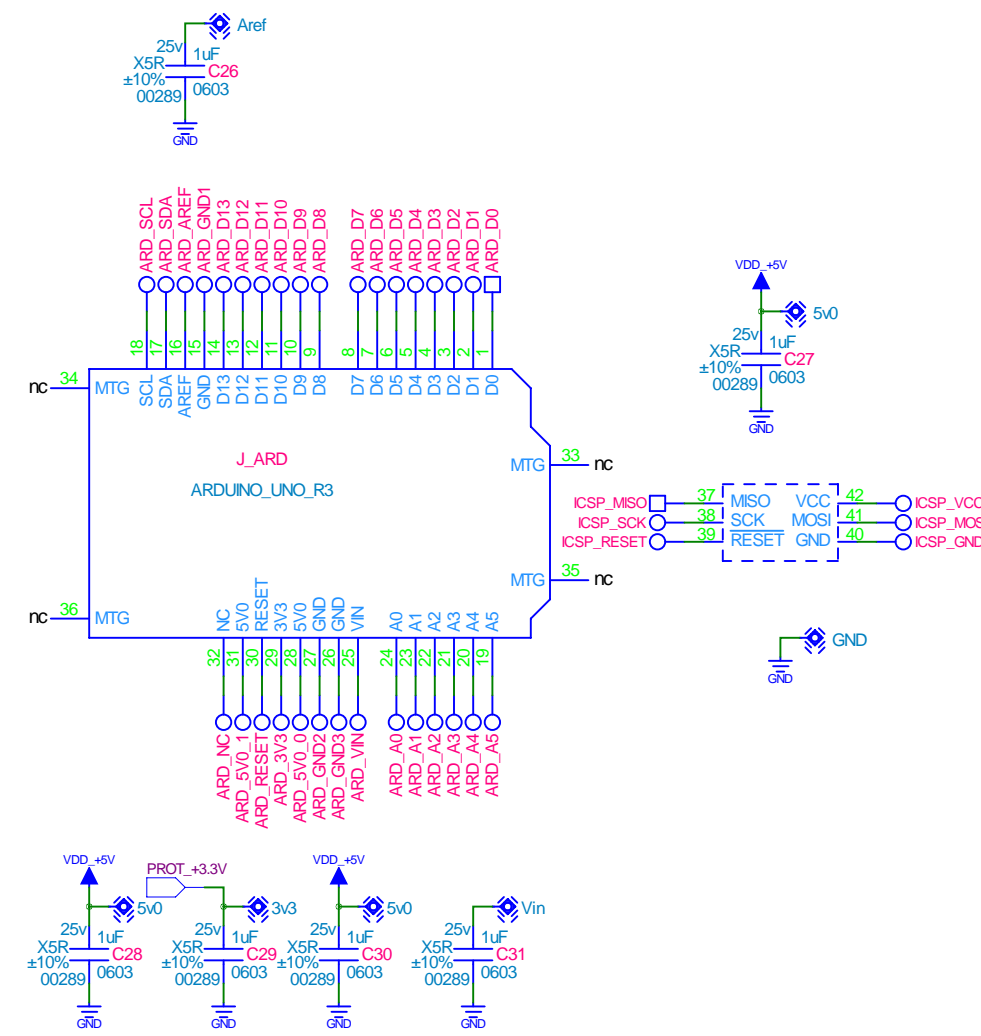
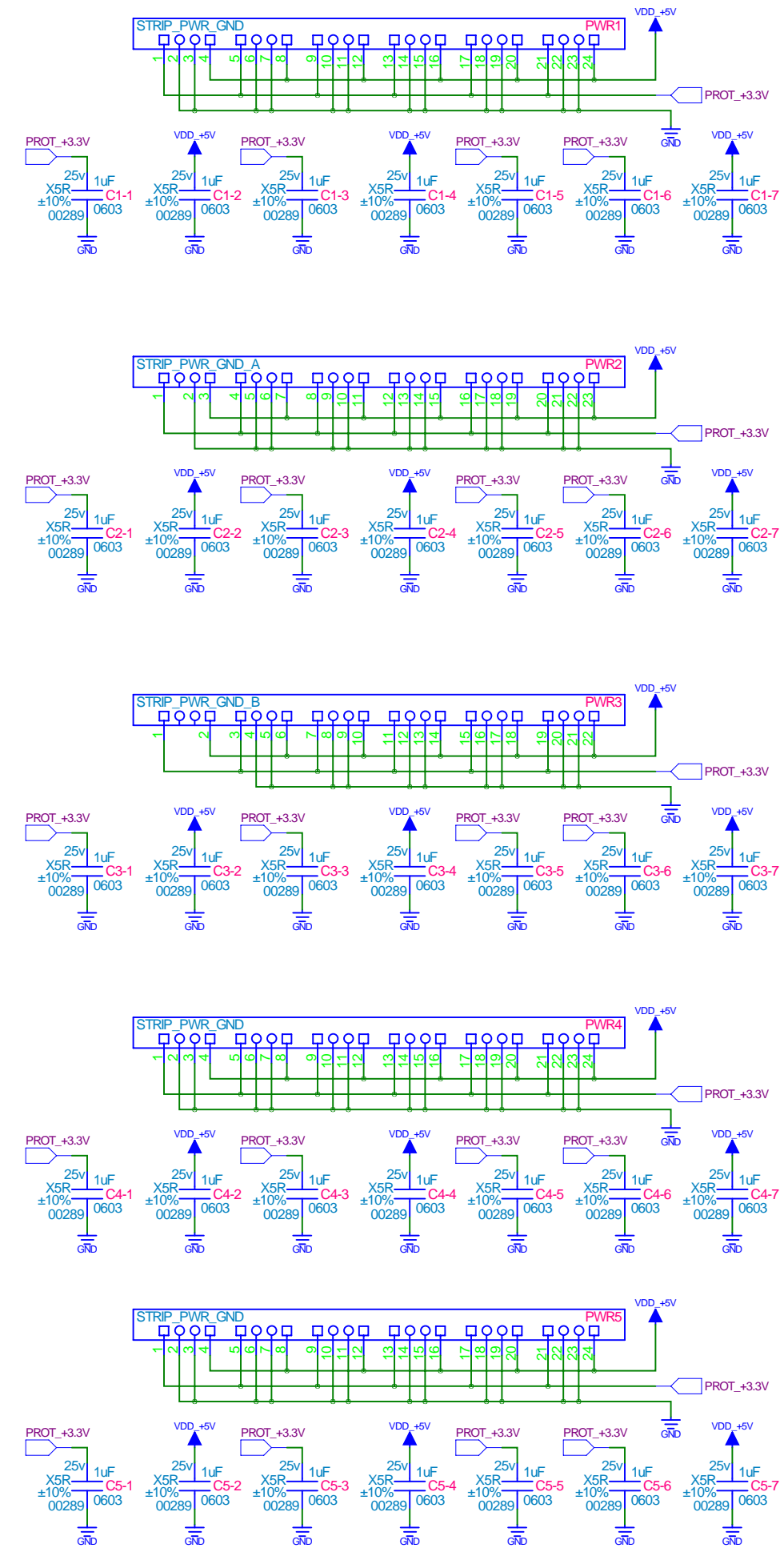
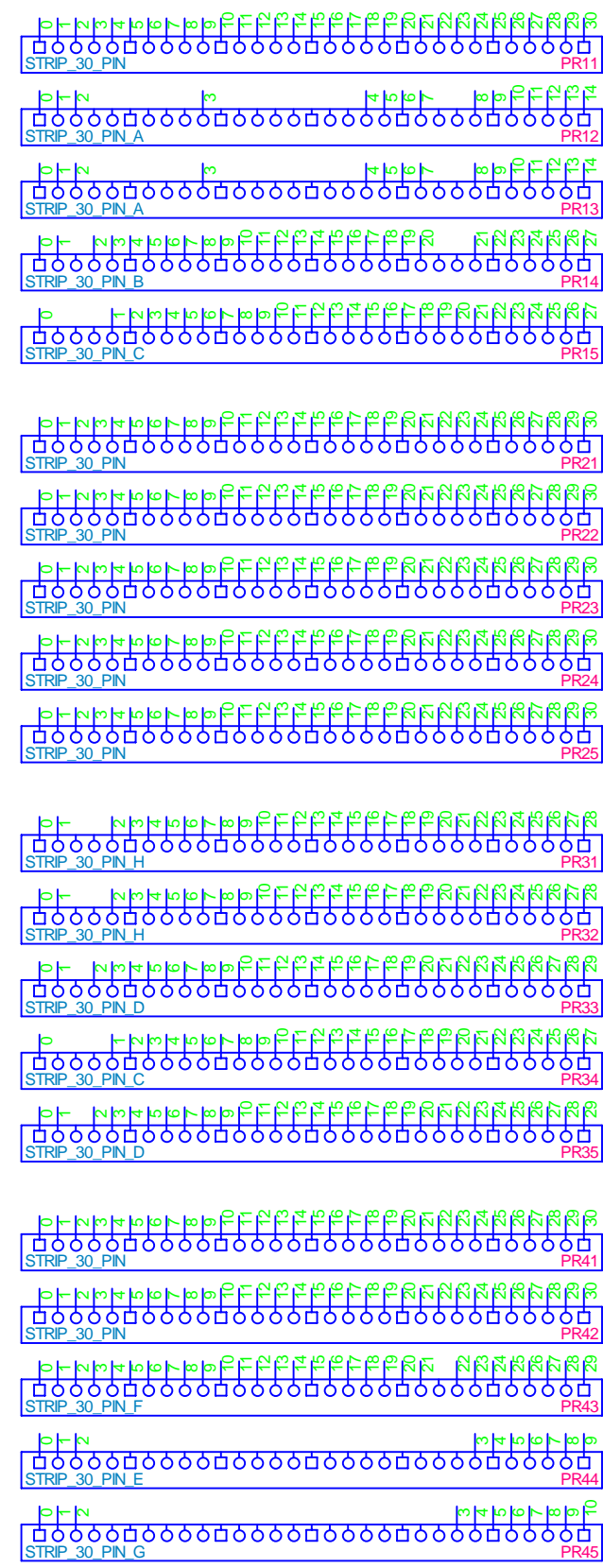
Non-port pins



Signal Naming:
B##/[NC]/FUNC[/P#./##]
B## = i.MX28 298-ball BGA ball/pin number
NC = No connect on i.MX283 processor
FUNC = dedicated function on 10K36, or i.MX28 dedicated function
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CFA-10037: Breakout Demo Board for CFA-10036
Page 5 / 6 : Ethernet and USB
 FILE NAME: **CFA-10037_v1p0.SCH** REVISION: **1.0**

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CFA-10037: Breakout Demo Board for CFA-10036

Page 6 / 6 : Proto Area

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