

# CFA921-TS User Guide



CFAF800480A-050T-TS TFT display shown with CFA10058 mother board. CFA10036 not shown.

CFA921-TS is an assembled product that contains:

- <u>CFA10036</u> SOM (System On Module)
- CFA10058 Mother Board
- <u>CFAF800480A-050T-TS</u> TFT Display

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## **CFA921-TS Hardware Revision Information**

For information about hardware revisions, see the Part Change Notifications (PCNs) under the "Notices" tab on the <u>CFA921-TS</u> web page.



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## INTRODUCTION

CFA921-TS is an assembled product that contains these three parts, described below. Software support is already built into the Linux mainline kernel.

#### CFA10058 Mother Board

The CFA921-TS is shipped with our CFAF800480A-050T-TS TFT display mounted on top of the CFA10058 mother board and the CFA10036 SOM (System On Module) mounted on the back of the mother board. The mother board is only a few millimeters larger than the display. The CFA10036 is smaller than the motherboard.

Pin descriptions for the CFA10058 are included in this user guide.

#### <u>CFA10036</u> SOM (System On Module)

The CFA10036 is a small, highly functional ARM9-based Linux SOM (System On Module) shipped with a full Linux operating system. Because a full Linux mainline kernel is already ported to the CFA10036, you can devote your resources to applications in the languages of your choice. It is low cost, easy to use, and has lots of GPIO. The CFA10036 in this assembled CFA921-TS has an i.MX283 processor. Please refer to the <u>Freescale i.MX28 Data Sheet</u> for more information on the processor. For more information on the CFA10036, see the *CFA10036 Getting Started Guide* listed under the <u>Doc/Files</u> tab on the CFA921-TS web page.

Blind threaded SMT standoffs hold the CFA10036 SOM securely to the back of the CFA10058 mother board.

#### CFAF800480A-050T-TS TFT Display

The CFAF800480A-050T-TS is an 800 x 480 full color touch screen display. The active area is 5-inches diagonal. Supply voltage is 3.3v. For more information, see the *CFAF800480A-050T-TS Data Sheet* listed under the <u>Doc/Files</u> tab on the CFAF800480A-050T-TS web page.

The TFT is connected to the CFA10058 by its FPC.







## \_\_\_\_\_

CFAL12832D-B



The optional <u>CFAL12832D-B</u> can be mounted onto the CFA10036 with its ZIF connector. This 128x32 OLED graphics module displays light (near-white) characters on a dark (near-black) background. Less than 0.5-inch high (11.5 mm), the OLED is useful for status messages and debugging.





The <u>CFA10040PWR</u> is a110 VAC +5v wall power supply that can be used to power the CFA10058 mother board. Cord length is ~63 inches.

WR-USB-Y03 Cable

**ABOUT ESD** 

The <u>WR-USB-Y03</u> is about 6-feet long. This cable has two different types of USB connectors, one smaller than the other. Connect the cable's smaller 2 mm female USB connector to the CFA10058's USB connector. Connect the cable's larger USB-A female connector to your host's USB-A connector.

## PHYSICAL CHARACTERISTICS

Specifications				
Overall Dimensions	TBD (W) x TBD(H) millimeters			
Weight	TBD grams			
Operating Temperature	-20°C to +70°C			
Humidity (RH noncondensing)	90%			
	<b>Y</b> A			

The circuitry is industry standard CMOS logic and is susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other static sensitive devices such as expansion cards, motherboards, or integrated circuits. Ground your body, work surfaces, and equipment.



## **PIN TABLES**

### **120-PIN EXTENSION DEBUG CONNECTOR**

The <u>SAMTEC FSI-130-10-L-D-AD</u> connector can be used to connect to the CFA10058's expansion ports.

DBG1

DBG1			DBG1		
Connector	Pin	Signal	Connector	Pin	Signal
1	G06	P3.21	31	R08	P0.02
2	G07	P3.20	32	T08	P0.01
3	K08	P3.18	33	U08	P0.00
4	L07	P3.17	34	L08	P0.23
5	K07	P3.16	35	M08	P0.22
6	B14	HSADC0	36	N08	P.021
7	C14	LRADC6	37	N06	P0.20
8	D15	LRADC5	38	M09	P0.19
9	D13	LARDC4	39	M07	P0.18
10	D09	LRADC3	40	N09	P0.17
11	C08	LARDC2	41	N07	P0.16
12	C09	L RAC1	42	K06	P3.15
13	VDD +3.3V		43	L06	P3.14
14	GND		44	L05	P3.13
15	C15	LARDC0	45	M05	P3.12
16	(A11)	PSWITCH RAW	46	H07	P3.11
17	(A14)	RESET RAW	47	VDD_+3.3V	
18	F14	P4.20	48	GND	
19	B09	DEBUG	49	H06	P3.10
20	D14	JTAG TRST	50	F05	P3.09
21	E13	JTAG TDO	51	F06	P3.08
22	F12	JTAG TDI	52	L09	P0.28
23	E11	JTAG TCK	53	P07	P0.27
24	D12	JTAG TMS	54	P06	P0.26
25	U06	P0.06	55	P08	P0.25
26	R07	P0.05	56	R06	P0.24
27	T07	P0.04	57	N05	P1.31
28	U07	P0.03	58	N01	P1.30
29	DCDC BATTERY	1 0.00	59	M01	P1.29
30	GND		60	L01	P1.28
	0.15				



#### DBG2

DBG2				DBG2		
Connector	Pin	Signal		Connector	Pin	Signal
1	E02	P4.16	-	31	B02	P2.026
2	F02	P4.08		32	C02	P2.25
3	F01	P4.07	-	33	A02	P2.24
4	F04	P4.06		34	C04	P2.19
5	H02	P4.04		35	B03	P2.18
6	H01	P4.03		36	C03	P2.17
7	F04	P4.02		37	A03	P2.16
8	H04	P4.01		38	E01	P2.15
9	G04	P4.00		39	D01	P2.14
10	J03	P4.15		40	C01	P2.13
11	.104	P4 14	-	41	B01	P2.12
12	F03	P4 13	-	42	A06	P2.10
13	VDD +5V		-	43	D10	P2.09
14	GND		-	44	A04	P2.08
15	G02	P4 12	-	45	B04	P2.07
16	G01	P4 11		46	D05	P2.06
17	102	P4 10		47	VDD_+3.3V	
18	101	P4.09		48	GND	
10	E03	P1.05		49	C05	P2.05
20	105	P3.07		50	A05	P2.03
20	505 K05	P3.06		51	D06	P2.02
21	K04	P3.05		52	C06	P2.01
22	1.04	P3 04		53	B06	P2.00
23	107	P3 03		54	M06	P3.30
25	106	P3 02		55	D07	P3.27
20	H05	P3 01		56	E08	P3.26
20	G05	P3.00	-	57	D08	P3.25
28	D02	P2 27	-	58	C07	P3.24
20		1 2.21	-	59	E07	P3.23
20	GND		-	60	F07	P3.22

## **CARE AND HANDLING PRECAUTIONS**

For optimum operation of the CFA921-TS and to prolong its life, please follow the precautions described below.

#### <u>Note</u>

The care and handling precautions listed below apply to the *CFA10058* mother board and the *CFA10036* SOM in this assembled product.

For the *CFAF800480A-050T-TS* TFT, see the care and handling in the Data Sheet under the <u>Doc/Files</u> tab on the display's web page.



### **ESD (ELECTRO-STATIC DISCHARGE) SPECIFICATIONS**

The circuitry is industry standard CMOS logic and is susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other static sensitive devices such as expansion cards, motherboards, or integrated circuits. Ground your body, work surfaces, and equipment.

### **DESIGN AND MOUNTING**

- Do not disassemble or modify.
- Solder only to the I/O terminals.
- Do not reverse polarity to the power supply connections. Reversing polarity will immediately ruin the product.

## AVOID SHOCK, IMPACT, TORQUE, OR TENSION

- Do not expose to strong mechanical shock, impact, torque, or tension.
- Do not drop, toss, bend, or twist.
- Do not place weight or pressure on the product.

### **OPERATION**

- Your circuit should be designed to protect the product from ESD and power supply transients.
- Observe the operating temperature limitations: a minimum of 0°C to a maximum of 50°C noncondensing with minimal fluctuation. Operation outside of these limits may shorten life and/or harm display. Changes in temperature can result in changes in contrast.
  - At lower temperatures.
  - At higher temperature.
- Operate away from dust, moisture, and direct sunlight.

### **STORAGE AND RECYCLING**

- Store in an ESD-approved container away from dust, moisture, and direct sunlight with humidity less than 90% noncondensing.
- Observe the storage temperature limitations: a minimum of -20°C minimum to +80°C noncondensing maximum with minimal fluctuations. Rapid temperature changes can cause moisture to form, resulting in permanent damage.
- Do not allow weight to be placed on the products while they are in storage.
- To discard, please recycle your products at an approved facility.



## **APPENDIX A: QUALITY ASSURANCE STANDARDS**

#### <u>Note</u>

The quality standards listed below apply to the *CFA10057* mother board and the *CFA10036* SOM in this assembled product.

For the *CFAF480800T07-043T-TS* TFT, see the quality assurance standards in the Data Sheet under the <u>Doc/Files</u> tab on the display's web page.

#### **INSPECTION CONDITIONS**

- Environment
  - Temperature: 25±5°C
  - Humidity: 30~85% RH

## ACCEPTANCE SAMPLING

DEFECT TYPE	6	AQL*
Major	<b>N</b> .	<u>&lt;</u> .65%
Minor		<1.0%
* Acceptable Quality Level: maximur	n allowable error r	ate or variation from standard

### **DEFECTS CLASSIFICATION**

Defects are defined as:

- A *major defect* is a defect that substantially reduces usability of unit for its intended purpose.
- A *minor defect*: is a defect that is unlikely to reduce usability for its intended purpose.



## **ACCEPTANCE STANDARDS**

#	DEFECT TYPE	ACCEPTANCE STANDARDS CRITERIA	MAJOR / MINOR
1	PCB defects	<ol> <li>Oxidation or contamination on connectors.*</li> <li>Wrong parts, missing parts, or parts not in specification.*</li> <li>Jumpers set incorrectly.</li> <li>Solder (if any) on bezel, LED pad, zebra pad, or screw hole pad is not smooth.</li> <li>*Minor if display functions correctly. Major if the display fails.</li> </ol>	Minor
2	Soldering defects	<ol> <li>Unmelted solder paste.</li> <li>Cold solder joints, missing solder connections, or oxidation.*</li> <li>Solder bridges causing short circuits.*</li> <li>Residue or solder balls.</li> <li>Solder flux is black or brown.</li> <li>*Minor if display functions correctly. Major if the display fails.</li> </ol>	Minor