



TFT DISPLAY MODULE DATASHEET



Datasheet Release Date 2017-11-27
for
CFAF240320A-032T

Crystalfontz America, Inc.

12412 East Saltese Avenue
Spokane Valley, WA 99216-0357
Phone: 888-206-9720
Fax: 509-892-1203
Email: support@crystalfontz.com
URL: www.crystalfontz.com

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1. General Information

Datasheet Revision History

Datasheet Release: **2017-11-27**
Datasheet for the CFAF240320A-032T TFT graphic display module.

Product Change Notifications

You can check for or subscribe to [Part Change Notices](#) for this display module on our website.

Variations

Slight variations between lots are normal (e.g., contrast, color, or intensity).

Volatility

This display module has volatile memory.

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2. Module Description

This is a 3.2-inch diagonal full color RGB TFT graphic display module with a white LED backlight. This display has a built-in Orise Tech OTM3225C controller.

Please see [Orise Tech OTM3225C LCD Controller Datasheet](#) for further reference.

3. Features

- 240*320 Dot Matrix
- +3.3V Power Supply
- Display Colors: 262K
- Built-in Controller: OTM3225C (or equivalent)
- Viewing Direction: 9 o'clock
- Operating Temperature: -20°C to +70°C
- Storage Temperature: -30°C to +80°C
- Interface: 16-Bit Parallel / SPI

4. Mechanical Data

Item	Specification (mm)	Specification (inch, reference)
Overall Module Dimension	57.04 (W) x 79.30 (H) x 3.85 (D)	2.246 (W) x 3.122 (H) x 0.152 (D)
Viewing Area	50.60 (W) x 71.26 (H)	1.992 (W) x 2.806 (H)
Active Area	48.60 (W) x 64.80 (H)	1.913 (W) x 2.551 (H)
Dot Pitch	0.21 (W) x 0.21 (H)	0.008 (W) x 0.008 (H)
Weight (Typical)	24 grams	0.85 ounces

6. Interface Pin Function

Pin	Symbol	I/O	Function					
			IM3	IM2	IM1	IM0	Interface Mode	DB Pin
1	IM1	I	0	0	0	0	Setting Invalid	-
			0	0	0	1	Setting Invalid	-
2	IM0		0	0	1	0	i80-system 16-bit	DB[17:10], DB[8:1]
			0	0	1	1	i80-system 8-bit	DB[17:10]
3	IM3		0	1	0	ID	SPI	SDI, SDO (DB[1:0])
			0	1	1	0	9-bit 3 wires SPI	SDA, SCL, nCS
4	IM2		0	1	1	1	8-bit 4 wires SPI	SDA, SCL, nCS, RS (D/CX)
			1	0	0	0	Setting Invalid	-
			1	0	0	1	Setting Invalid	-
			1	0	1	0	i80-system 18-bit	DB[17:0]
5	RESET	I	Reset Pin. Setting either pin low initializes the LSI. Must be reset after power is supplied.					
6	VSYNC	I	Frame Synchronizing Signal for RGB Interface Operation. Fix this pin at VCI or GND when not in use.					
7	HSYNC	I	Line Synchronizing Signal for RGB Interface Operation. Fix this pin at VCI or GND when not in use.					
8	DOTCLK	I	Dot Clock Signal for RGB Interface Operation. Fix this pin at VCI or GND when not in use.					
9	DEN	I	Data Enable Signal for RGB Interface Operation. Fix this pin at VCI or GND when not in use.					
10-27	DB17-DB0	I/O	Data Bus. Fix to GND level when not in use.					
28	RD	I	Serves as a read signal and MCU read data at the rising edge. Fix this pin at VCI or GND when not in use.					
29	WR/SCK	I	Write Strobe Signal in DBI Type B Operation					
30	RS	I	Display Data/Command Selection Pin					
31	SDO	I	SPI interface output pin. The data is output on the falling edge of the SCL signal. If not used, leave this pin open.					
32	SDI	I	Data lane in 1 data lane serial interface. The data is latched on the rising edge of the SCL signal.					
33	NC	-	No Connection					
34	CS	I	Chip Select Input Pin ("Low" enable). Fix this pin at VCI or GND when not in use.					
35	GND	P	Ground					
36	VCC	P	Supply Voltage (3.3v)					
37	LED-	P	Cathode Pin of Backlight					
38	LED+	P	Anode Pin of Backlight					
39	GND	P	Ground					
40	NC	-	No Connection					
41	NC	-	No Connection					
42	NC	-	No Connection					
43	NC	-	No Connection					
44	NC	-	No Connection					
45	NC	-	No Connection					

ESD (Electro-Static Discharge)

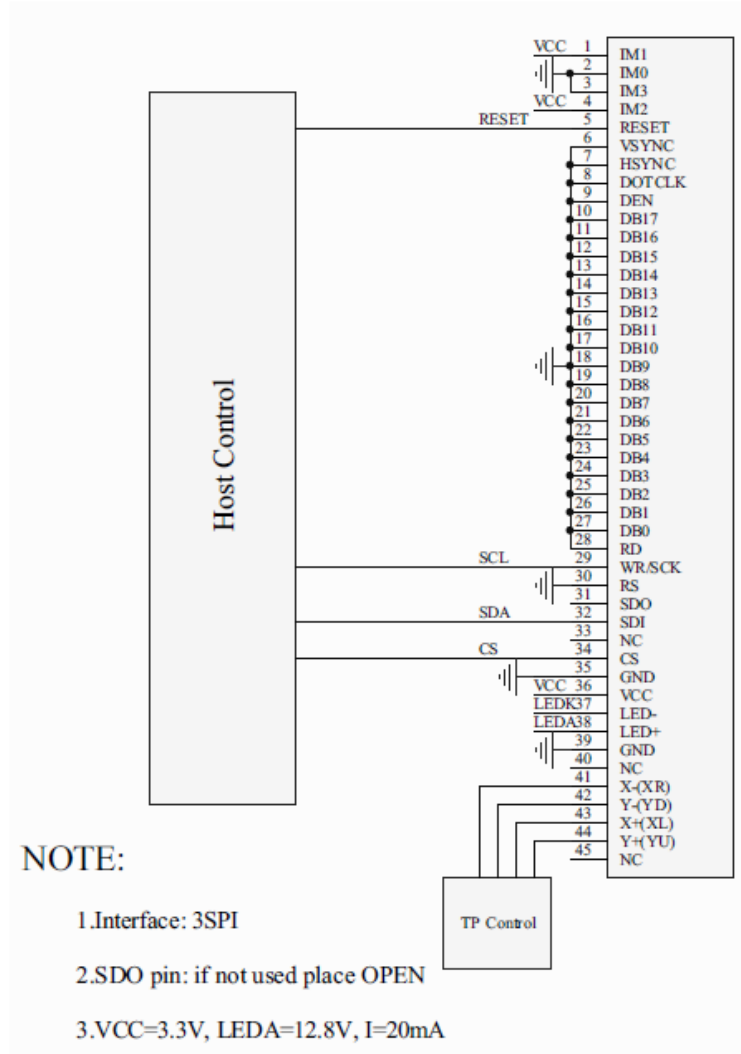
The circuitry is industry standard CMOS logic and is susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other static sensitive devices such as expansion cards, motherboards, or integrated circuits. Ground your body, work surfaces, and equipment.



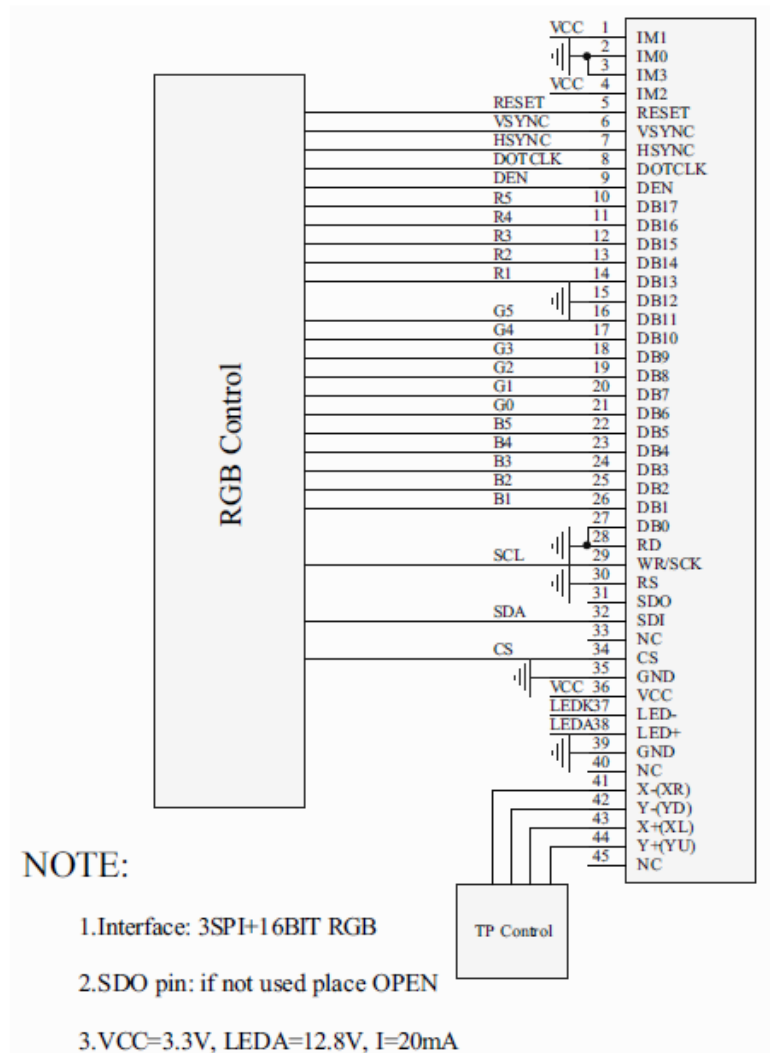
7. System Block Diagrams

Pin descriptions are shown for the both the touch screen and non-touch screen variations. For this variation, CFAF240320A-032T, pins 41-44 are NC/No Connection.

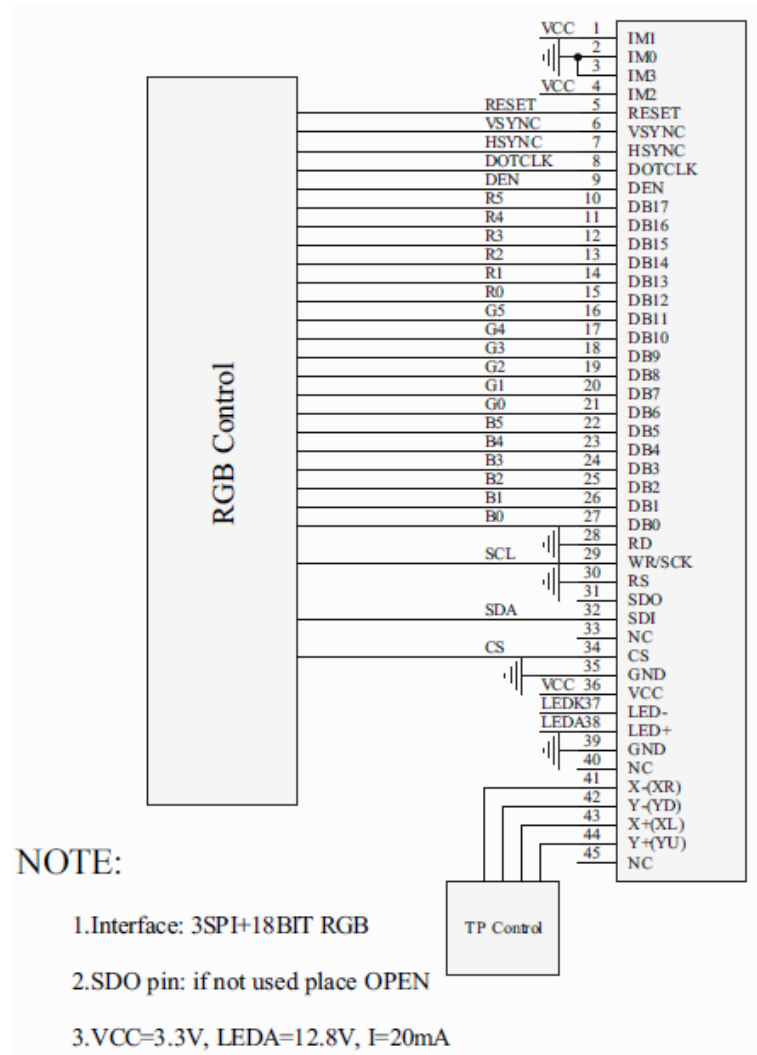
3-Wire SPI



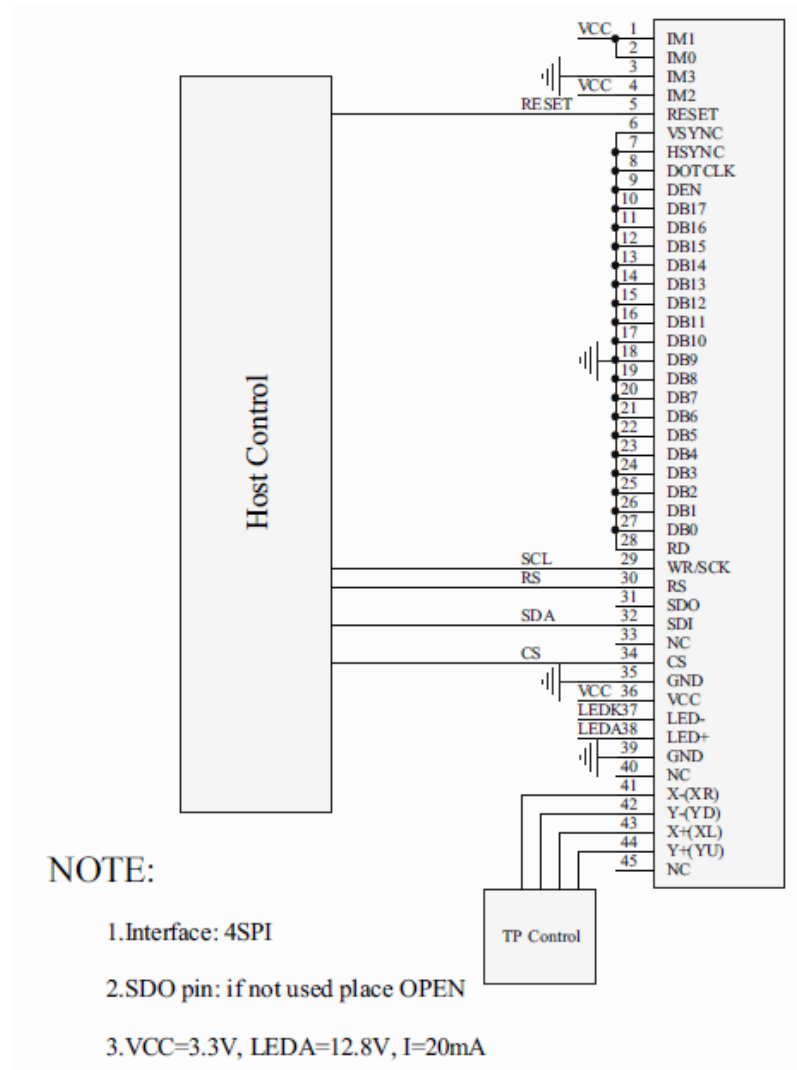
3-Wire SPI + 16-Bit RGB



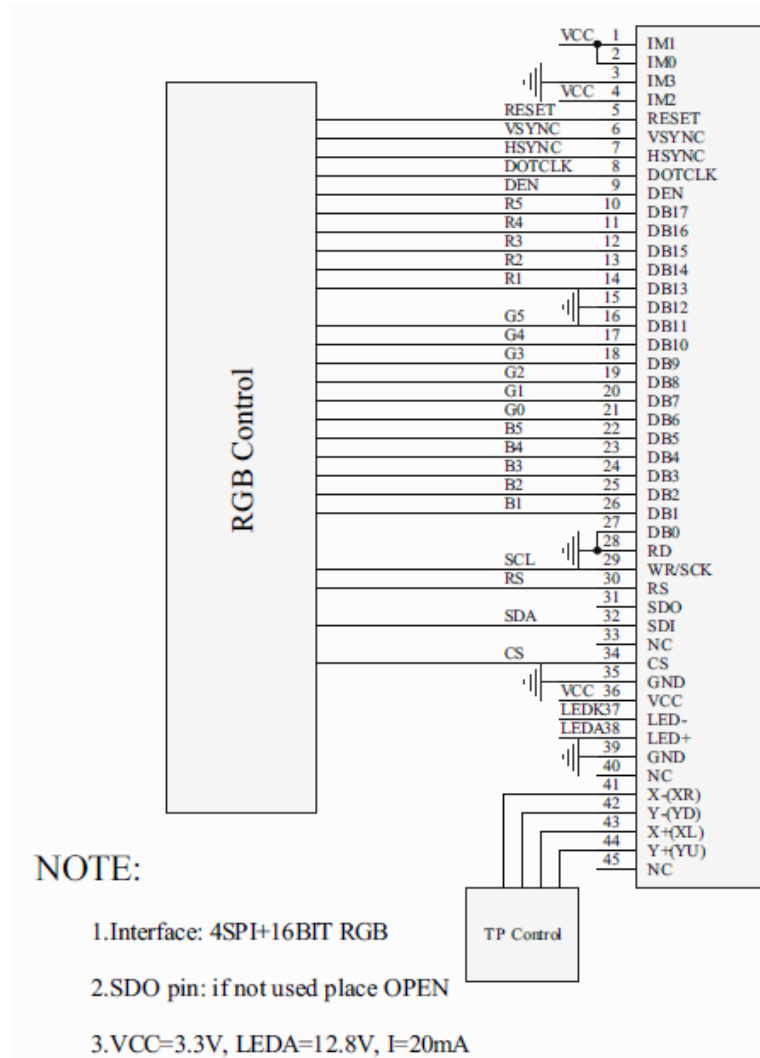
3-Wire SPI + 18-Bit RGB



4-Wire SPI

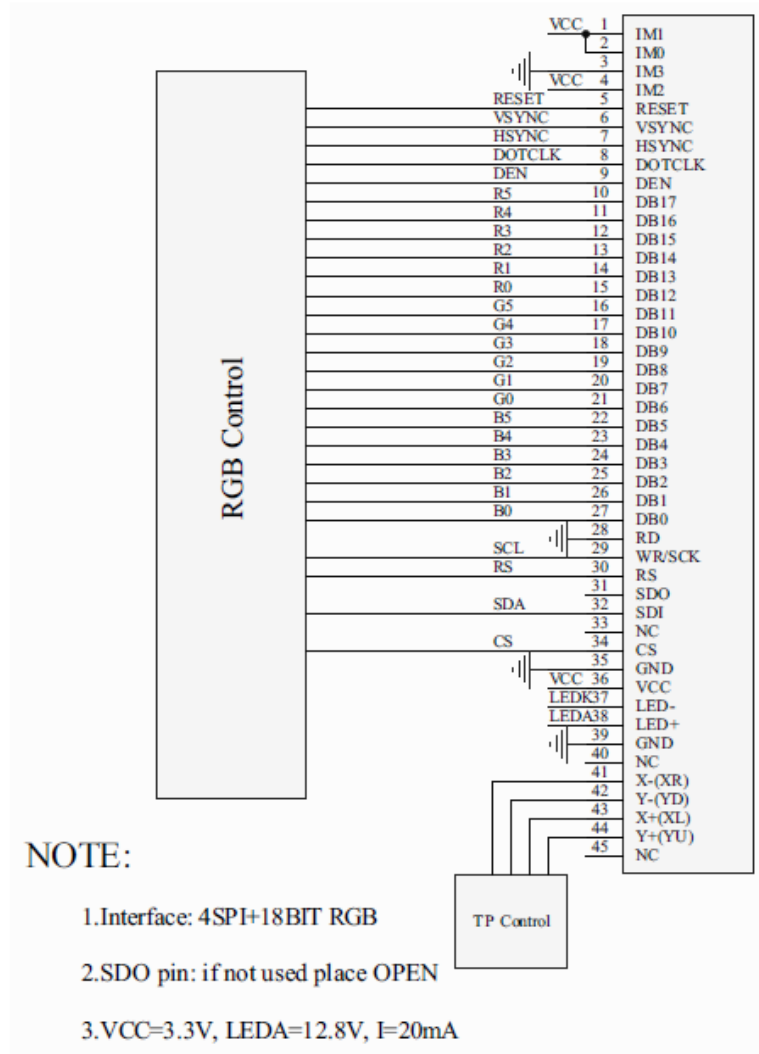


4-Wire SPI + 16-Bit RGB



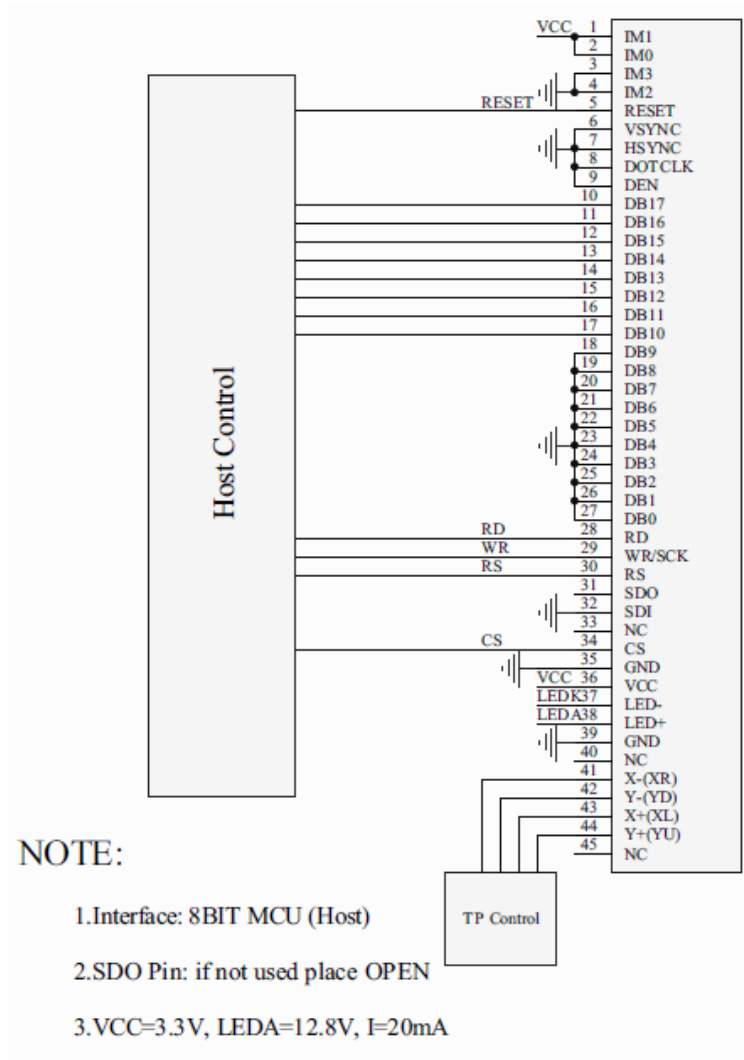


4-Wire SPI + 18-Bit RGB

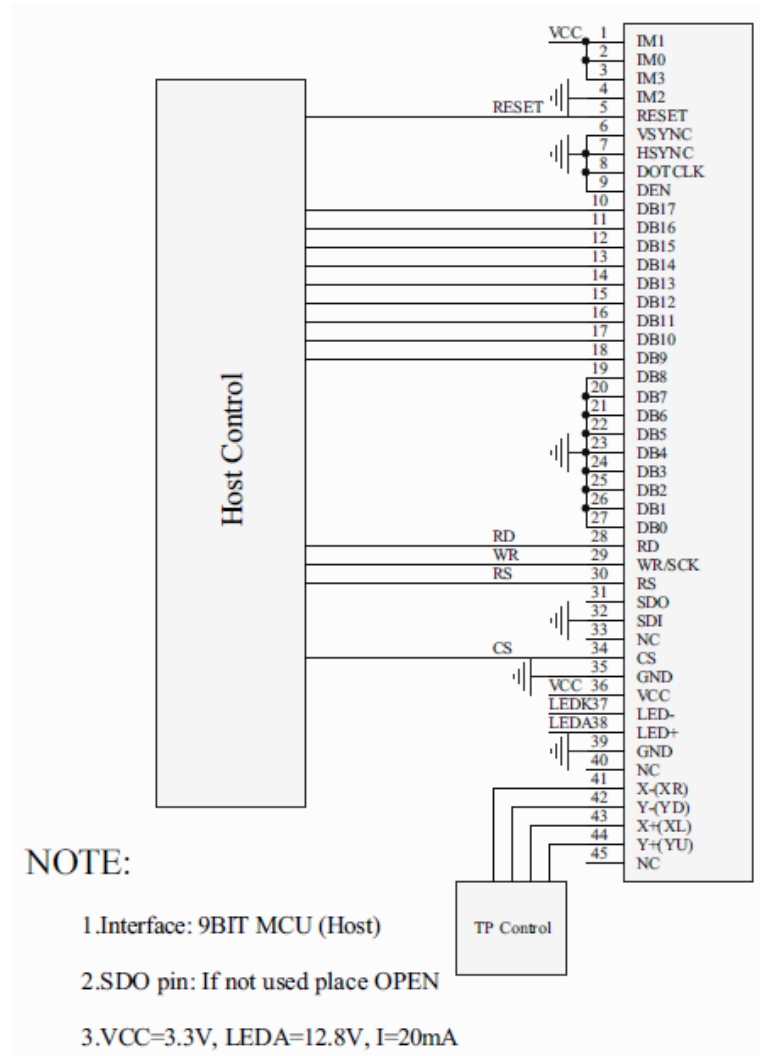




8-Bit Parallel

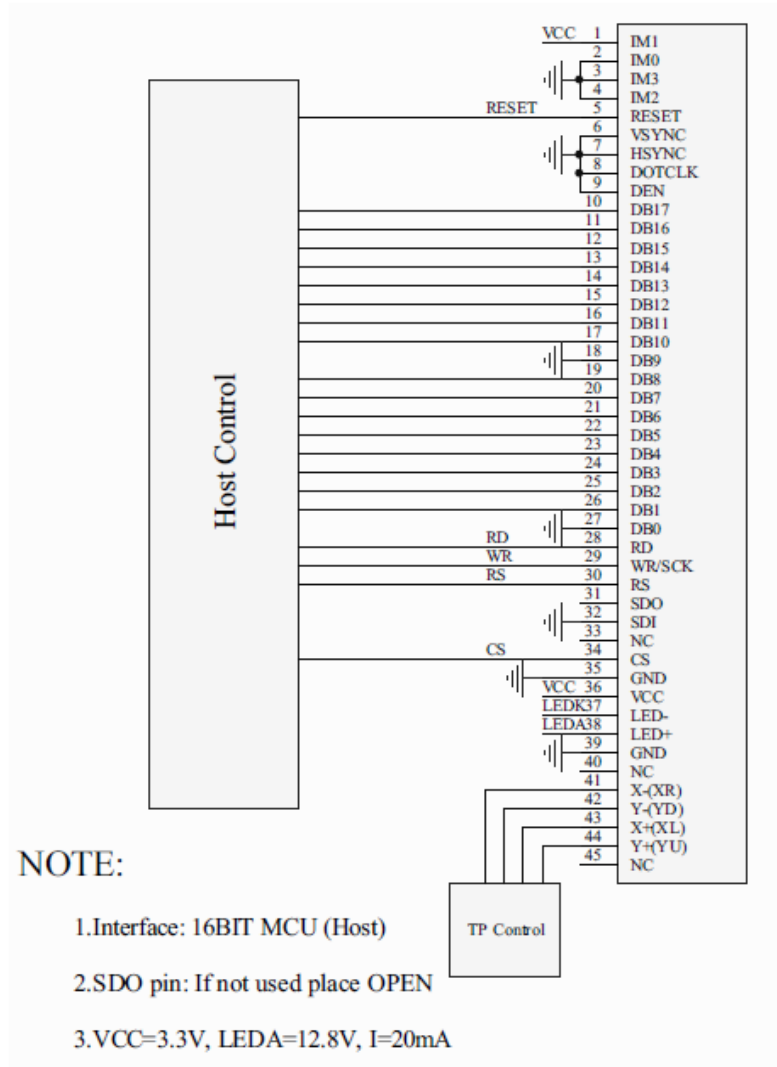


9-Bit Parallel

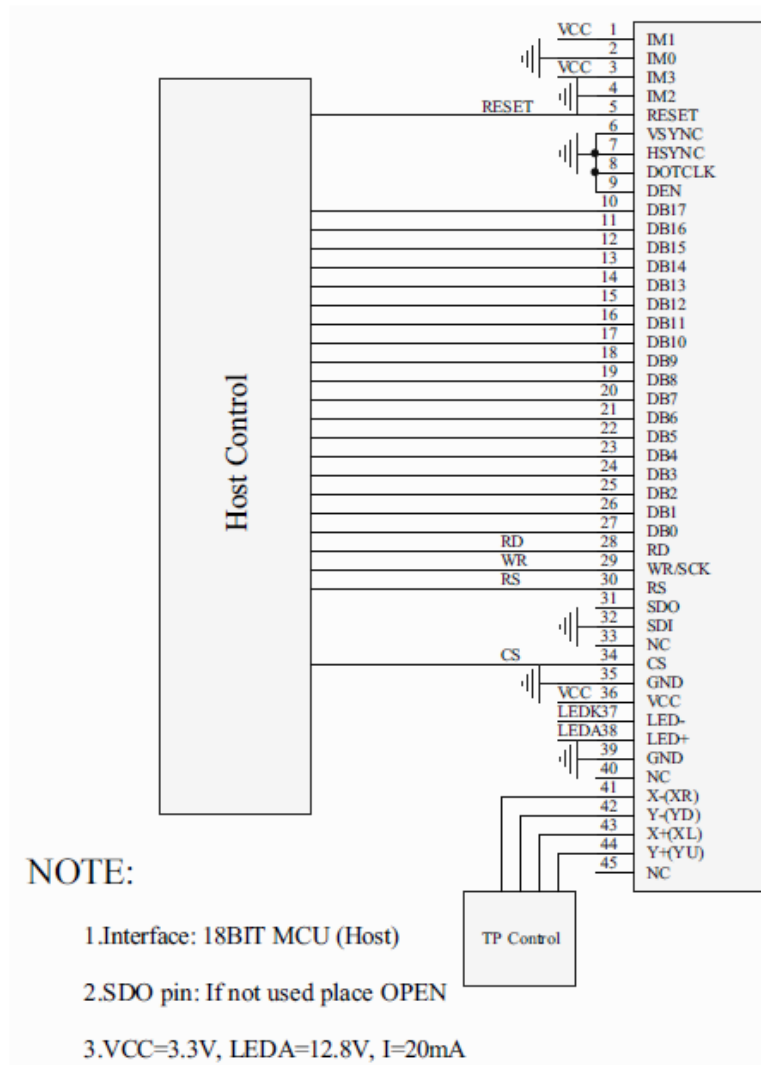




16-Bit Parallel



18-Bit Parallel



8. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	V _{CC}	-0.3	+3.3	V	(1)
Logic Signal Input Voltage	V _{IN}	-0.3	V _{CC} +0.3	V	(1)
Operating Temperature	T _{OP}	-20	+70	°C	(2)
Storage Temperature	T _{ST}	-30	+80	°C	(2)
Humidity	RH	0	90	%	(3)

Notes:

- (1) These are stress ratings only. Extended exposure to the absolute maximum ratings listed above may affect device reliability or cause permanent damage.
- (2) Temperature $\leq 60^{\circ}\text{C}$, 90% RH Maximum temperature $>60^{\circ}\text{C}$.
- (3) Absolute humidity should be less than 90%RH at 60°C .

9. Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage for Logic	V _{CC}	+2.7	+2.8	+3.3	V
Current Consumption	V _{OP}	-	8	-	mA
Input High Voltage	V _{IH}	+0.7v * V _{CCi} for V _{CC} =+2.8v V _{IH} =+0.7v x +2.8v=+1.96v	-	V _{CCi}	V
Input Low Voltage	V _{IL}	0v (V _{SS} /GND)	-	+0.3v * V _{CC} for V _{CC} =+2.8v V _{IL} =+0.3v x +2.8v=+0.84v	V
Output High Voltage	V _{OH}	+0.8v * V _{CCi} for V _{CC} =+2.8v V _{IH} =+0.7v x +2.8v=+2.24v	-	V _{DD}	V
Output Low Voltage	V _{OL}	0v (GND)	-	+0.2v * V _{CC} for V _{CC} =+2.8v V _{IL} =+0.3v x +2.8v=+0.56v	V

10. Backlight Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Notes
Forward Current	I _{LED}	-	15	20	mA	(1)
Forward Voltage	V _{LED}	+12.3	+12.8	+13.3	V	(1)
LED Lifetime	-	-	10K	-	Hr.	(2)

Notes:

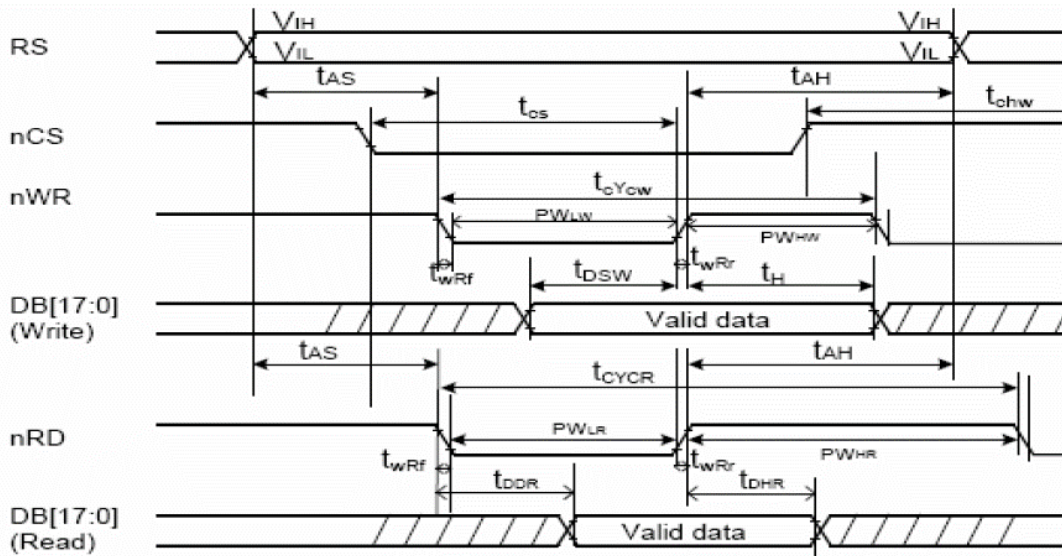
- (1) Forward current minimum value is only for reference since the LED brightness efficiency keeps enhancing. Current consumption becomes less and less to achieve the same luminance.
- (2) Lifetime is defined as the amount of time when the luminance has decayed to <50% of the initial value (10K hours is an estimate for reference only).

11. AC Characteristics

11.1. Display Parallel 18/16/9/8-Bit Interface Timing Characteristics (8080 System)

Normal Write Mode (IOVCC = 1.65~3.3V)

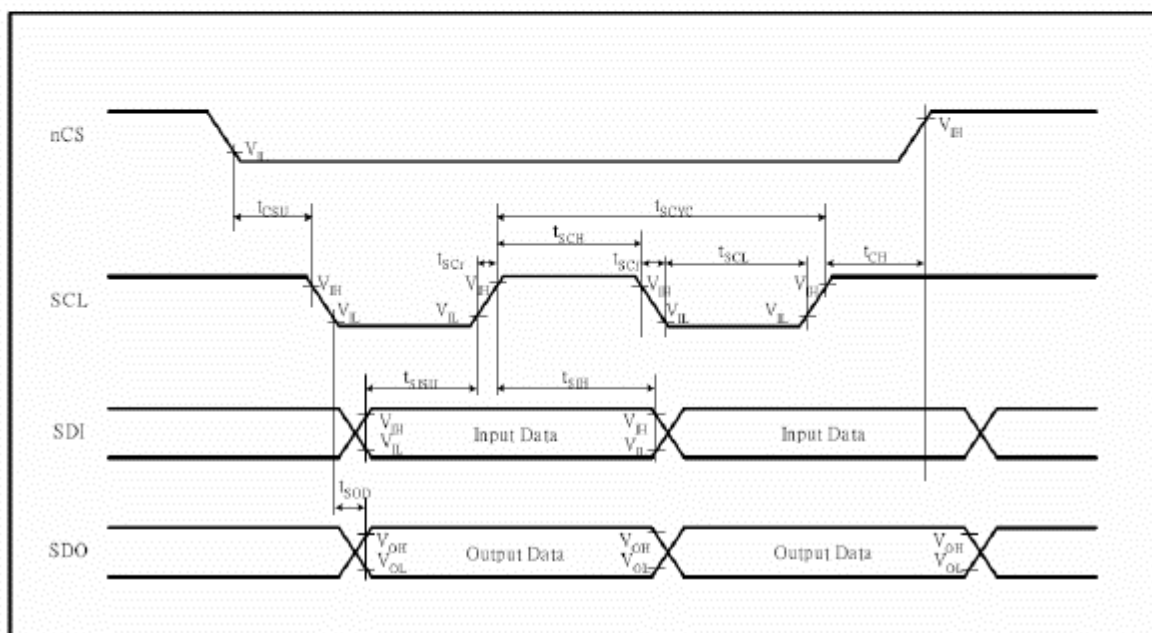
Item	Symbol	Unit	Min.	Typ.	Max.	Test Condition
Bus cycle time	Write	t_{CYCW}	ns	80	-	-
	Read	t_{CYCR}	ns	300	-	-
Write low-level pulse width	PW_{LW}	ns	50	-	500	-
Write high-level pulse width	PW_{HW}	ns	15	-	-	-
Read low-level pulse width	PW_{LR}	ns	150	-	-	-
Read high-level pulse width	PW_{HR}	ns	150	-	-	-
Write / Read rise / fall time	t_{WR}/t_{WR}	ns	-	-	25	-
Setup time	Write (RS to nCS, E/nWR)	t_{AS}	ns	10	-	-
	Read (RS to nCS, RW/nRD)			5	-	-
Address hold time	t_{AH}	ns	5	-	-	-
Write data set up time	t_{DSW}	ns	10	-	-	-
Write data hold time	t_H	ns	15	-	-	-
Read data delay time	t_{DDR}	ns	-	-	100	-
Read data hold time	t_{DHR}	ns	5	-	-	-



11.2. Serial Data Transfer Interface Timing Characteristics

(IOVCC= 1.65 ~ 3.3V)

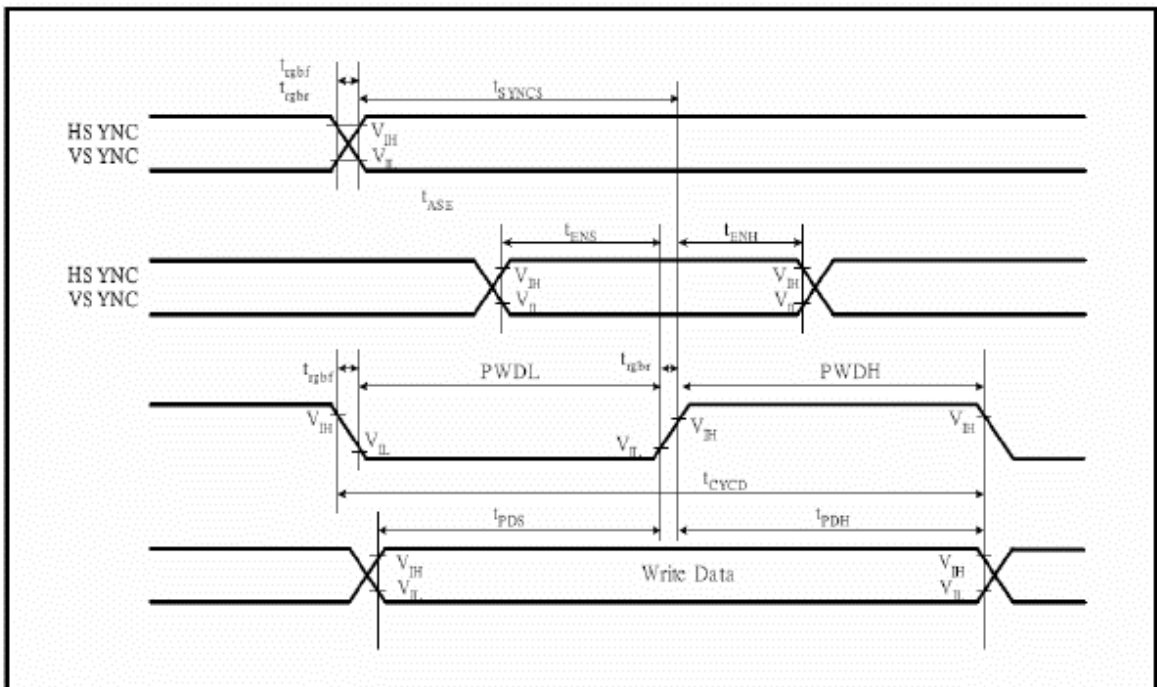
Item	Symbol	Unit	Min.	Typ.	Max.	Test Condition
Serial clock cycle time	Write (received)	t_{SCYC}	Ns	100	-	-
	Read (transmitted)	t_{SCYC}	ns	200	-	-
Serial clock high – level pulse width	Write (received)	t_{SCH}	ns	40	-	-
	Read (transmitted)	t_{SCH}	ns	100	-	-
Serial clock low – level pulse width	Write (received)	t_{SCL}	ns	40	-	-
	Read (transmitted)	t_{SCL}	ns	100	-	-
Serial clock rise / fall time	t_{SCr}, t_{SCf}	ns	-	-	5	
Chip select set up time	t_{CSU}	ns	10	-	-	
Chip select hold time	t_{CH}	ns	50	-	-	
Serial input data set up time	t_{SDU}	ns	20	-	-	
Serial input data hold time	t_{SDH}	ns	20	-	-	
Serial output data set up time	t_{SOD}	ns	-	-	100	
Serial output data hold time	t_{SOH}	ns	5	-	-	



11.3. RGB Interface Timing Characteristics

18/16-bit Bus RGB Interface Mode (IOVCC = 1.65 ~ 3.3V)

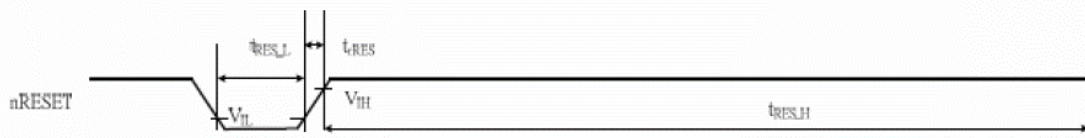
Item	Symbol	Unit	Min.	Typ.	Max.	Test Condition
VSYNC/HSYNC setup time	t_{VNSC}	ns	0	-	-	-
ENABLE setup time	t_{ENS}	ns	10	-	-	-
ENABLE hold time	t_{ENH}	ns	10	-	-	-
PD Data setup time	t_{PDS}	ns	10	-	-	-
PD Data hold time	t_{PDH}	ns	40	-	-	-
DOTCLK high-level pulse width	PWDH	ns	40	-	-	-
DOTCLK low-level pulse width	PWDL	ns	40	-	-	-
DOTCLK cycle time	t_{CYCD}	ns	100	-	-	-
DOTCLK, VSYNC, HSYNC, rise/fall time	t_{qbr}, t_{qbf}	ns	-	-	25	-



11.4. Reset Timing

Reset Timing Characteristics (IOVCC = 1.65 ~ 3.3 V)

Item	Symbol	Unit	Min.	Typ.	Max.
Reset low-level width	t_{RESL}	ms	1	-	-
Reset rise time	t_{RES}	μ s	-	-	10
Reset high-level width	t_{RESH}	ms	50	-	-



12. Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit		
Red Chromaticity	Rx	$\theta=\varphi-0^\circ$	0.621	0.641	0.661	ms		
	Ry		0.327	0.347	0.367			
Green Chromaticity	Gx		0.284	0.304	0.324			
	Gy		0.553	0.573	0.593			
Blue Chromaticity	Bx		0.115	0.135	0.155			
	By		0.101	0.121	0.141			
White Chromaticity	Wx		0.292	0.312	0.332			
	Wy		0.321	0.341	0.361			
Viewing Direction	9 o'clock							

13. LCD Module Precautions

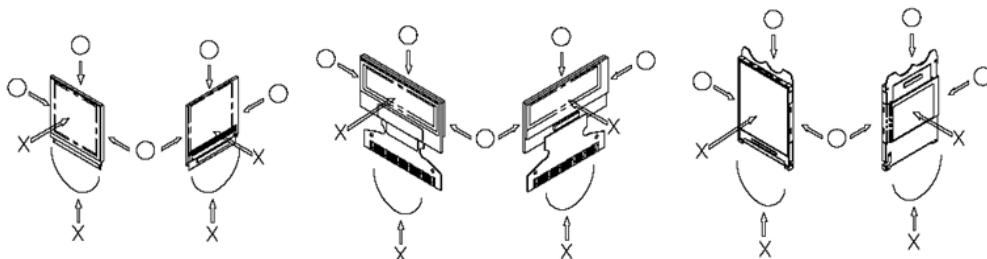
The precautions below should be followed when using LCD modules to help ensure personal safety, module performance, and compliance of environmental regulations.

13.1. Modules

- Avoid applying excessive shocks to module or making any alterations or modifications to it.
- Do not make extra holes on the printed circuit board, modify its shape or change the components of LCD display module.
- Do not disassemble the LCD display module.
- Do not operate the LCD display module above the absolute maximum rating.
- Do not drop, bend or twist the LCD display module.
- Soldering: only to the I/O terminals.
- Store in an anti-static electricity container and clean environment.
- It is common to use the "screen saver" to extend the lifetime of the LCD display module.
 - Do not use the fixed information for long periods of time in real application.
 - Do not use fixed information in LCD panel for long periods of time to extend "screen burn" effect time.
- Crystalfontz has the right to change the passive components, including R3, R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- Crystalfontz have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance, etc., under the premise of not affecting the electrical characteristics and external dimensions, Crystalfontz has the right to modify the version.)

13.2. Handling Precautions

- Since the display panel is made of glass, do not apply mechanical impacts such as dropping from a high position.
- If the display panel is accidentally broken, and the internal organic substance leaks out, be careful not to inhale or touch the organic substance.
- If pressure is applied to the display surface or its neighborhood of the LCD display module, the cell structure may be damaged, so be careful not to apply pressure to these sections.
- The polarizer covering the surface of the LCD display module is soft and can be easily scratched. Please be careful when handling the LCD display module.
- Clean the surface of the polarizer covering the LCD display module if it becomes soiled using following adhesion tape.
 - Scotch Mending Tape No. 810 or an equivalent
 - Never breathe the soiled surface or wipe the surface using a cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.
 - The following liquids/solvents may spoil the polarizer:
 - Water
 - Ketone
 - Aromatic Solvents
- Hold the LCD display module very carefully when placing the LCD display module into the system housing.
- Do not apply excessive stress or pressure to the LCD display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, be sure to secure the sufficient rigidity for the outer cases.





- Do not apply stress to the LSI chips and the surrounding molded sections.
- Do not disassemble or modify the LCD display module.
- Do not apply input signals while the logic power is off.
- Pay sufficient attention to the working environments when handing the LCD display module to prevent occurrence of element breakage accidents by static electricity.
 - Be sure to make human body grounding when handling LCD display modules.
 - Be sure to ground tools to use for assembly such as soldering irons.
 - To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
 - Protective film is being applied to the surface of the display panel of the LCD display module. Be careful since static electricity may be generated when exfoliating the protective film.
- Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the LCD display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after the film has been removed. In such a case, remove the residue material by the method discussed above.
- If electric current is applied when the LCD display module is being dewed or when it is placed under high humidity environments, the electrodes may become corroded. If this happens proceed with caution when handling the LCD display module.

13.3. Storage Precautions

- When storing the LCD display modules put them in static electricity preventive bags to avoid exposure to direct sunlight and fluorescent lamps. Also avoid high temperature and high humidity environments and low temperatures (less than 0°C) environments. (We recommend you store these modules in the packaged state when they were shipped from Crystalfontz). Be careful not to let water drops adhere to the packages or bags, and do not let dew gather on them.
- If electric current is applied when water drops are adhering to the surface of the LCD display module the LCD display module may have become dewed. If a dewed LCD display module is placed under high humidity environments it may cause the electrodes to become corroded. If this happens proceed with caution when handling the LCD display module.

13.4. Designing Precautions

- The absolute maximum ratings are the ratings that cannot be exceeded for LCD display module. If these values are exceeded, panel damage may happen.
- To prevent occurrence of malfunctioning by noise pay attention to satisfy the V_{IL} and V_{IH} specifications and, at the same time, to make the signal line cable as short as possible.
- We recommend that you install excess current preventive unit (fuses, etc.) to the power circuit (V_{DD}). (Recommend value: 0.5A)
- Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- As for EMI, take necessary measures on the equipment side.
- When fastening the LCD display module, fasten the external plastic housing section.
- If the power supply to the LCD display module is forcibly shut down, by such errors as taking out the main battery while the LCD display panel is in operation, we cannot guarantee the quality of this LCD display module.
 - Connection (contact) to any other potential than the above may lead to rupture of the IC.

13.5. Precautions When Disposing of the LCD Display Modules

- Request the qualified companies to handle the industrial wastes when disposing of the LCD display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

13.6. Other Precautions

- When an LCD display module is operated for a long period of time with a fixed pattern, the fixed pattern may remain as an after image or a slight contrast deviation may occur.
 - If the operation is interrupted and left unused for a while, normal state can be restored.
 - This will not cause a problem in the reliability of the module.



- To protect the LCD display module from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the LCD display modules.
 - Pins and electrodes
 - Pattern layouts such as the TCP & FPC
- With this LCD display module, the LCD driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this LCD driver is exposed to light, malfunctioning may occur.
 - Design the product and installation method so that the LCD driver may be shielded from light in actual usage.
 - Design the product and installation method so that the LCD driver may be shielded from light during the inspection processes.
- Although this LCD display module stores the operation state data by the commands and the indication data, when excessive external noise, etc. enters into the module, the internal status may be changed. Therefore, it is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
- We recommend that you construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data), to cope with catastrophic noise.
- Resistors, capacitors, and other passive components will have different appearance and color caused by the different supplier.
- Crystalfontz has the right to upgrade and modify the product function.
- The limitation of FPC bending:

