

CFAF240400D-030T

3.0" 400x240 Color TFT

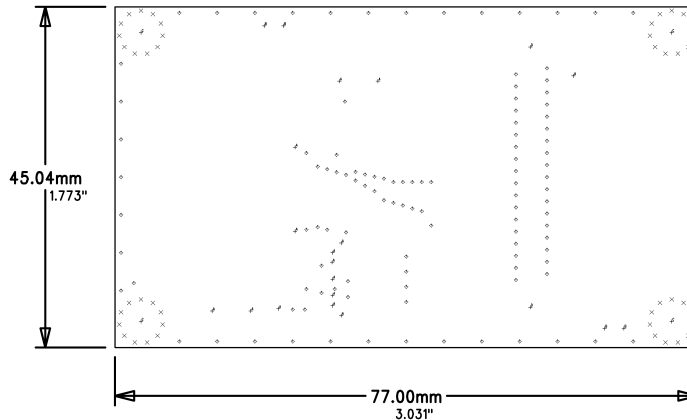
LCD1

6C	5C	0C	52	02	51	01	5	1
39	35	30	25	20	15	10	5	1

LAYERS: 2
 MATERIAL: FR4, RoHS, 94v0
 THICKNESS: 1.6mm (62.99 mils)
 MINIMUM SPACING: 0.1778 mm (7 mils)
 MINIMUM TRACE: 0.1778 mm (7 mils)
 SMALLEST VIA: 0.3048 mm drill in 0.6 mm pad
 (12 mil drill in 23.6 mil pad)
 FINISH: Gold (ENIG)
 SILK SCREEN: White
 SOLDER MASK: Green


>> IMPORTANT FABRICATION NOTES <<

- 1) Many pads have intentional undersize solder mask openings/oversize pads for physical strength.
- 2) Make top and bottom solder masks exactly as shown. Do NOT "open-up" the solder mask layers.
This design uses "solder-mask-defined-pads".



SIZE	QTY	SYM	PLATED	TOL
12	119	◇	YES	+/-0.0
15.75	44	×	YES	+/-0.0
20	15	⊕ ^A	YES	+/-0.0
39.37	4	⊕ ^B	YES	+/-0.0
157.48	4	⊕ ^C	YES	+/-0.0
43.31	2	⊕ ^D	NO	+/-0.0

BARE TRACE BEYOND EDGE IS INTENTIONAL
 ESD HANDLING FEATURE. ROUTING WILL
 TRIM COPPER TO EXACT BOARD OUTLINE.
 SEE PDF "BOARD PREVIEW" FOR EXAMPLE.

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	REVISION	DATE	
	0.0	2016 - 01 - 06	
	LAYER		FILE NAME
DRILL DRAWING		C-10075_v0p1_DD.pho	