



Crystalfontz America, Incorporated

CHARACTER LCD MODULE SPECIFICATIONS



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Hardware Version	Revision A
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REVISION HISTORY

HARDWARE	
Current hardware version: vA	

DATA SHEET	
2008/08/31	Current Data Sheet version: v2.0 Changes since last revision (v1.0): <ul style="list-style-type: none">● Improved drawings, tables, and text.● Added a photo with pins labeled. See Quick Reference for Pin Functions (Front & Back Photos) (Pg. 12).● Corrected error in Details of Interface Pin Functions (Pg. 11). Arrows for description of R/W signal were reversed.● Corrected Luminous Intensity in Backlight Characteristics. Was Minimum: 80 cd/m² Typical: 100 cd/m². Correction is Minimum: 135 cd/m² Typical: 195 cd/m².
2007/11/01	Data Sheet version: v1.0 New Data Sheet.

The Fine Print

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MAIN FEATURES

- ❑ 16 characters by 2 lines LCD has a large display area in a compact 85.0 (W) x 30.0 (H) x 13.2 (D) millimeter package (3.35" (W) x 1.18" (H) x 0.52" (D)).
- ❑ 4-bit or 8-bit parallel interface.
- ❑ Standard Hitachi HD44780 equivalent controller.
- ❑ Yellow-green array LED backlight with FSTN, negative, near-black, transmissive mode LCD (displays yellow-green characters on dark background).
- ❑ Wide temperature operation: -20°C to +70°C.
- ❑ RoHS compliant.

MODULE CLASSIFICATION INFORMATION




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❶	Brand	CrystalFontz America, Inc.
❷	Display Type	H – Character
❸	Number of Characters (Width)	16 Characters
❹	Number of Lines (Height)	2 Lines
❺	Model Identifier	D
❻	Backlight Type & Color	Y – LED, yellow-green
❼	Fluid Type, Image (Positive or Negative), & LCD Glass Color	T – FSTN, negative, near-black
❽	Polarizer Film Type, Wide (WT) Temperature Range, & Viewing Angle (O'clock)	I – Transmissive, WT, 6:00 ¹
❾	Character Set (CGROM)	E – English and European fonts
❿	Controller	T – Sitronix ST7066U
⓫	Special Codes	* – May have additional manufacturer's codes at this location.
¹ Note: For more information on Viewing Angle, see Definition of 6 O'Clock and 12:00 O'Clock Viewing Angles (Pg. 16) .		



ORDERING INFORMATION

PART NUMBER	FLUID	LCD GLASS COLOR	IMAGE	POLARIZER FILM	BACKLIGHT COLOR/TYPE
CFAH1602D-YTI-ET	FSTN	near-black	negative	transmissive	yellow-green LED 
<i>Additional variants (same form factor, different LCD mode or backlight):</i>					
CFAH1602D-TMI-ET	STN	blue	negative	transmissive	white LED 
CFAH1602D-YYH-ET	STN	yellow-green	positive	transflective	yellow-green LED 

MECHANICAL SPECIFICATIONS

PHYSICAL CHARACTERISTICS

ITEM	SIZE
Number of Characters and Lines	16 Characters x 2 Lines
Module Dimensions	85.0 (W) x 30.0 (H) x 13.2 (D) mm
Viewing Area	66.0 (W) x 16.0 (H) mm
Active Area	56.2 (W) x 11.5 (H) mm
Character Size	2.95 (W) x 5.55 (H) mm
Character Pitch	3.55 (W) x 5.95 (H) mm
Dot Size	0.55 (W) x 0.65 (H) mm
Dot Pitch	0.60 (W) x 0.70 (H) mm
Weight	31 grams (typical)



ELECTRICAL SPECIFICATIONS

SYSTEM BLOCK DIAGRAM

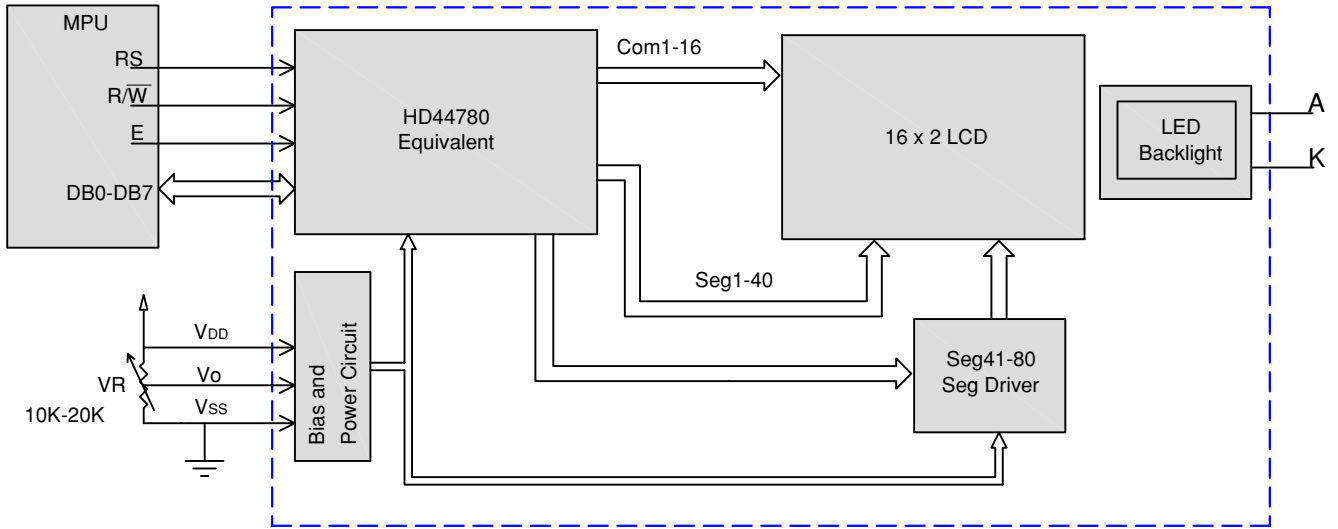


Figure 2. System Block Diagram



DRIVING METHOD

DRIVING METHOD	SPECIFICATION
Duty	1/16
Bias	1/5

ABSOLUTE MAXIMUM RATINGS

ABSOLUTE MAXIMUM RATINGS	SYMBOL	MINIMUM	MAXIMUM
Operating Temperature*	T _{OP}	-20°C	+70°C
Storage Temperature*	T _{ST}	-30°C	+80°C
Input Voltage	V _I	V _{SS}	V _{DD}
Supply Voltage for Logic	V _{DD} - V _{SS}	-0.3v	+7v
Supply Voltage for LCD	V _{DD} - V _O	-0.3v	+13v

**Note: Prolonged exposure at temperatures outside of this range may cause permanent damage to the module.*



DC CHARACTERISTICS (5V AND 3.3V OPERATION)

5V OPERATION							
PART	DC CHARACTERISTICS (4.5 to 5.5 volts)	TEST CONDITION	SYMBOL	MINIMUM	TYPICAL	MAXIMUM	NOTES
Controller and Board	Supply Voltage for Logic		$V_{DD} - V_{SS}$	+4.5v	+5.0v	+5.5v	
	Input High Voltage	$V_{DD} = 5V$	V_{IH}	+3.5v		V_{DD}	Pins: E, RS, R/W, DB0 - DB7
	Input Low Voltage		V_{IL}			+0.6v	
	Output High Voltage	$V_{DD} = 5V$	V_{OH}	+3.7v			$I_{OH} = -0.1\text{ mA}$ Pins: DB0 - DB7
	Output Low Voltage		V_{OL}			+0.4v	$I_{OL} = 0.1\text{ mA}$ Pins: DB0 - DB7
	Supply Current	without backlight	I_{DD}		1.2 mA		
LCD Glass	Supply Voltage for Driving LCD	TA = -20°C	$V_{DD} - V_O$			+4.2v	
		TA = +25°C			+3.8v		
		TA = +70°C		+3.6v			
<p><i>This is a summary of the module's major operating parameters. For detailed information, see APPENDIX C: SITRONIX ST7066U CONTROLLER SPECIFICATION SHEET (Pg. 29).</i></p>							



3.3V OPERATION							
PART	DC CHARACTERISTICS (2.7 to 4.5 volts)	TEST CONDITION	SYMBOL	MINIMUM	TYPICAL	MAXIMUM	NOTES
Controller and Board	Supply Voltage for Logic		$V_{DD} - V_{SS}$	+2.7v	+3.3v	+4.5v	
	Input High Voltage	$V_{DD} = 3.3V$	V_{IH}	+2.3v		V_{DD}	Pins: E, RS, $\overline{R/W}$, DB0 - DB7
	Input Low Voltage		V_{IL}			+0.6v	
	Output High Voltage	$V_{DD} = 3.3V$	V_{OH}	+2.4v			$I_{OH} = -0.1 \text{ mA}$ Pins: DB0 - DB7
	Output Low Voltage		V_{OL}			+0.4v	$I_{OL} = 0.1 \text{ mA}$ Pins: DB0 - DB7
	Supply Current	without backlight	I_{DD}		1.2 mA		
LCD Glass	Supply Voltage for Driving LCD	TA = -20°C	$V_{DD} - V_O$			+4.2v	
		TA = +25°C			+3.8v		
		TA = +70°C		+3.6v			
<p><i>This is a summary of the module's major operating parameters. For detailed information see APPENDIX C: SITRONIX ST7066U CONTROLLER SPECIFICATION SHEET (Pg. 29).</i></p> <p><i>For more information about 3.3v operation, please see APPENDIX B: APPLICATION NOTE FOR 3.3V OPERATION (Pg. 27).</i></p>							



DETAILS OF INTERFACE PIN FUNCTIONS

PIN	SIGNAL	LEVEL	DIRECTION	DESCRIPTION
1	V _{SS}	0v		Ground
2	V _{DD}	+5.0v		Supply voltage for logic
3	V _O	variable		Supply voltage for driving LCD is V _O = +1v typical at V _{DD} = +5v which gives a V _{LCD} = (V _{DD} - V _O) = +4v
4	RS	H/L	I	Register selection input. H: Data register (for read and write) L: Instruction code (for write)
5	R/W	H/L	I	H: Read (MPU←Module) L: Write (MPU→Module)
6	E	H,H→L	I	Read/write enable signal. H: Read data is enabled by a high level. H→L: Write data is latched on the falling edge.
7	DB0	H/L	I/O	Data bit 0
8	DB1	H/L	I/O	Data bit 1
9	DB2	H/L	I/O	Data bit 2
10	DB3	H/L	I/O	Data bit 3
11	DB4	H/L	I/O	Data bit 4
12	DB5	H/L	I/O	Data bit 5
13	DB6	H/L	I/O	Data bit 6
14	DB7	H/L	I/O	Data bit 7

For backlight connections, please refer to [LED Backlight Characteristics \(Pg. 17\)](#).



QUICK REFERENCE FOR PIN FUNCTIONS (FRONT & BACK PHOTOS)

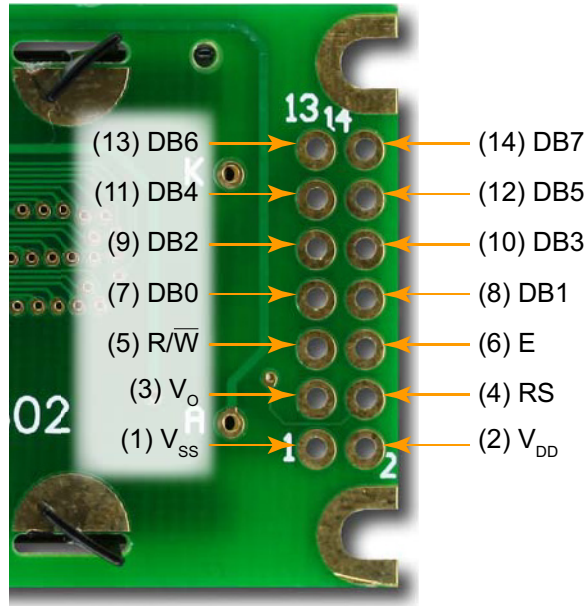


Figure 3. Back View of Pins (Labeled)

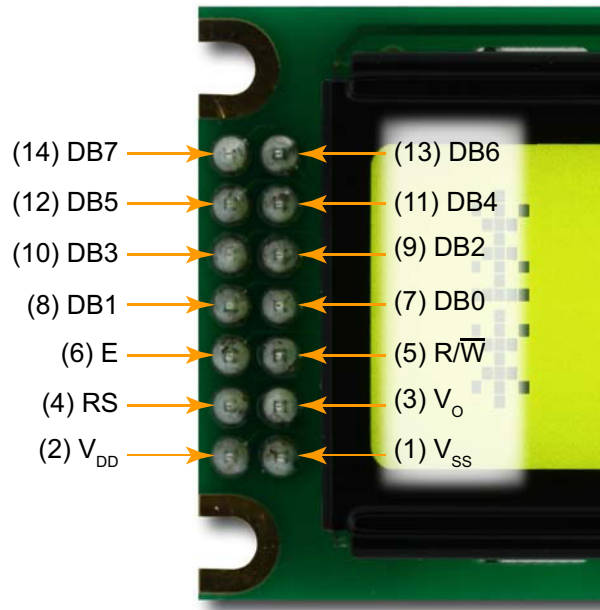


Figure 4. Front View of Pins (Labeled)



TYPICAL V_O CONNECTIONS FOR DISPLAY CONTRAST

Adjust V_O to +1v ($V_{LCD} = +4v$) as an initial setting. When the module is operational, readjust V_O for optimal display appearance.

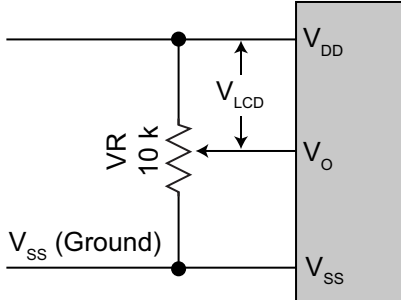


Figure 5. Typical V_O Connections

We recommend allowing field adjustment of V_O for all designs. The optimal value for V_O will change with temperature, variations in V_{DD} , and viewing angle. V_O will also vary module-to-module and batch-to-batch due to normal manufacturing variations.

Ideally, adjustments to V_O should be available to the end user so each user can adjust the display to the optimal contrast for their required viewing conditions. At a minimum, your design should allow V_O to be adjusted as part of your product's final test.

Although a potentiometer is shown as a typical connection, V_O can be driven by your microcontroller, either by using a DAC or a filtered PWM. Displays that require V_O to be negative may need a level-shifting circuit. Please do not hesitate to contact CrystalFontz application support for design assistance on your application.

ESD (ELECTRO-STATIC DISCHARGE) SPECIFICATIONS

This circuitry is industry standard CMOS logic and is susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other PCB such as expansion cards or motherboards. For more information, see [CARE AND HANDLING PRECAUTIONS \(Pg. 22\)](#).



OPTICAL SPECIFICATIONS

OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MINIMUM	TYPICAL	MAXIMUM
Viewing Angle (6 o'clock) (Vertical, Horizontal)	(V) θ	CR \geq 2	30°		60°
	(H) ϕ	CR \geq 2	-40°		45°
Contrast Ratio	CR			5	
LCD Response Time*	T rise	Ta = 25°C		150 ms	200 ms
	T fall	Ta = 25°C		150 ms	200 ms
*Response Time: The amount of time it takes a liquid crystal cell to go from active to inactive or back again.					

OPTICAL DEFINITIONS

- Operating Voltage (V_{LCD}): V_{OP}
- Viewing Angle
 - Vertical (V) θ : 0°
 - Horizontal (H) ϕ : 0°
- Frame Frequency: 64 Hz
- Driving Waveform: 1/16 Duty, 1/5 Bias
- Ambient Temperature (Ta): 25°C



Definition of Operation Voltage (Vop)

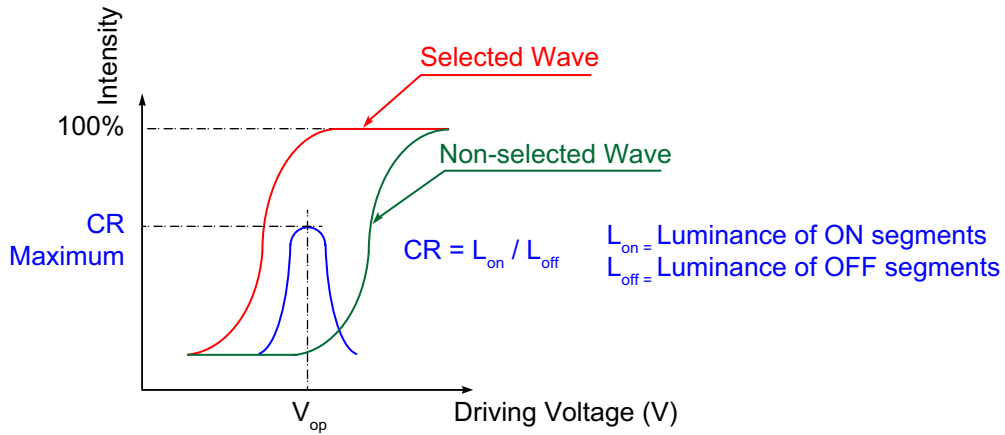


Figure 6. Definition of Operation Voltage (V_{OP}) (Negative)

Definition of Response Time (T_r , T_f)

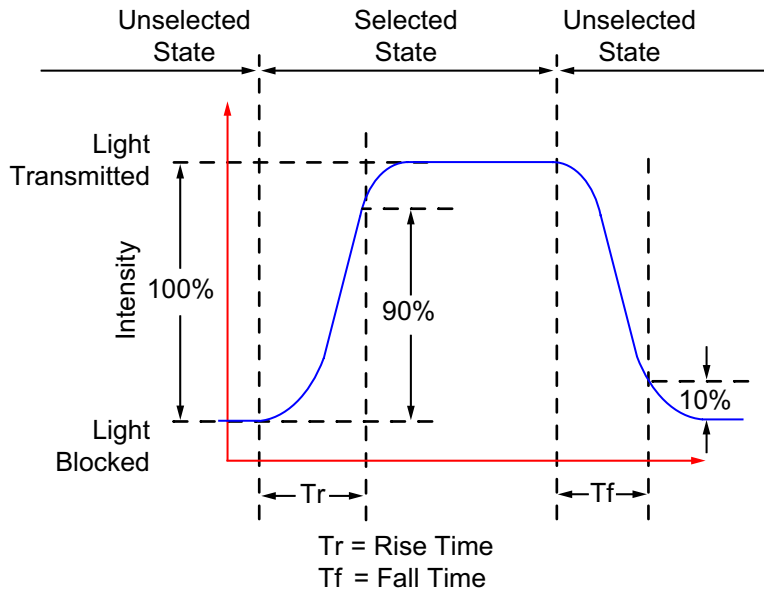


Figure 7. Definition of Response Time (T_r , T_f) (Negative)



Definition of Vertical and Horizontal Viewing Angles ($CR \geq 2$)

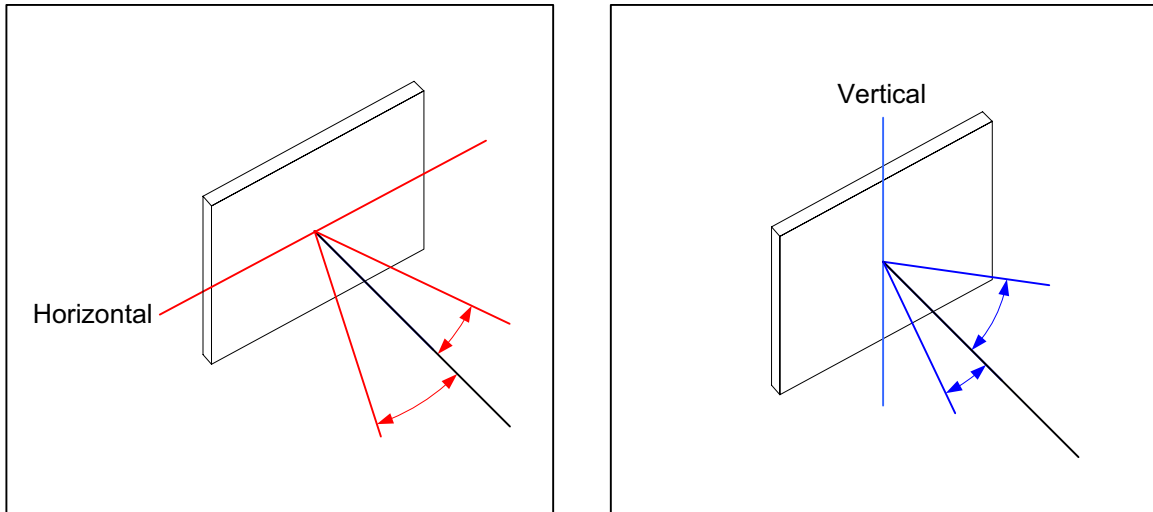


Figure 8. Definition of Horizontal and Vertical Viewing Angles ($CR > 2$)

Definition of 6 O'Clock and 12:00 O'Clock Viewing Angles

This module has a 6:00 o'clock viewing angle. A 6:00 o'clock viewing angle is a bottom viewing angle like what you would see when you look at a cell phone or calculator. A 12:00 o'clock viewing angle is a top viewing angle like what you would see when you look at the gauges in a golf cart or airplane.

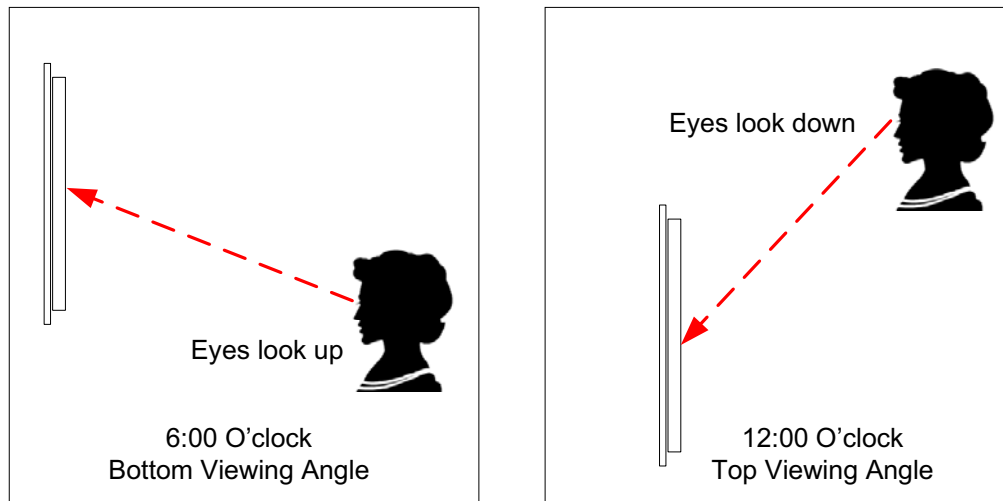


Figure 9. Definition of 6:00 O'Clock and 12:00 O'Clock Viewing Angles



LED BACKLIGHT CHARACTERISTICS

The CFAH1602D-YTI-ET uses an LED backlight. LED backlights are easy to use, but they are also easily damaged by abuse.

NOTE

Do not connect +5v directly to the backlight terminals. This will ruin the backlight.

LEDs are “current” devices. The important aspect of driving an LED is the current flowing through it, not the voltage across it. Ideally, a current source would be used to drive the LEDs. In practice, a simple current limiting resistor in line from a voltage source will work well in most applications and is much less complex than a current source.

You need to know what the forward voltage of the LEDs is so you can calculate the current limiting resistor (R_{LIMIT}). The forward voltage will vary slightly from display to display.

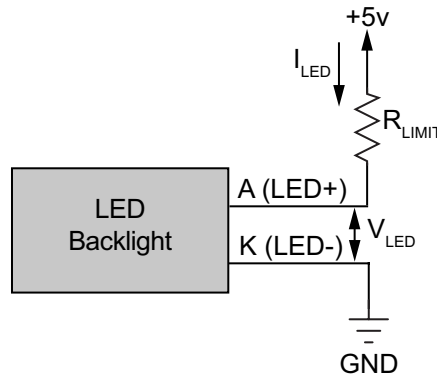


Figure 10. Typical LED Backlight Connections for “Always On”

The equation to calculate R_{LIMIT} is:

$$R_{LIMIT} \text{ (minimum)} = \frac{V_{DD} \text{ (Supply Voltage)} - V_{LED} \text{ (Typical LED Forward Voltage)}}{I_{LED} \text{ (Typical LED Forward Current)}}$$

The specific R_{LIMIT} calculation for the CFAH1602D-YTI-ET at $V_{DD} = +5v$ is:

$$R_{LIMIT} = \frac{5v - 4.2v}{0.13 \text{ A}} = 6.15\Omega \text{ (minimum)}$$



The backlight may be dimmed by PWM (Pulse Width Modulation). The typical range for the PWM frequency is from 100 to 300 Hz.

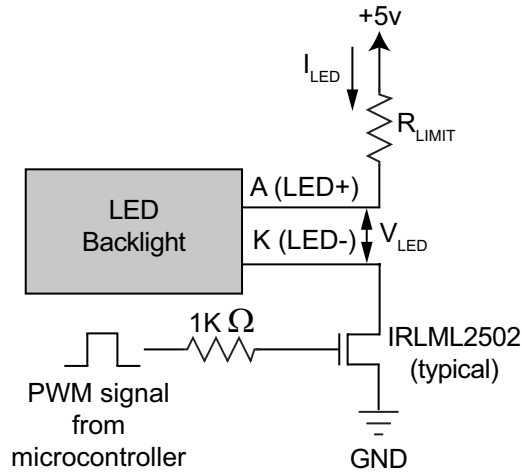


Figure 11. Example of LED Backlight Connections for PWM Dimming

Backlight Characteristics <i>Yellow-green dots on dark background</i>			
PARAMETER	MINIMUM	TYPICAL	MAXIMUM
Forward Current (I_{LED}) $V = 4.2v$	104 mA	130 mA	195 mA
Forward Voltage (V_{LED})	+4.0v	+4.2v	+4.4v
Reverse Voltage (V_R)			+8v
Luminous Intensity* (I_V) $I_{LED} = 130\text{ mA}$	135 cd/m ²	195 cd/m ²	
Wavelength* ($I \lambda$) $I_{LED} = 130\text{ mA}$		573 nm	
<i>*Direct measurement of backlight—the backlight is not measured through the LCD.</i>			



LCD CONTROLLER INTERFACE

This module uses a Sitronix ST7066U controller. The Sitronix ST7066U is compatible with the industry standard Hitachi HD44780 controller. Software written for modules that use the HD44780 should work without modification.

For your reference, we added [APPENDIX C: SITRONIX ST7066U CONTROLLER SPECIFICATION SHEET \(Pg. 29\)](#) to this Data Sheet.

DISPLAY POSITION DDRAM ADDRESS

The following table shows the relationship between the controller's addresses and the corresponding character location on the module.

		COLUMN															
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
ROW	0	0x00	0x01	0x02	0x03	0x04	0x05	0x06	0x07	0x08	0x09	0xA	0xB	0xC	0xD	0xE	0xF
	1	0x40	0x41	0x42	0x43	0x44	0x45	0x46	0x47	0x48	0x49	0x4A	0x4B	0x4C	0x4D	0x4E	0x4F



CHARACTER GENERATOR ROM (CGROM)

To find the code for a given character, add the two numbers that are shown in bold for its row and column. For example, the lowercase “h” is in the column labeled “96₁₀” and in the row labeled “8₁₀”. So you would add 96 + 8 to get 104. When you send a byte with the value of 104 to the display, then a lowercase “h” will be shown. (See [APPENDIX C: SITRONIX ST7066U CONTROLLER SPECIFICATION SHEET \(Pg. 29\)](#)).

upper 4 bits lower 4 bits	0 ₁₀ 0000 ₂	16 ₁₀ 0001 ₂	32 ₁₀ 0010 ₂	48 ₁₀ 0011 ₂	64 ₁₀ 0100 ₂	80 ₁₀ 0101 ₂	96 ₁₀ 0110 ₂	112 ₁₀ 0111 ₂	128 ₁₀ 1000 ₂	144 ₁₀ 1000 ₂	160 ₁₀ 1001 ₂	176 ₁₀ 1001 ₂	192 ₁₀ 1100 ₂	208 ₁₀ 1101 ₂	224 ₁₀ 1110 ₂	240 ₁₀ 1111 ₂
0 ₁₀ 0000 ₂	CGRAM [0]	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣	␣
1 ₁₀ 0001 ₂	CGRAM [1]	␣	!	1	A	Q	a	9	0	æ	ı	ı	J	T	Y	U
2 ₁₀ 0010 ₂	CGRAM [2]	␣	"	2	B	R	b	r	e	ŕ	ş	ş	*	ø	ø	ø
3 ₁₀ 0011 ₂	CGRAM [3]	␣	#	3	C	S	c	s	ç	ç	ç	ç	ç	ç	ç	ç
4 ₁₀ 0100 ₂	CGRAM [4]	␣	\$	4	D	T	d	t	đ	đ	đ	đ	đ	đ	đ	đ
5 ₁₀ 0101 ₂	CGRAM [5]	␣	%	5	E	U	e	u	ü	ü	ü	ü	ü	ü	ü	ü
6 ₁₀ 0110 ₂	CGRAM [6]	␣	&	6	F	V	f	v	ŧ	ŧ	ŧ	ŧ	ŧ	ŧ	ŧ	ŧ
7 ₁₀ 0111 ₂	CGRAM [7]	␣	'	7	G	W	g	w	ğ	ğ	ğ	ğ	ğ	ğ	ğ	ğ
8 ₁₀ 1000 ₂		␣	(8	H	X	h	x	ş	ş	ş	ş	ş	ş	ş	ş
9 ₁₀ 1001 ₂		␣)	9	I	Y	i	y	ı	ı	ı	ı	ı	ı	ı	ı
10 ₁₀ 1010 ₂		␣	*	0	J	Z	j	z	ç	ç	ç	ç	ç	ç	ç	ç
11 ₁₀ 1011 ₂		␣	+	1	K	0	k	0	ç	ç	ç	ç	ç	ç	ç	ç
12 ₁₀ 1100 ₂		␣	,	2	L	X	l	x	ç	ç	ç	ç	ç	ç	ç	ç
13 ₁₀ 1101 ₂		␣	-	3	M	I	m	i	ç	ç	ç	ç	ç	ç	ç	ç
14 ₁₀ 1110 ₂		␣	.	4	N	^	n	^	ç	ç	ç	ç	ç	ç	ç	ç
15 ₁₀ 1111 ₂		␣	/	5	O	_	o	_	ç	ç	ç	ç	ç	ç	ç	ç

Figure 12. Character Generator ROM (CGROM)



MODULE RELIABILITY AND LONGEVITY

MODULE RELIABILITY

ITEM	SPECIFICATION
LCD including yellow-green LED backlight	50,000 to 100,000 hours (typical)

MODULE LONGEVITY (EOL / REPLACEMENT POLICY)

Crystalfontz is committed to making all of our LCD modules available for as long as possible. For each module we introduce, we intend to offer it indefinitely. We do not preplan a module's obsolescence. The majority of modules we have introduced are still available.

We recognize that discontinuing a module may cause problems for some customers. However, rapidly changing technologies, component availability, or low customer order levels may force us to discontinue ("End of Life", EOL) a module. For example, we must occasionally discontinue a module when a supplier discontinues a component or a manufacturing process becomes obsolete. When we discontinue a module, we will do our best to find an acceptable replacement module with the same fit, form, and function.

In most situations, you will not notice a difference when comparing a "fit, form, and function" replacement module to the discontinued module it replaces. However, sometimes a change in component or process for the replacement module results in a slight variation, perhaps an improvement, over the previous design.

Although the replacement module is still within the stated Data Sheet specifications and tolerances of the discontinued module, changes may require modification to your circuit and/or firmware. Possible changes include:

- *LCD fluid, polarizers, or the LCD manufacturing process.* These items may change the appearance of the display, requiring an adjustment to V_O (See [Typical \$V_O\$ Connections for Display Contrast \(Pg. 13\)](#)).
- *Backlight LEDs.* Brightness may be affected (perhaps the new LEDs have better efficiency) or the current they draw may change (new LEDs may have a different VF).
- *Controller.* A new controller may require minor changes in your code.
- *Component tolerances.* Module components have manufacturing tolerances. In extreme cases, the tolerance stack can change the visual or operating characteristics.

Please understand that we avoid changing a module whenever possible; we only discontinue a module if we have no other option. We will post Part Change Notices on the product's webpage as soon as possible. If interested, you can subscribe to future part change notifications.



CARE AND HANDLING PRECAUTIONS

For optimum operation of the module and to prolong its life, please follow the precautions below.

ESD (ELECTRO-STATIC DISCHARGE)

The circuitry is industry standard CMOS logic and susceptible to ESD damage. Please use industry standard antistatic precautions as you would for any other PCB such as expansion cards or motherboards. Ground your body, work surfaces, and equipment.

DESIGN AND MOUNTING

- The exposed surface of the LCD “glass” is actually a polarizer laminated on top of the glass. To protect the soft plastic polarizer from damage, the module ships with a protective film over the polarizer. Please peel off the protective film slowly. Peeling off the protective film abruptly may generate static electricity.
- The polarizer is made out of soft plastic and is easily scratched or damaged. When handling the module, avoid touching the polarizer. Finger oils are difficult to remove.
- To protect the soft plastic polarizer from damage, place a transparent plate (for example, acrylic, polycarbonate, or glass) in front of the module, leaving a small gap between the plate and the display surface. We use GE HP-92 Lexan, which is readily available and works well.
- Do not disassemble or modify the module.
- Do not modify the tab of the metal holder or make connections to it.
- Solder only to the I/O terminals. Use care when removing solder—it is possible to damage the PCB.
- Do not reverse polarity to the power supply connections. Reversing polarity will immediately ruin the module.

AVOID SHOCK, IMPACT, TORQUE, AND TENSION

- Do not expose the module to strong mechanical shock, impact, torque, and tension.
- Do not drop, toss, bend, or twist the module.
- Do not place weight or pressure on the module.

IF LCD PANEL BREAKS

- If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or eyes.
- If the liquid crystal fluid touches your skin, clothes, or work surface, wash it off immediately using soap and plenty of water.
- Do not eat the LCD panel.

CLEANING

The polarizer (laminated to the glass) is soft plastic. The soft plastic is easily scratched or damaged. Be very careful when you clean the polarizer.

- Do not clean the polarizer with liquids. Do not wipe the polarizer with any type of cloth or swab (for example, Q-tips).
- Use the removable protective film to remove smudges (for example, fingerprints) and any foreign matter. If you no longer have the protective film, use standard transparent office tape (for example, Scotch® brand “Crystal Clear Tape”). If the polarizer is dusty, you may carefully blow it off with clean, dry, oil-free compressed air.



OPERATION

- We do not recommend connecting this module to a PC's parallel port as an "end product." This module is not "user friendly" and connecting them to a PC's parallel port is often difficult, frustrating, and can result in a "dead" display due to mishandling. For more information, see our forum thread at <http://www.crystalfontz.com/forum/showthread.php?s=&threadid=3257>.
- Your circuit should be designed to protect the module from ESD and power supply transients.
- Observe the operating temperature limitations: from -20°C minimum to +70°C maximum with minimal fluctuations. Operation outside of these limits may shorten the life and/or harm the display.
 - At lower temperatures of this range, response time is delayed.
 - At higher temperatures of this range, display becomes dark. (You may need to adjust the contrast.)
- Operate away from dust, moisture, and direct sunlight.

STORAGE AND RECYCLING



- Store in an ESD-approved container away from dust, moisture, and direct sunlight.
- Observe the storage temperature limitations: from -30°C minimum to +80°C maximum with minimal fluctuations. Rapid temperature changes can cause moisture to form, resulting in permanent damage.
- Do not allow weight to be placed on the modules while they are in storage.
- Please recycle your outdated CrystalFontz LCD modules at an approved facility.



APPENDIX A: QUALITY ASSURANCE STANDARDS

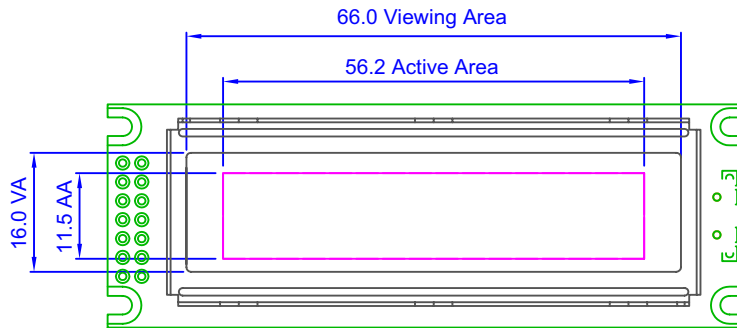
INSPECTION CONDITIONS

- Environment
 - Temperature: 25±5°C
 - Humidity: 30~85% RH (noncondensing)
- For visual inspection of active display area
 - Source lighting: two 20-Watt or one 40-Watt fluorescent light
 - Display adjusted for best contrast
 - Viewing distance: 30±5 cm (about 12 inches)
 - Viewing angle: inspect at 45° angle of vertical line right and left, top and bottom

COLOR DEFINITIONS

We try to describe the appearance of our LCD modules as accurately as possible. For the photos, we adjust the backlight (if any) and contrast for optimal appearance. Actual display appearance may vary due to (1) different operating conditions, (2) small variations of component tolerances, (3) inaccuracies of our camera, (4) color interpretation of the photos on your monitor, and/or (5) personal differences in the perception of color.

DEFINITION OF ACTIVE AREA AND VIEWING AREA



ACCEPTANCE SAMPLING

DEFECT TYPE	AQL*
Major	≤.65%
Minor	<1.0%
* Acceptable Quality Level: maximum allowable error rate or variation from standard	

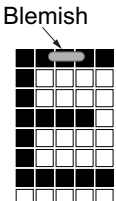
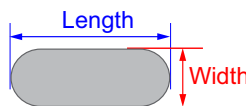
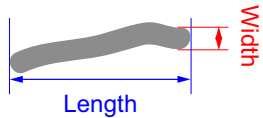


DEFECTS CLASSIFICATION

Defects are defined as:

- Major Defect: results in failure or substantially reduces usability of unit for its intended purpose
- Minor Defect: deviates from standards but is not likely to reduce usability for its intended purpose

ACCEPTANCE STANDARDS

#	DEFECT TYPE	CRITERIA			MAJOR / MINOR	
1	Electrical defects	1. No display, display malfunctions, or shorted segments. 2. Current consumption exceeds specifications.			Major	
2	Viewing area defect	Viewing area does not meet specifications.			Major	
3	Contrast adjustment defect	Contrast adjustment fails or malfunctions.			Major	
4	Blemishes or foreign matter on display segments		<i>Defect Size</i>	<i>Acceptable Qty</i>	Minor	
			≤0.30 mm	3		
		≤2 defects within 10 mm of each other				
5	Blemishes or foreign matter outside of display segments	Defect Size = (Width + Length)/2 	<i>Defect Size</i>	<i>Acceptable Qty</i>	Minor	
			≤0.15 mm	Ignore		
			0.15 to 0.20 mm	3		
			0.20 to 0.25 mm	2		
			> 0.30 mm	1		
6	Dark lines or scratches in display area		<i>Defect Width</i>	<i>Defect Length</i>	<i>Acceptable Qty</i>	Minor
			≤0.03 mm	≤3.0 mm	3	
			0.03 to 0.05	≤2.0 mm	2	
			0.05 to 0.08	≤2.0 mm	1	
			0.08 to 0.10	≤3.0 mm	0	
			≥0.10	>3.0 mm	0	



#	DEFECT TYPE	CRITERIA		MAJOR / MINOR
7	Bubbles between polarizer film and glass	<i>Defect Size</i>	<i>Acceptable Qty</i>	Minor
		≤0.20 mm	Ignore	
		0.20 to 0.40 mm	3	
		0.40 to 0.60 mm	2	
		≥0.60 mm	0	
8	Display pattern defect			Minor
		<i>Dot Size</i>	<i>Acceptable Qty</i>	
		$((A+B)/2) \leq 0.20$ mm	≤3 total defects ≤2 pinholes per digit	
		C > 0 mm		
		$((D+E)/2) \leq 0.25$ mm		
		$((F+G)/2) \leq 0.25$ mm		
9	Backlight defects	1. Light fails or flickers. (Major) 2. Color and luminance do not correspond to specifications. (Major) 3. Exceeds standards for display's blemishes, foreign matter, dark lines or scratches. (Minor)		See list ←
10	PCB defects	1. Oxidation or contamination on connectors.* 2. Wrong parts, missing parts, or parts not in specification.* 3. Jumpers set incorrectly. (Minor) 4. Solder (if any) on bezel, LED pad, zebra pad, or screw hole pad is not smooth. (Minor) *Minor if display functions correctly. Major if the display fails.		See list ←
11	Soldering defects	1. Unmelted solder paste. 2. Cold solder joints, missing solder connections, or oxidation.* 3. Solder bridges causing short circuits.* 4. Residue or solder balls. 5. Solder flux is black or brown. *Minor if display functions correctly. Major if the display fails.		Minor



APPENDIX B: APPLICATION NOTE FOR 3.3V OPERATION

This module can be used with a 3.3v power supply. In order to meet the requirements of V_{LCD} , you must provide a negative voltage source for V_O (pin 3, see [Details of Interface Pin Functions \(Pg. 11\)](#)). You need to drive V_O to below ground (typically -1v or -2v) until the V_{LCD} is met, making display contrast acceptable.

You can supply the negative voltage by one of the following methods:

1. Use an available source for the negative voltage.

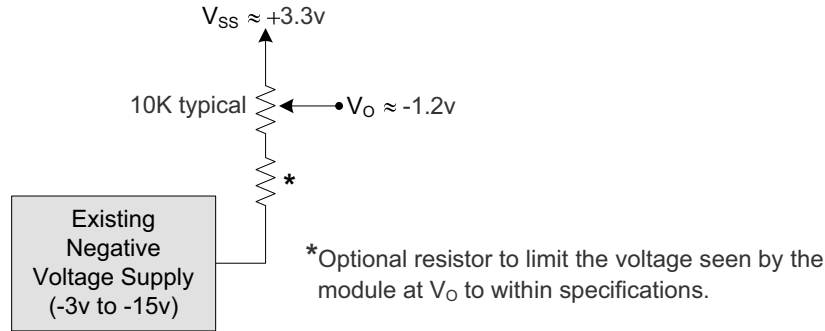


Figure 1. Use Existing Negative Voltage Supply

2. Use a “7660” CMOS switched-capacitor voltage converter or one of the many other available solutions for creating a negative voltage from a positive supply.

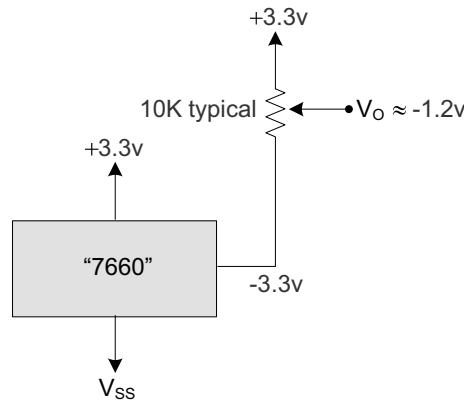


Figure 2. “7660” Switched-Capacitor Voltage Converter



3. Use the circuit in the figure below to create the voltage for V_O by using a PWM (Pulse Width Modulation) output of your microcontroller. This circuit allows the contrast to be adjusted under software control.

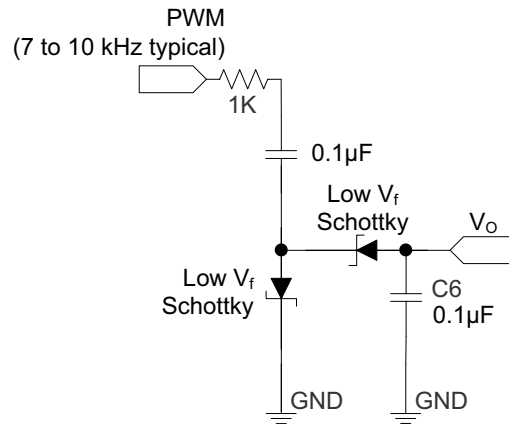


Figure 3. V_O Driving Circuit

Since V_O is pulled up internally by the LCD controller, this circuit will produce positive ($\approx +1\text{v}$) V_{LCD} ($V_{LCD} = \text{small}$, contrast is light) for low ($\approx 10\%$) or high (90%) duty cycles. For duty cycles near 50% , this circuit will produce negative ($\approx -2\text{v}$) levels of V_O ($V_{LCD} = \text{big}$, contrast is dark).

4. Replace this module with the module in this series that has an on-board negative voltage generator. (The part number has a "V" at the end of it.)

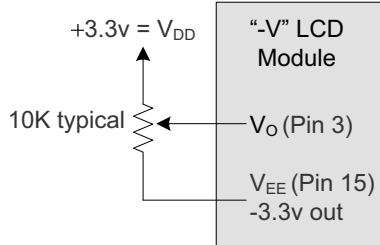


Figure 4. On-Board Negative Voltage Generator



APPENDIX C: SITRONIX ST7066U CONTROLLER SPECIFICATION SHEET

The complete *Sitronix ST7066U Dot Matrix LCD Controller/Driver* specifications (42 pages) follows.

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August 2008